

# **ROHM Solution Simulator**

# Automotive Low Offset Low Noise & Rail-to-Rail Input/Output CMOS Operational Amplifier LMR376YG-C – Voltage Follower (Pulse Input) – Transient Response simulation

This circuit simulates the transient response to pulse input with voltage follower configured Op-Amps. You can observe the fluctuation of the output voltage when the input voltage is abruptly changed. You can customize the parameters of the components shown in blue, such as VSOURCE, or peripheral components, and simulate the voltage follower with the desired operating condition.

You can simulate the circuit in the published application note: Operational amplifier, Comparator (Tutorial). [JP] [EN] [CN] [KR]

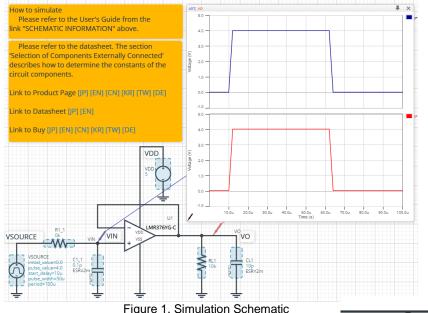
#### **General Cautions**

*Caution 1:* The values from the simulation results are not guaranteed. Please use these results as a guide for your design. *Caution 2:* These model characteristics are specifically at Ta=25°C. Thus, the simulation result with temperature variances may significantly differ from the result with the one done at actual application board (actual measurement).

*Caution 3:* Please refer to the Application note of Op-Amps for details of the technical information.

Caution 4: The characteristics may change depending on the actual board design and ROHM strongly recommend to double check those characteristics with actual board where the chips will be mounted on.

### **1** Simulation Schematic



#### 2 How to simulate

Тэ

The simulation settings, such as parameter sweep or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation.

In case of simulation convergence issue, you can change advanced options to solve. The temperature is set to 27 °C in the default statement in 'Manual Options'. You can modify it.

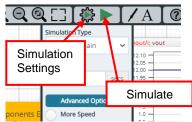


Figure 2. Simulation Settings and execution

able 1	Simulation	settings	default	setup

Parameters	Default	Note			
Simulation Type	Time-Domain	Do not change Simulation Type			
End Time	100 µs	-			
Advanced entions	Balanced	-			
Advanced options	Convergence Assist	-			
Manual Options	.temp 27	-			

# 3 Simulation Conditions

Table 2. List of the simulation condition parameters

Instance Type		Parameters	Default	Variable Range		Units
Name	туре	Farameters	Value	Min	Max	Units
	Voltage Source	Initial_value	0	0	5.5	V
		Pulse_value	4	0	5.5	V
		ramptime_initial_to_pulse	2	free		μs
VSOURCE		ramptime_pulse_to_initial	2	free		μs
		Start_delay	10	free		μs
		Pulse_width	50	free		μs
		Period	100		ee	μs
	Voltage Source For Op-Amp	Voltage_level	5	2.5 <sup>(Note1)</sup>	5.5 <sup>(Note1)</sup>	V
VDD		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fix	ed	0

(Note 1) Set it to the guaranteed operating range of the Op-Amps.

# 3.1 VSOURCE parameter setup

Figure 3 shows how the VSOURCE parameters correspond to the VIN stimulus waveform.

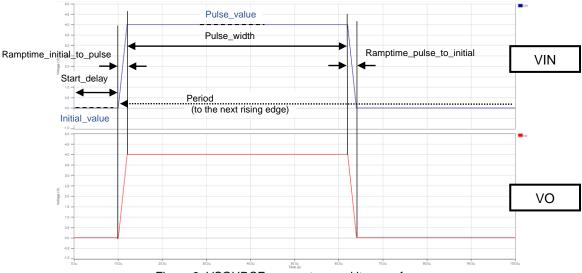


Figure 3. VSOURCE parameters and its waveform

# 4 Op-Amp model

Table 3 shows the model pin function implemented. Note that the Op-Amp model is the behavioral model for its input/output characteristics, and neither protection circuits nor functions unrelated to the purpose are implemented.

Pin Name	Description
+IN	Non-inverting input
-IN	Inverting input
VDD	Positive power supply
VSS	Negative power supply / Ground
OUT	Output

Table 3. Op-Amp model pins used for the simulation

# 5 Peripheral Components

#### 5.1 Bill of Material

Table 4 shows the list of components used in the simulation schematic. Each of the capacitors has the parameters of equivalent circuit shown below. The default values of equivalent components are set to zero except for the ESR of C. You can modify the values of each component.

Turna	Instance Name	Default Value	Variable Range		Linita	
Туре	Instance Marne	Delault value	Min	Max	Units	
Resistor	R1_1	0	0	10	kΩ	
Resision	RL1	10k	1k	1M, NC	Ω	
Consoitor	C1_1	0.1	0.1	22	pF	
Capacitor	CL1	10	free	, NC	pF	

Table 4. List of ca	pacitors used in	h tha	simulation	circuit
Table 4. LISCOLCA	pacitors used in	i uie	Simulation	CIICUIL

### 5.2 Capacitor Equivalent Circuits

Property Editor	-14	×
capacitor_model_v2		
Label CL1		0
CVALUE		
10p	F	Ø
ESR		
2m	Ohm	0
ESL		
0	Н	0
USE_INITIAL_VOL	TAGE	0
INITIAL_VOLTAGE	14	~
0.0	V	Q

(a) Property editor

(b) Equivalent circuit

Figure 4. Capacitor property editor and equivalent circuit

The default value of ESR is  $2m \Omega$ .

(Note 2) These parameters can take any positive value or zero in simulation but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine adequate value of parameters.

### 6 Recommended Products

6.1 Op-Amp

LMR376YG-C : Automotive Low Offset Low Noise & Rail-to-Rail I/O CMOS Op-Amp. [JP] [EN] [CN] [KR] [TW] [DE]

Technical Articles and Tools can be found in the Design Resources on the product web page.

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