

ROHM Solution Simulator

Automotive High Precision & Input/Output Rail-to-Rail CMOS Operational Amplifiers (Op-Amps)

TLR377YG-C - Non-inverting Amplifier (Sine Wave Input) - Transient Response sim

This circuit simulates the transient response to sine wave input with non-inverting amplifier configured Op-Amps. You can observe the output voltage and how faithfully the sine wave input voltage is reproduced. You can customize the parameters of the components shown in blue, such as VSOURCE, or peripheral components, and simulate the non-inverting amplifier with the desired operating condition.

You can simulate the circuit in the published application note: Operational amplifier, Comparator (Tutorial). [JP] [EN] [CN] [KR]

General Cautions

Caution 1: The values from the simulation results are not guaranteed. Please use these results as a guide for your design.

Caution 2: These model characteristics are specifically at Ta=25°C. Thus, the simulation result with temperature variances may significantly differ from the result with the one done at actual application board (actual measurement).

Caution 3: Please refer to the Application note of Op-Amps for details of the technical information.

Caution 4: The characteristics may change depending on the actual board design and ROHM strongly recommend to double check those characteristics with actual board where the chips will be mounted on.

Simulation Schematic 1

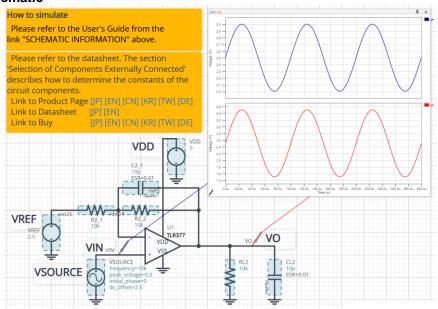


Figure 1. Simulation Schematic

2 How to simulate

The simulation settings, such as parameter sweep or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation.

In case of simulation convergence issue, you can change advanced options to solve. The temperature is set to 27 °C in the default statement in 'Manual Options'. You can modify it.

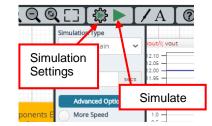


Figure 2. Simulation Settings and execution

Table 1. Simulation settings default setup

Parameters	Default	Note		
Simulation Type	Time-Domain	Do not change Simulation Type		
End Time	300 µs	-		
	Balanced	-		
Advanced options	Time Resolution Enhancement Convergence Assist	-		
Manual Options	.temp 27	-		

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3 **Simulation Conditions**

Table 2. List of the simulation condition parameters

Instance	Туре	Parameters	Default	Variable Range		Units
Name			Value	Min	Max	Utills
	Voltage Source	Frequency	10k	10	10M	Hz
		Peak_voltage	0.5	0	5.5	V
		Initial_phase	0	free		0
VSOURCE		DC_offset	2.5	0	5.5	V
		DF	0.0	fixed		1/s
		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		0
	Voltage Source For Op-Amp	Voltage_level	5	2.5 ^(Note1)	5.5 ^(Note1)	V
VDD		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		0
	Voltage Source	Voltage_level	2.5	VSS	VDD	V
VREF		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		0

(Note 1) Set it to the guaranteed operating range of the Op-Amps.

3.1 VSOURCE parameter setup

Figure 3 shows how the VSOURCE parameters correspond to the VIN stimulus waveform.

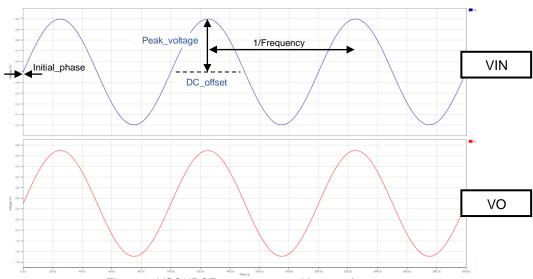


Figure 3. VSOURCE parameters and its waveform

Op-Amp model

Table 3 shows the model pin function implemented. Note that the Op-Amp model is the behavior model for its input/output characteristics, and no protection circuits or the functions not related to the purpose are not implemented.

Table 3. Op-Amp model pins used for the simulation

Pin Name	Description		
+IN	Non-inverting input		
-IN	Inverting input		
VDD	Positive power supply		
VSS	SS Negative power supply / Ground		
OUT	Output		

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Peripheral Components 5

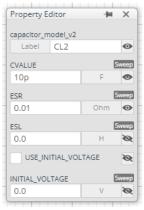
5.1 Bill of Material

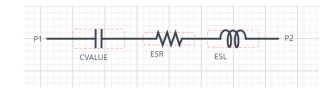
Table 4 shows the list of components used in the simulation schematic. Each of the capacitors has the parameters of equivalent circuit shown below. The default values of equivalent components are set to zero except for the ESR of C. You can modify the values of each component.

Table 4. List of capacitors used in the simulation circuit

Typo	Instance Name	Default Value	Variable Range		Units
Type			Min	Max	Utilis
	R2_1	10k	1k	1M	Ω
Resistor	R2_2	10k	1k	1M	Ω
	RL2	10k	1k	1M, NC	Ω
Conneitor	C2_1	10	10	100	pF
Capacitor	CL2	10	free	, NC	pF

5.2 Capacitor Equivalent Circuits





(a) Property editor

(b) Equivalent circuit

Figure 4. Capacitor property editor and equivalent circuit

The default value of ESR is 0.01 Ω .

(Note 2) These parameters can take any positive value or zero in simulation but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine adequate value of parameters.

6 **Recommended Products**

6.1 Op-Amp

TLR377YG-C: Automotive High Precision & Input/Output Rail-to-Rail CMOS Operational Amplifier. [JP] [EN] [CN] [KR] [TW] [DE]

TLR2377YFVM-C: Automotive High Precision & Input/Output Rail-to-Rail CMOS Operational Amplifier (Dual Op-Amp). [JP] [EN] [CN] [KR] [TW] [DE]

LMR1802G-LB: Low Noise, Low Input Offset Voltage CMOS Operational Amplifier. [JP] [EN] [CN] [KR] [TW] [DE]

Technical Articles and Tools can be found in the Design Resources on the product web page.

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