

ROHM Solution Simulator

Automotive High Precision & Input/Output Rail-to-Rail CMOS Operational Amplifiers (Quad Op-Amps) TLR4377YFV-C – Voltage Follower (Pulse Input) – Transient Response simulation

This circuit simulates the transient response to pulse input with voltage follower configured Op-Amps. You can observe the fluctuation of the output voltage when the input voltage is abruptly changed. You can customize the parameters of the components shown in blue, such as VSOURCE, or peripheral components, and simulate the voltage follower with the desired operating condition.

You can simulate the circuit in the published application note: Operational amplifier, Comparator (Tutorial). [JP] [EN] [CN] [KR]

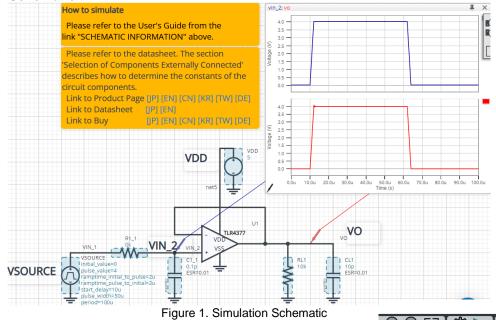
General Cautions

Caution 1: The values from the simulation results are not guaranteed. Please use these results as a guide for your design. Caution 2: These model characteristics are specifically at Ta=25°C. Thus, the simulation result with temperature variances may significantly differ from the result with the one done at actual application board (actual measurement).

Caution 3: Please refer to the Application note of Op-Amps for details of the technical information.

Caution 4: The characteristics may change depending on the actual board design and ROHM strongly recommend to double check those characteristics with actual board where the chips will be mounted on.

1 Simulation Schematic



2 How to simulate

The simulation settings, such as parameter sweep or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation.

In case of simulation convergence issue, you can change advanced options to solve. The temperature is set to 27 °C in the default statement in 'Manual Options'. You can modify it.



Table 1.	Simulation settings default setup	

Figure 2. Simulation	Settings and execution
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Parameters	Default	Note
Simulation Type	Time-Domain	Do not change Simulation Type
End Time	100 µs	-
	Balanced	-
Advanced options	Time Resolution Enhancement Convergence Assist	-
Manual Options	.temp 27	-

3 Simulation Conditions

Table 2. List of the simulation condition parameters

Instance	Tuno	Parameters	Default	Variable	e Range	Units
Name	Туре	Falameters	Value	Min	Max	Units
		Initial_value	0	0	5.5	V
		Pulse_value	4	0	5.5	V
		ramptime_initial_to_pulse	2	free		μs
VSOURCE	Voltage Source	ramptime_pulse_to_initial	2	free		μs
		Start_delay	10	free		μs
		Pulse_width	50	fre	ee	μs
		Period	100	free		μs
		Voltage_level	5	2.5 ^(Note1)	5.5 ^(Note1)	V
VDD	Voltage Source For Op-Amp	AC_magnitude	0.0	fixed		V
	For Op-Amp	AC_phase	0.0	fix	ed	0

(Note 1) Set it to the guaranteed operating range of the Op-Amps.

3.1 VSOURCE parameter setup

Figure 3 shows how the VSOURCE parameters correspond to the VIN_2 stimulus waveform.

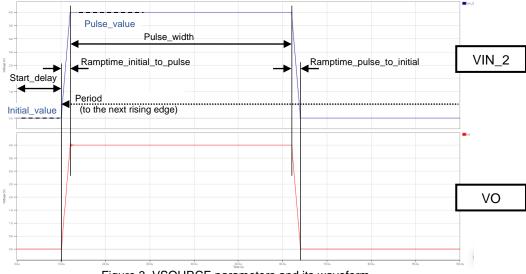


Figure 3. VSOURCE parameters and its waveform

4 Op-Amp model

Table 3 shows the model pin function implemented. Note that the Op-Amp model is the behavior model for its input/output characteristics, and no protection circuits or the functions not related to the purpose are not implemented.

Pin Name	Description	
+IN	Non-inverting input	
-IN	Inverting input	
VDD	Positive power supply	
VSS	Negative power supply / Ground	
OUT	Output	

Table 3. Op-Amp model pins used for the simulation

5 **Peripheral Components**

5.1 Bill of Material

Table 4 shows the list of components used in the simulation schematic. Each of the capacitors has the parameters of equivalent circuit shown below. The default values of equivalent components are set to zero except for the ESR of C. You can modify the values of each component.

Туре	Instance Name Default Value	Default \/alua	Variable Range		Units
		Min	Max	Units	
Resistor	R1_1	0	0	10	kΩ
Resision	RL1	10k	1k	1M, NC	Ω
Capacitor	C1_1	0.1	0.1	22	pF
	CL1	10	free	, NC	pF

Table 4. List of capacitors used in the simulation circuit

5.2 Capacitor Equivalent Circuits

Property Editor 🛛 🛏 🗙	
capacitor_model_v2	
Label CL1	
CVALUE Sweep	
10p F 👁	
ESR Sweep	
0.01 Ohm 👁 –	
ESL Sweep	
0.0 Н 🗞	
USE_INITIAL_VOLTAGE	
	CVALUE ESR ESL
INITIAL_VOLTAGE Sweep	
0.0 V 🗞	
(a) Property editor	(b) Equivalent circuit

Figure 4. Capacitor property editor and equivalent circuit

The default value of ESR is 0.01 Ω .

(Note 2) These parameters can take any positive value or zero in simulation but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine adequate value of parameters.

6 **Recommended Products**

6.1 Op-Amp

TLR4377YFV-C : Automotive High Precision & Input/Output Rail-to-Rail CMOS Operational Amplifier (Quad Op-Amp). [JP] [EN] [CN] [KR] [TW] [DE]

TLR2377YFVM-C : Automotive High Precision & Input/Output Rail-to-Rail CMOS Operational Amplifier (Dual Op-Amp). [JP] [EN] [CN] [KR] [TW] [DE]

TLR377YG-C : Automotive High Precision & Input/Output Rail-to-Rail CMOS Operational Amplifier. [JP] [EN] [CN] [KR] [TW] [DE]

LMR1802G-LB : Low Noise, Low Input Offset Voltage CMOS Operational Amplifier. [JP] [EN] [CN] [KR] [TW] [DE]

Technical Articles and Tools can be found in the Design Resources on the product web page.

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