

# Switching Regulator Series Isolated Flyback DC/DC Converter BD7J200EFJ-LB EVK BD7J200EFJ-EVK-002 (48V→12V, 1A)

User's Guide

# <High Voltage Safety Precautions>

Read all safety precautions before use

Please note that this document covers only the BD7J200EFJ-LB evaluation board (BD7J200EFJ-EVK-002) and its functions. For additional information, please refer to the datasheet.

# To ensure safe operation, please carefully read all precautions before handling the evaluation board



Depending on the configuration of the board and voltages used,

## Potentially lethal voltages may be generated.

Therefore, please make sure to read and observe all safety precautions described in the red box below.

#### **Before Use**

- [1] Verify that the parts/components are not damaged or missing (i.e. due to the drops).
- [2] Check that there are no conductive foreign objects on the board.
- [3] Be careful when performing soldering on the module and/or evaluation board to ensure that solder splash does not occur.
- [4] Check that there is no condensation or water droplets on the circuit board.

#### **During Use**

- [5] Be careful to not allow conductive objects to come into contact with the board.
- [6] Brief accidental contact or even bringing your hand close to the board may result in discharge and lead to severe injury or death.

Therefore, DO NOT touch the board with your bare hands or bring them too close to the board. In addition, as mentioned above please exercise extreme caution when using conductive tools such as tweezers and screwdrivers.

- [7] If used under conditions beyond its rated voltage, it may cause defects such as short-circuit or, depending on the circumstances, explosion or other permanent damages.
- [8] Be sure to wear insulated gloves when handling is required during operation.

#### **After Use**

- [9] The ROHM Evaluation Board contains the circuits which store the high voltage. Since it stores the charges even after the connected power circuits are cut, please discharge the electricity after using it, and please deal with it after confirming such electric discharge.
- [10] Protect against electric shocks by wearing insulated gloves when handling.

This evaluation board is intended for use only in research and development facilities and should by handled **only by qualified personnel familiar with all safety and operating procedures.** 

We recommend carrying out operation in a safe environment that includes the use of high voltage signage at all entrances, safety interlocks, and protective glasses.

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#### **Switching Regulator Series**

# Isolated Flyback DC/DC Converter **BD7J200EFJ-LB EVK**

BD7J200EFJ-EVK-002 (48V->12V, 1A)

BD7J200EFJ-EVK-002 delivers an output 12 volts from an input 48 volts using BD7J200EFJ-LB, Isolated Flyback DC/DC converter integrated circuit, with output current rating of maximum 1A.

#### **Performance specification**

These are representative values, and it is not a guaranteed against the characteristics.

V<sub>IN</sub> = 48V, V<sub>OUT</sub> = 12V, unless otherwise specified.

Parameter	Min	Тур	Max	Units	Conditions
Input Voltage		48.0		V	
Output Voltage		12.0		V	R4=2kΩ, R5=78.7kΩ
Output Current Range	100		1000	mA	Maximum Output Power:12W
Operating Frequency		400		kHz	
Maximum Efficiency		84		%	Io = 1000mA

#### **EVK**

PCB size: 70mm×50mm×1.6mm



Figure 1. BD7J200EFJ-EVK-002 Top View

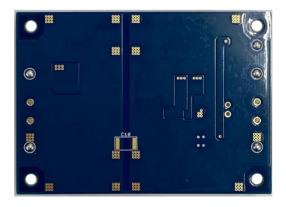


Figure 2. BD7J200EFJ-EVK-002 **Bottom View** 

#### **Operation Procedures**

- 1. Necessary equipments
  - (1) DC power-supply capable of supplying 48V / 1A
  - (2) Maximum 1A load
  - (3) DC voltmeter
- 2. Connecting the equipments(as shown in Figure 3)
  - (1) Preset DC power-supply to 48V and turn off.
  - (2) Set the load less than 1A and disable.
  - (3) Connect the power-supply to VIN and GND pins with a pair of wire.
  - (4) Connect the load to VOUT+ and VOUT- pins with a pair of wire.
  - (5) Connect the voltmeter1 to VIN and GND pins to measure input voltage.
  - (6) Connect the voltmeter2 to VOUT+ and VOUT- pins to measure output voltage.
  - (7) Turn on the power supply.
  - Check if the value of voltmeter2 is 12V.
  - (9) Enable the load.

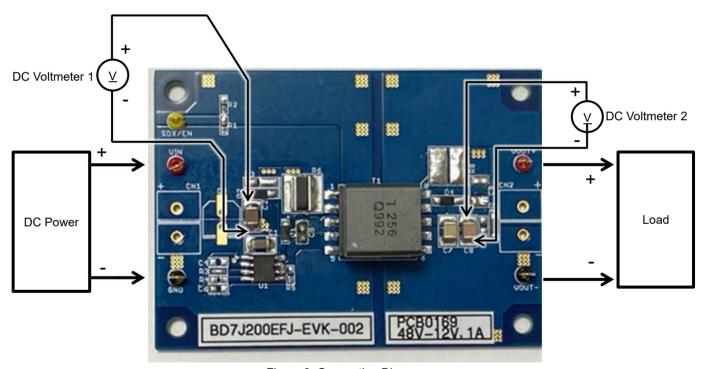


Figure 3. Connection Diagram

# **Circuit Diagram**

V<sub>IN</sub> = 48V, V<sub>OUT</sub> = 12V

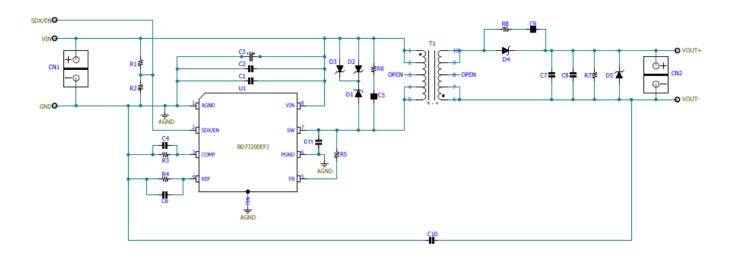


Figure 4. BD7J200EFJ-EVK-002 Circuit Diagram

#### **Bill of Materials**

Table 1. Bill of Materials

No.	Value	Description	Size	Part Number / Series	Manufactuer
C1	1µF	Capacitor, Chip, 100V, X7R	3216	GRM31CR72A105KA01	MURATA
C2	10μF	Capacitor, Chip, 100V, X7S	3225	GRM32EC72A106KE05	MURATA
C3	-	Notinstalled	-	-	-
C4	ı	Notinstalled	-	-	-
C5	100pF	Capacitor, Chip, 100V, COG	1608	GCM1885C2A101JA16	MURATA
C6	ı	Notinstalled	-	-	-
C7	22µF	Capacitor, Chip, 25V, X7R	3225	GRM32ER71E226KE15	MURATA
C8	47µF	Capacitor, Chip, 16V, X6S	3225	GRT32EC81C476KE13L	MURATA
C9	-	Notinstalled	-	-	-
C10	ı	Notinstalled	-	-	-
C11	100pF	Capacitor, Chip, 250V, COG	2012	GRM21A5C2E101JW01	MURATA
D1	1A/90V	Diode, Schottky	3516	RB160MM-90	ROHM
D2	Vz=36.0∼	Diode, Zener, Vz=36.0~40.0V	3516	KDZV36B	ROHM
D3	ı	Notinstalled	-	-	-
D4	3A/60V	Diode, Schottky	3516	RBR3MM60B	ROHM
D5	$Vz=13.3V\sim$	Diode, Zener, Vz=13.3∼15.0V	3516	KDZV13B	ROHM
R1	1ΜΩ	Resistor, Chip, 1/16W, 1%	1005	MCR01MZPF1004	ROHM
R2	68kΩ	Resistor, Chip, 1/16W, 1%	1005	MCR01MZPF6802	ROHM
R3	-	Short	-	-	-
R4	2kΩ	Resistor, Chip, 1/16W, 1%	1005	MCR01MZPF2001	ROHM
R5	78.7kΩ	Resistor, Chip, 1/16W, 1%	1005	MCR01MZPF7872	ROHM
R6	1kΩ	Resistor, Chip, 2W, 5%	3264	LTR100JZPJ102	ROHM
R7	-	Notinstalled	-	-	-
R8	-	Notinstalled	-	-	-
T1	70µH	Transformer, Np:Ns=5:2, ±20%	13.5×18.0×12.5mm	CEP1311F-4812121R	sumida
U1	BD7J200EFJ	I.C. BD7J200EFJ, HTSOP-J8	4.90×6.00×1.00mm	BD7J200EFJ	ROHM

# **Board Layout**

**EVK PCB information** 

Number of Layers	Material	Board Size	
4	FR-4	70mm x 50mm x 1.6mmt	

The following are layers of the BD7J200EFJ-EVK-002

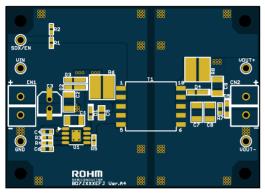


Figure 5. Top silk screen, layout (Top View)

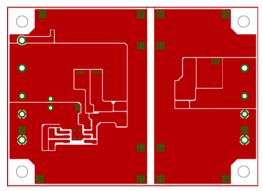


Figure 7. Top Layer layout (Top View)

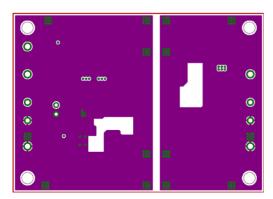


Figure 9. Middle Layer2 Layout (Top View)

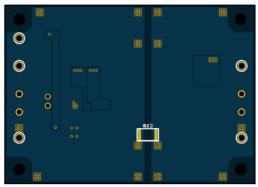


Figure 6. Bottom silk screen, layout (Top View)

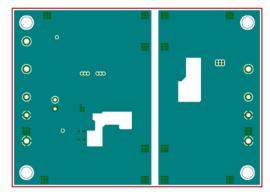


Figure 8. Middle Layer1 layout (Top View)

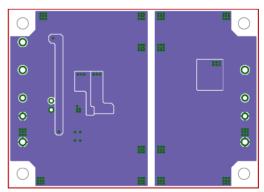


Figure 10. Bottom Layer Layout (Top View)

### **Reference Application Data**

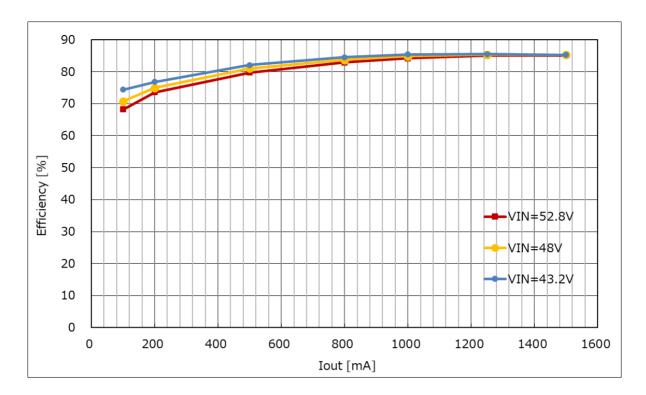


Figure 11. Power Conversion Efficiency vs Output Current

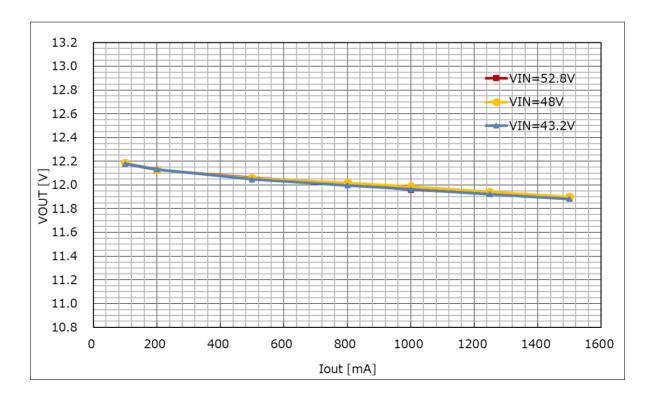


Figure 12. Load Regulation

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# C11 mounting method

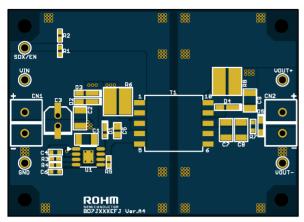


Figure 13. Top land pattern (Top View)

\*\*There is no land pattern for C11.



Figure 14. C11 mounting position

C11 mounting position

C11 is mounted on the lead terminals of U1\_6pin (PGND) and U1\_7pin (SW).

# **Revision History**

Date	Revision Number	Description
9. Sep. 2021	001	Initial release

#### Notes

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