ROHM Solution Simulator

2.7 V to 5.5 V Input, 3 A Integrated MOSFET Single Synchronous Buck DC/DC Converter

BD9A300MUV / Frequency Response

This circuit simulates the frequency response of BD9A300MUV. You can customize the simulation conditions by changing the parameters of components highlighted in blue.

General Cautions

Caution 1: The values from the simulation results are not guaranteed. Please use these results as a guide for your design.
Caution 2: These model characteristics are specifically at Ta=25°C. Thus, the simulation result with temperature variances may significantly differ from the result with the one done at actual application board (actual measurement).
Caution 3: Please refer to the datasheet for details of the technical information.
Caution 4: The characteristics may change depending on the actual board design and ROHM strongly recommend to double check those characteristics with actual board where the chips will be mounted on.

1 Simulation Schematic

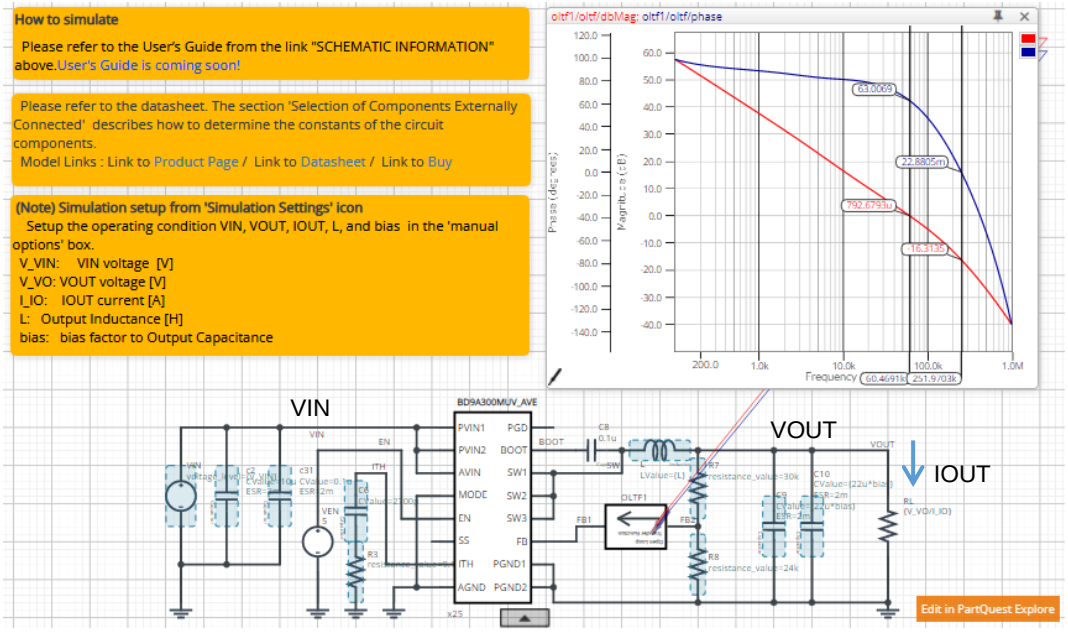


Figure 1. Simulation Circuit

2 How to simulate

The simulation settings, such as frequency range or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation.

In case of simulation convergence issue, you can change advanced options to solve.

The parameters V_VIN, V_VO, I_IO, L, and bias are defined in the 'Manual Options'.

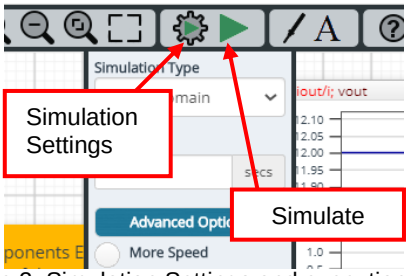


Figure 2. Simulation Settings and execution

Table 1. Simulation settings default setup

Parameters	Default	Note
Simulation Type	Frequency-Domain	(Do not change Simulation Type)
Start Frequency	100 Hz	Simulate the frequency response for the frequency range from 100 Hz to 1 MHz.
End Frequency	1.0e6 Hz	
Advanced options	Balanced	
Manual Options	".param V_VIN=5 V_VO=1.8 I_IO=1 L=1.5u bias=0.79"	*See "Simulation Condition" for details*

3 Simulation Conditions

3.1 How to define V_{IN}, V_{OUT}, I_{OUT}, L, and bias

These parameters are used to setup the simulation conditions and BD9A300MUV_Average model parameters, therefore these are defined in the Manual Options as the common variables.

Table 2 shows the default value of V_{IN}, V_{OUT}, I_{OUT}, L, and bias. Those values are defined and can be set in the 'Manual Options' text box from Simulation Settings as shown in Figure 3.

The output voltage of VIN and the load resistance RL are automatically set according to those parameters.

Table 2. Simulation Conditions

Parameters	Variable Name	Default Value	Units	Descriptions
V_{IN}	V_VIN	5	V	Input Voltage
V_{OUT}	V_VO	1.8	V	Output Voltage
I_{OUT}	I_IO	1	A	Output Current
L	L	1.5	µH	Output Inductor
bias factor	bias	0.79	-	Bias factor to Output Capacitance

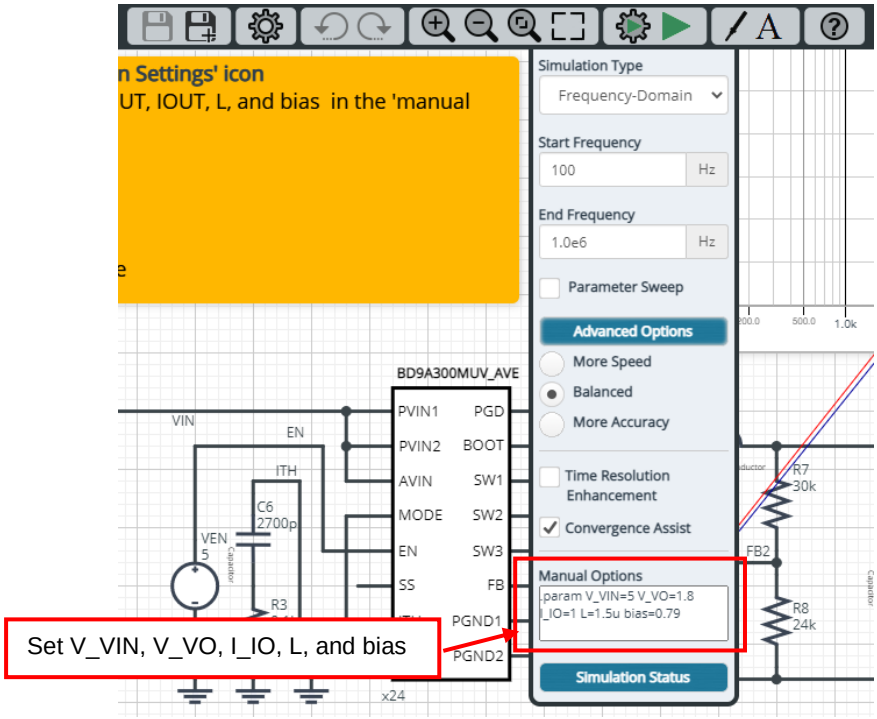


Figure 3. Definition of V_{IN}, V_{OUT}, I_{OUT}, L, and bias

3.2 Resistive Load RL

RL is the resistive load and its resistance is determined from V_{OUT} and I_{OUT}. The resistance value is defined as the equation below.

Table 3. Resistive load

Instance Name	Default Value	Unit
RL	{ V_VO / I_IO }	Ω

4 BD9A300MUV_Average model

The simulation model in this circuit is designed for frequency response, and the functions not related to frequency response are not implemented.

Table 4. BD9A300MUV_Average model terminals used for frequency response

Terminals	Description
PVIN, AVIN	Power supply input
EN	Enable input
ITH	Input terminal for the gm error amplifier output and the output switch current comparator
FB	Output voltage feedback pin. Inverting input node for the gm error amplifier.
SW	Switching node
AGND, PGND	Ground

Table 5. BD9A300MUV_Average model terminals NOT used for frequency response

Terminals	Description
MODE	Input is ignored (no changes to mode in this model)
SS	Not available (this model can only be used for AC analysis)
BOOT	Input is ignored (no switching operation in this model)
PGD	Not available (this model can only be used for AC analysis)

(Note 1) This model is not compatible with the influence of ambient temperature.
(Note 2) This model is not compatible with the external synchronization function.
(Note 3) Use the simulation results only as a design guide and the data reported herein is not a guaranteed value.

4.1 BD9A300MUV_Average Model Parameters

BD9A300MUV_Average model has its parameters shown in Table 6. All the parameters are pre-defined and fixed in the simulation. V_VIN is substituted to V_VIN as shown in Table 6.

Table 6. Parameter List

Parameters	Values	Description
V_VIN	V_VIN	VIN voltage
V_VO	V_VO	VOUT voltage
L	L	Output Inductor

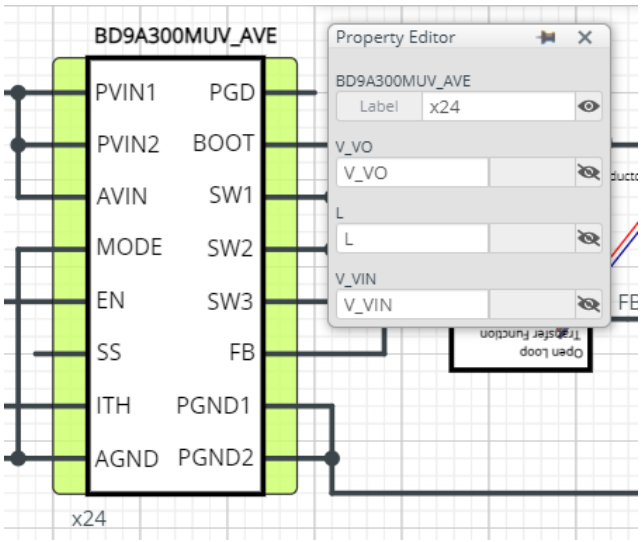


Figure 4. Property Editor of BD9A300MUV_Average model

5 Peripheral Components

To set parameters of components, open 'property' by double click or right click on a component. You can input a value to a property text box if available. Please refer to the hands-on manual for more details.

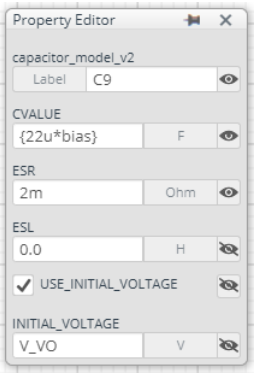
5.1 Bill of Material

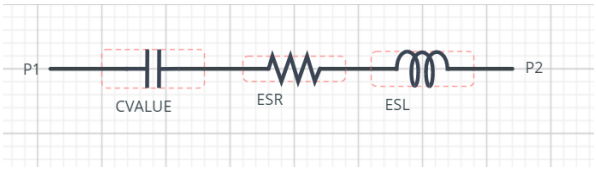
Table 7 shows the list of components used in the simulation schematic. Each of the capacitor and inductor has the parameters of equivalent circuit shown below. The default value of equivalent components are set to zero except for the parallel resistance of L and series resistance of capacitors. You can modify the values of each component.

Table 7. List of components used in the simulation circuit

Type	Instance Name	Default Value	Units
Capacitor	C8	0.1	µF
	C6	2700	pF
	C9, C10	22	µF
Inductor	L	1.5	µH
Resistor	R7	30	kΩ
	R8	24	kΩ
	R3	9.1	kΩ

5.2 Capacitor Equivalent Circuits

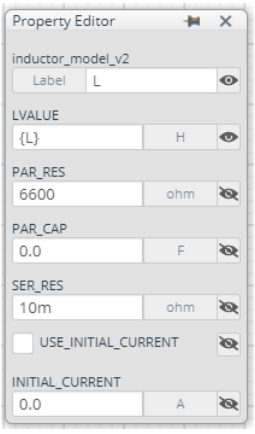


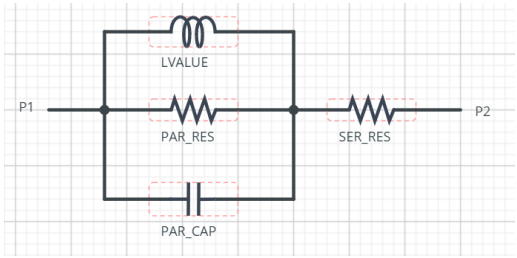


(a) Property editor (b) Equivalent circuit

Figure 5. Capacitor property editor and equivalent circuit

5.3 Inductor Equivalent Circuits





(a) Property editor (b) Equivalent circuit

Figure 6. Inductor property editor and equivalent circuit

The default value of PAR_RES is 6.6kohm.

(Note 5) These parameters can take any positive value or zero in simulation but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine adequate value of parameters.

6 Open Loop Transfer Function (OLTF) Monitor

OLTF1 is the insert model to measure AC open loop transfer function and is inserted to acquire the gain and phase output. To monitor the gain and phase from OLTF1, select probe items 'dbMag' for gain and 'phase' for phase plot, respectively from 'property' of OLTF1.

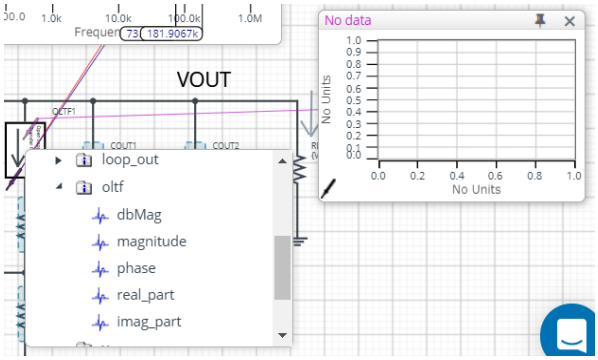


Figure 7. Probe Items of OLTF1

7 Link to the product information and tools

7.1 Product webpage link:
https://www.rohm.com/products/power-management/switching-regulators/integrated-fet/buck-converters-synchronous/bd9a300muv-product

7.2 Related documents
The application notes are available from 'Documents in Design Resources' tab of the product page.

7.3 Design assist tools are available from 'Tools in Design Resources' tab of the product page.
The Circuit constant calculation sheet is useful for deciding the application circuit constants.

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