Automotive LED Drivers Selection Guide



Ver.2.3

Creating the future of Automobiles

Automotive

ROHM Co., Ltd.

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*Before using the product(s) please verify the numerical values, data, and functions listed in the latest datasheet.

Pickup Application Configuration Examples



Pickup LED Driver Lineup for Automotive Lamps

LED channel & number	20mA	130mA	500mA	800mA	1A	2 to 6A
Long chain 6 to 15 LEDs	BD18351 ☆BD18353	EFV-M(Boost, Buck EFV-M/MUF-M(Be	-Boost) post, Buck-Boost)			
Multi channels 3 to 6 LEDs	☆BD18312 Front light	MUF-M(1ch Boost-	2ch Buck) ☆ BD183	91EFV-M	☆BD18395	☆BD18394
Multi channels 1 to 3 LEDs	BD1834x ☆BD18336i ☆BD18347i RCL/DRL/Tu	series(Controller) NUF-M(Small PKG. EFV-M/BD18337 rn/CL/Fog/High Ic	1ch I Integrated /1ch) EFV-M(FET/4ch) ow beam	Suck FET(1A)	EFV-M 1ch Buck Integrated FET(2A)	EFV-M/MUF-M 1ch Buck Controller Front light (LED/Laser)
One channel 1 to 3 LEDs	BD837x ser (Integrated FE RCL/DRL/Tu	ies T/up to 500mA) rn/CL/Fog				
Buck LED D	river	Boost, Buck-B	oost LED Driver	Linear	LED Driver	☆: Under Developmen

Automotive LED Drivers Selection Guide 02

BD18394EFV-M/BD18394MUF-M

Key Features

- Synchronous rectification buck LED drivers
- Hysteresis function
- Operating voltage range: 5.0V to 70V
- LED voltage range: 2.0V to 60V
- Switching frequency: 100kHz to 500kHz
- Fixed switching frequency control
- Spread spectrum function
- High-side LED current detection function
- PWM/DC dimming function
- Supports matrix drive
- LED open/short detection function
- UVLO, TSD, OCP functions

Block Diagram



Target Applications

- High/Low Beam
- Tail-Stop Lamp, DRL
- Position/Turn Lamp

Package



 HTSSOP-B16

 W(Typ)
 ×
 D(Typ)
 ×
 H(Max)

 5.00mm
 6.40mm
 1.00mm



 VQFN24FV4040

 W(Typ)
 ×
 D(Typ)
 ×
 H(Max)

 4.00mm
 4.00mm
 1.00mm

Advantages of EMC Countermeasures (Fixed Switching Frequency Control)

- Fixed switching frequency control addresses automotive EMC standards such as CISPR25 by focusing on switching frequency fluctuations that are issues of OFF time control.
- In addition, combining with a spread spectrum function ensures sufficient margin for automotive EMC standards.
- Hysteresis control provides faster response when changing the number of LEDs compared with current mode.





Hysteresis control ensures good response.

BD18391EFV-M/BD18395EFV-M

Key Features



Tail-Stop Lamp, DRL

Position/Turn Lamp



Block Diagram





HTSSOP-B20 W(Typ) × D(Typ) × H(Max)

6.50mm × 6.40mm × 1.00mm

OFF Time Control

 LED drivers that light up multiple LEDs through matrix control, such as ADB and dynamic indicators, require fast response, reduced LED rush current, and minimal fluctuation in the average current when changing the number of LEDs.
 OFF time control keeps the LED current ripple constant by detecting LED peak current with comparator and fixed OFF time control of buck converter.

The above requirements are realized because no phase compensation is necessary and output CAP can be minimized.

Sample Waveforms When Changing the Number of LEDs(BD18391EFV-M)



New Pr<u>oducts</u>

Boost LED Driver

BD18351EFV-M

Key Features

- 1ch boost LED driver
- Operating voltage range: 4.5 to 65V
- Boost voltage range: 6.0 to 65V
- High accuracy power supply: 2.5V±3%
- LED current accuracy: ±3%(-40°C to +125°C)
- Switching frequency: 200kHz to 700kHz
- PWM/DC dimming function
- Built-in PWM generator
- High-side LED current detection function
- Spread spectrum function
- LED open/short protection function
- UVLO, TSD, OCP functions
- Self-diagnostic function(FAIL)

Target Applications

- High/Low Beam
- Tail-Stop Lamp, DRL
- Position/Turn Lamp

Package



 HTSSOP-B24

 W(Typ)
 ×
 D(Typ)
 ×
 H(Max)

 7.80mm
 7.60mm
 1.00mm

Spread Spectrum Function

- Spread spectrum(spread spectrum clock generator) is a function that mitigates noise concentration at a certain frequency(switching frequency and its harmonics) by intentionally modulating the switching frequency. It is effective in reducing the peak noise.
- The EMC level required for automotive applications is expected to become more stringent in the future following the continued electrification of vehicles and adoption of ADAS/Autonomous driving, but adding a spread spectrum function provides a better margin against automotive EMI standards such as CISPR25. This is why it is possible to decrease the size of the input filter block by optimization of filter components.

CISPR25/Conduction Emission Test Comparison

 Comparison of noise measurement result with π type filter constants required to meet CISPR25/Class 5 (8 white LED in series, ILED=300mA)





The spread spectrum function makes it possible to reduce the input filter constants to comply with the CISPR25 standard

Block Diagram



Boost LED Drivers

BD18353EFV-M/BD18353MUF-M

Key Features

- 1ch boost LED driver
- Operating voltage range: 5.0V to 65V
- Boost voltage range: up to 60V
- LED current accuracy: ±3%(-40°C to +125°C)
- Switching frequency: 100kHz to 2.2MHz(T.B.D)
- PWM/DC dimming function
- **2**-system DC dimming function
- Built-in PWM generator(200Hz)
- High-side LED current detection function
- Integrated Pch MOSFET driver for PWM dimming
- Spread spectrum function
- Hiccup timer
- LED open/short protection function
- UVLO, UVD, OVP, OCP, TSD functions
- Diagnostic function(FAULT_B)

Block Diagram



Target Applications

- High/Low Beam
- Tail-Stop Lamp, DRL
- Position/Turn Lamp







 HTSSOP-B20

 W(Typ)
 ×
 D(Typ)
 ×
 H(Max)

 6.50mm
 ×
 6.40mm
 1.00mm

 VQFN20FV3535

 W(Typ)
 ×
 D(Typ)
 ×
 H(Max)

 3.50mm
 3.50mm
 1.00mm

2-System DC Dimming Function

• The BD18353EFV-M and BD18353MUF-M each have 2 DC dimming terminals(DCDIM1 and DCDIM2)

Configuration Example	DCDIM1	DCDIM2
1	BIN resistor	Thermal derating(NTC)
2	Analog input from MCU	Thermal derating(NTC)
3	Analog input from resistor dividing	Thermal derating(NTC)
4	MCU control	Unused





BD18312MUF-M

Key Features

- Ich boost power supply + 2ch buck LED driver Boost block: Current mode DC/DC controller Buck block: Buck LED driver featuring OFF time control
- Operating voltage range: 5.5V to 50V
- LED voltage range: 2.5V to 62V
- LED settable current range: Depends on external FET
- Switching frequency: 63kHz to 500kHz(Boost) : 25kHz to 1MHz(Buck)
- Standby current: 0µA(Typ)
- PWM/DC dimming function
- Supports matrix drive(output capacitance≤10nF)
- High-side LED current detection function
- Limp home function
- Peak current correction function
- LED open/short protection function
- UVLO, TSD, OCP functions

Target Applications

- ADB
- High/Low Beam
- Tail-Stop Lamp, DRL
- Position/Turn Lamp

Limp Home Function

When communication between the LED driver IC and MCU is interrupted, the BD18312MUF-M detects an error via SPI and enters Limp Home mode.

Limp-home mode first reads the BIN(Binary) resistors which is external using the A/D converter. The boost output voltage and frequency can be set by these external BIN resistors, and BD18312MUF-M maintains LED lighting operation on these settings.



Block Diagram



Package



VQFN40FV6060 W(Typ) × D(Typ) × H(Max) 6.00mm × 6.00mm × 1.00mm

Peak Current Correction Function

During ADB operation, the LED voltage fluctuates dynamically, causing variations in the LED current. The BD18312MUF-M can correct these LED current fluctuations using a **peak current correction function**.



BD18362EFV-M



Set the LED lighting interval using external parts

Dynamic Indicator Solution Without an MCU

Sequential control is easily achieved by combining ROHM's buck-boost LED driver(BD18351EFV-M) with a matrix LED controller.



is 9V/ch

BD18337EFV-M/BD18347EFV-M

Key Features

- 4ch linear LED drivers with built-in FET
- Operating voltage range: 5.5V to 20V
- Max output current: 150mA/ch(Total: 500mA)
- Output current accuracy: ±5%(Ta=-40°C to +125°C)
- Power Shift function
- License lamp mode
- LED open detection mask function BD18337: 11.0V(Typ) BD18347: 7.65V(Typ)
- LED open/output short detection function
- Overvoltage mute function: V_{IN}>20V(Min)
- PBUS function
- TSD/UVLO
- UVLO, TSD functions



HTSSOP-B16

W(Typ) × D(Typ) × H(Max)

5.00mm × 6.40mm × 1.00mm

Target Applications

- Rear Lamps
- Position/DRL
- Fog
- Turn

Individual PWM Dimming

• With BD18337EFV-M and BD18347EFV-M, individual PWM dimming can be done by connecting an Nch MOSFET to each channel. The delay time for the PWM signal when using external MOSFETs is shown in the figure of Measurement Conditions at the lower right.



Measurement Results



Combining with an MCU supports applications such as dynamic Indicators

Power Shift Function

• Power shift function simplifies thermal design of LED driver board by adding external resister.

With conventional linear LED drivers, thermal design is difficult in high power applications due to the constraints for permissible package power loss.

In contrast, BD18337EFV-M and BD18347EFV-M dissipates the power ,which is consumed internally with conventional linear LED driver, externally with resistor(R_{EXT}) added between V_{IN} and VINRES. Thus IC temperature increase is minimized. The voltage between VINRES pin and OUT1-4 pins are controlled to be less than 2.0V.



Adding a resistor simplifies thermal design and contributes to lower total cost by reducing the number of LED drivers

Actual Effects



New Produc<u>ts</u>

Linear LED Controllers

BD18340FV-M/BD18341FV-M/BD18342FV-M BD18343FV-M/*New*/BD18345EFV-M

Key Features

- Linear LED controllers
- Operating voltage range: 4.5V to19V
- Reference voltage(5V) output: ±3%(Ta=25°C to 125°C)
- FB terminal voltage accuracy: ±3%(Ta=25°C to 125°C)
- Standby current: 0µA(Typ)
- PWM dimming function
 DC dimming function (BD18340, 18341, 18345 only)
- PWMOUT synchronization signal output function (BD18340, 18341, 18345 only)
- **LED** open detection mask function(Variable voltage)
- LED open/output short detection function
- Overvoltage mute function: 20V(Min)
- PBUS function
- UVLO, TSD functions

Target Applications

- Rear Lamps
- Position/DRL
- Fog
- Turn



Package





 SSOP-B16(Other than BD18345)

 W(Typ)
 ×
 D(Typ)
 ×
 H(Max)

 5.00mm
 6.40mm
 ×
 1.00mm

 HTSSOP-B20(BD18345)

 W(Typ)
 ×
 D(Typ)
 ×
 H(Max)

 6.50mm
 6.40mm
 ×
 1.00mm

Multiple Built-In Functions



Block Diagram

BD18345EFV-M

BD18345EFV-M with Enhanced DC Dimming Function

- The number of DC dimming functions was expanded from 1 system in conventional models (BD18340/BD18341) to 2 systems in the BD18345
- **Achieves both** LED coding and temperature derating functionality
- DC dimming accuracy improved to ±3%(DCDIM pin)

Control Board ASF VREF **LED Board** THD Dim"' 1 \$₹ NTC 7% Therm DCDIM DC Dimming I -15 GND

Eliminates the needs to change resistor on the control board for different luminous flux ranks

DCDIM pin for DC dimming

Select a BIN resistor on the LED board corresponding to the LED luminous flux rank. The voltage to DCDIM will vary depending on BIN resistor so that voltage of FB pin will be adjusted automatically.

Ensuring high reliability by reducing LED degradation.

THD Pin For Temperature Derating

Connect an NTC thermistor. (The voltage of THD pin is resistor divided voltage from V_{REG}) Following a rise in temperature of the LED board, the resistance value of the thermistor decreases along with the THD pin voltage. The output current can be derated based on the THD pin voltage.

Reference Boards

3-System







Ultra-Compact Linear LED Driver

BD18336NUF-M

Key Features

- Linear LED driver with built-in FET
- Operating voltage range: 5.5V to 20V
- Max output current: 400mA(DC), 600mA(50% duty)
- Output current accuracy: ±5%(240mA to 600mA) Tj=-40°C to +150°C
- LED current bypass control during input voltage drops
- **PWM dimming function**: 1% to 100%(@200Hz)
- Output current derating function
- LED open detection mask function: 11V(Typ)
- LED open/output short detection
- ISET pin short protection
- Overvoltage mute function: 16V(Min)
- PBUS function
- UVLO, TSD functions

Target Applications

- Socket LED
- Rear Lamps
- Position/DRL
- Fog
- Turn





Block Diagram



Package



 VSON10FV3030

 W(Typ)
 ×
 D(Typ)
 ×
 H(Max)

 3.00mm
 3.00mm
 ×
 1.00mm

Current Bypass Function During Input Voltage Drops

The BD18336NUF-M can maintain lighting by dimming LED1 at the lowest stage even in the V_F of LED3 (approx. 9V) is greater than V_{IN} due to the LED current bypass function when the input voltage drops.





Introducing a current bypass function makes it possible to light

Linear LED Drivers with Built-in FET

BD83732HFP-M/BD83733HFP-M



BD8374HFP-M/BD8374EFJ-M

Key Features

- Linear LED driver with built-in FET
- Operating voltage range: 4.5V to 42V
- Max output current: 500mA
- Output current accuracy: ±3%(Ta=25°C)

PWM dimming function

- (PWM dimming range: 0.4% to 100%@200Hz)
- LED open/short detection function
- Overvoltage mute function: 27V(Min)

PBUS function

TSD protection function



Target Applications

- Rear LampsPosition/DRL
- Fog

Turn

Package

Block Diagram





HTSOP-J8 W(Typ) × D(Typ) × H(Max) 4.90mm × 6.00mm × 1.00mm

9.3

BD1834x-M series/BD837x-M series

BD1834x-M series

		Channel	Operating	Absolute	Drivo	Dimme	r mode		
	Part Number	Number (ch)	Range (V)	Rating (V)	Current	PWM CRT	DC	OUT	Package
	BD18340FV-M	1 to 10	4.5 to 19	70		\checkmark			SSOP-B16
	BD18341FV-M	1 to 10	4.5 to 19	70		\checkmark	±13%	~	SSOP-B16
	BD18342FV-M	1 to 10	4.5 to 19	70	Depend on External Transistor	\checkmark	_	_	SSOP-B16
	BD18343FV-M	1 to 10	4.5 to 19	70	(10tal. 100011A)	Only External Input	_	_	SSOP-B16
٨	BD18345EFV-M	1	4.5 to 19	70		\checkmark	2port ±3%	\checkmark	HTSSOP-B20
	☆BD18336NUF-M	1	5.5 to 20	42	400mA(DC) 600mA(50% Duty)	\checkmark	\checkmark	_	VSON10FV3030
^ ^	ew/BD18347EFV-M/ W/BD18337EFV-M	4	5.5 to 20	40	150mA/ch (Total: 450mA)	\checkmark	✓ –		HTSSOP-B16

☆: Under Development

BD837x-M series

	channel	Operating	Absolute	Drive	Dimme	er mode		
Part Number	Number (ch)	Range (V)	Rating (V)	Current (mA)	PWM CRT	DC	OUT	Package
BD8372HFP-M/ BD8372EFJ-M	1	5.5 to 40	50	200	_	High Current Low Current	_	HRP7/ HTSOP-J8
BD8374HFP-M/ BD8374EFJ-M	1	4.5 to 42	50	500	\checkmark	-	_	HRP7/ HTSOP-J8
BD83732HFP-M/ BD83733HFP-M	1	4.5 to 42	50	500	\checkmark	\checkmark	_	HRP7

Pickup / PBUS Function

BD1834x-M series/BD837x-M series

Patent Registered Japan : JP 5636241 US : US8754592 China : CN102431486A

PBUS Function

Basic Patent: PBUS Function

The PBUS(Protect BUS) function automatically determines IC master or slave when detecting LED disconnection or IC output ground fault in multi-channel LED drive circuist that drive multiple LEDs and turns OFF all channels at once(all off). Each model in the BD1834x-M and BD837x-M series is equipped with this function as standard.



BD1834x-M series Circuit Example

IPD(Intelligent Power Device)

Key Features

ROHM offers an enhanced lineup of IPD integrating MOSFET protection functions in 1ch/2ch/8ch series



(Automotive Lamp Application Example)

- 1 Replaces mechanical relays and fuses
- ② Replaces FETs used for high/low beam switching and lighting control
- Replaces FETs adopted for resistive rear lamp switching

Lineup																								
HSON8		MSOP8	HSON-A8	S	OP-J8	HTS	OP-J8	1	TO2 5	52-J3	;	ŀ	ISSO	OP-C	:16		HTS	SOF	P-B24	1	S	SOP	-A24	1
2.9mm×3.0mm×0.	6mm	2.9mm×4.0mm×0.9mm 3.5m	m×4.65mm×1.0mm	5 4.9mm×	6.0mm×1.0mm 4.	9mm×6.0	Imm×1.0mm	6.6mm	×10.1	mm×2	2.3mm	4.9m	m×6.	Omm >	3	n 7.	8mm>	<7.6mr	x n×1.0	mm	10.0mr	17757575 17757575 17×7.81	mm×.) 1.8m
									Fund	ctions		Err	or Fla	a Out	put			Pac	kage(Packa	age Co	ode) i		
	No. of Ch	Part No.	Voltage Range (V)	VDS (Max) (V)	Overcurrent Detection (Min) (A)	ON Resistance (Typ) (mΩ)	TSD	Error Flag Output	Current Sense	Variable Overcurrent	Overcurrent Mask Time Setting	Overheat Detection	Overcurrent Detection	Load Open Detection	Load Open Detection	HSON8(HFN)	MSOP8(FVM)	HSON-A8(HFS)	SOP-J8(FJ)	HTSOP-J8(EFJ)	TO252-J3(FPJ)	HSSOP-C16(EFU)	HTSSOP-B24(EFV)	SSOP-A24(FS)
	1	☆BV1HF045EFJ-C	6.0 to 28.0	45	21	45	Self-restart	\checkmark	-	-	-	\checkmark	\checkmark	\checkmark	\checkmark	—	-	-	-	\checkmark	—	-	—	-
	1	☆BV1HJ045EFJ-C	6.0 to 28.0	45	5	45	Self-restart	\checkmark	-	-	-	\checkmark	\checkmark	\checkmark	\checkmark	-	—	-	—	\checkmark	-	—	—	-
	1	BV1HD090FJ-C	4.5 to 36.0	45	2.7	90	Self-restart	\checkmark	—	-	-	$\overline{}$	✓*2	\checkmark	—	—	—	-	\checkmark	—	—	—	—	-
	1	☆BV1HN090FJ-C	4.5 to 36.0	45	4	90	Self-restart	\checkmark	—	-	—	\checkmark	\checkmark	\checkmark	—	—	—	—	\checkmark	—	—	—	—	-
	1	BD1HC500 C*1	4.0 to 18.0	44.5	0.8	500	Off-latch	\checkmark	—	-	-	\checkmark	✓*2	\checkmark	—	\checkmark	\checkmark	-	—	\checkmark	—	—	—	-
High-Side	1	BD1HD500 C*1	4.0 to 18.0	44.5	0.8	500	Self-restart	\checkmark	—	-	-	\checkmark	✓*2	\checkmark	—	\checkmark	\checkmark	-	—	\checkmark	—	—	—	-
Switches	1	☆BV1HB045EFJ-C	6.0 to 28.0	45	21	45	Self-restart	\checkmark	\checkmark	-	-	\checkmark	\checkmark	\checkmark	—	-	—	-	—	\checkmark	—	—	—	-
Gwitches	2	New BV2HC045EFU-C	6.0 to 19.0	41	Fix(21A)& Adj(1 to 10A)	45	Off-latch	\checkmark	-	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	—	-	-	-	-	-	-	\checkmark	-	-
	2	New BV2HD045EFU-C	6.0 to 28.0	41	Fix(21A)& Adj(1 to 10A)	45	Self-restart	\checkmark	_	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	_	-	_	-	-	-	-	\checkmark	-	-
	2	☆BV2HD070EFU-C	6.0 to 28.0	41	Fix(21A)& Adj(0.5 to 7A)	70	Self-restart	\checkmark	-	~	\checkmark	\checkmark	\checkmark	\checkmark	_	-	_	-	-	-	-	\checkmark	-	-
	1	New BV1LB010FPJ-C	3.0 to 5.5	42	42	10	Self-restart	-	—	-	—	—	_	_	—	-	-	-	-	-	\checkmark	-	_	-
	1	BV1LB028FPJ-C	3.0 to 5.5	42	30	28	Self-restart	-	—	-	—	—	—	_	—	-	—	—	—	—	\checkmark	—	—	_
	1	BV1LB045FPJ-C	3.0 to 5.5	42	18	45	Self-restart	-	—	-	—	—	_	_	—	-	-	-	-	-	\checkmark	—	—	-
	1	BV1LB085FJ-C*1	3.0 to 5.5	42	13	85	Self-restart	_	_	-	-	—	—	_	_	_	_	_	\checkmark	_	-	_	—	_
	1	BV1LB150FJ-C*1	3.0 to 5.5	42	6.5	150	Self-restart	_	_	-	_	_	_	_	_	_	_	_	\checkmark	_	_	—	_	-
	1	BV1LB300FJ-C*1	3.0 to 5.5	42	1.7	300	Self-restart	_	_	_	_	_	_	_	_	_	_	_	\checkmark	_	_	_	—	_
	1	BD1LB500 -C*1	3.0 to 5.5	42	0.8	350	Self-restart	-	—	-	—	—	_	_	—	-	\checkmark	-	-	\checkmark	-	—	—	-
	1	☆BV1LC085EFJ-C	3.0 to 5.5	42	5	85	Self-restart	\checkmark	—	-	-	\checkmark	\checkmark	\checkmark	—	-	-	-	-	\checkmark	-	-	—	_
	1	BV1LC105FJ-C	3.0 to 5.5	42	3	105	Self-restart	\checkmark	_	-	_	\checkmark	\checkmark	\checkmark	_	_	_	_	\checkmark	_	_	—	_	-
Low-Side	1	BV1LC150EFJ-C	3.0 to 5.5	42	3.5	150	Self-restart	\checkmark	_	-	-	\checkmark	\checkmark	\checkmark	_	_	_	_	_	\checkmark	-	_	—	_
Switches	1	BV1LC300FJ-C*1	3.0 to 5.5	42	1.7	350	Self-restart	\checkmark	_	_	_	\checkmark	\checkmark	\checkmark	_	_	_	_	\checkmark	_	_	—	_	_
	2	BM2LB150FJ-C	3.0 to 5.5	42	6.5	150	Self-restart	_	_	-	-	—	—	_	_	_	_	_	\checkmark	_	-	_	—	_
	2	BM2LB110FJ-C	3.0 to 5.5	42	2.5	120	Self-restart	-	-	-	_	-	_	_	-	-	_	-	\checkmark	-	_	_	_	-
	2	BM2LB300FJ-C	3.0 to 5.5	42	1.7	300	Self-restart	-	-	-	-	—	—	_	_	_	_	_	\checkmark	_	-	-	_	_
	2	BM2LC105FJ-C	3.0 to 5.5	42	3	105	Self-restart	\checkmark	-	- 1	-	\checkmark	\checkmark	\checkmark	-	-	_	-	\checkmark	-	-	-	-	-
	2	BM2LC300FJ-C	3.0 to 5.5	42	1.7	350	Self-restart	\checkmark	-	-	_	\checkmark	\checkmark	\checkmark	_	_	_	_	\checkmark	_	-	_	_	_
	8	BD8LB600FS-C	3.0 to 5.5(Digital) 4.0 to 5.5(Analog)	45	1	600	Self-restart	~	-	-	-	~	\checkmark	\checkmark	-	-	-	-	-	-	-	-	-	~
	8	BD8LA700EFV-C	3.0 to 5.5(Digital) 4.0 to 5.5(Analog)	45	0.5	700	Off-latch	\checkmark	_	-	-	\checkmark	\checkmark	\checkmark	-	-	_	-	-	-	-	-	\checkmark	-

1: Under Development *1: The 🔲 in the part number are reserved for the package code. Refer to the package columns at the right side of the table. *2: Hiccup type

BV2Hx045EFU-C series Overcurrent Protection Operation

Variable overcurrent limit and mask time make it easy to adjust current protection conditions depending on the load conditions.



Fixed Overcurrent Limi

Fixed value of the IC

Variable Overcurrent Limit

Unlike fixed overcurrent limit methods for IC protection, abnormal current is detected and limited during steady-state operation. This limit can be adjusted based on load characteristics using an external resistor(functions after the mask time ends)

Variable Mask Time

Mask time prevents erroneous detection of inrush current to the capacitive load as abnormal current during steady-state operation. It can be varied based on load characteristics via external capacitor

MOSFETs/Bipolar Transistors for Automotive LED Drivers

Nch MOSFET for BD18394EFV-M

Package	Part No.	Absolute	Maximum	n Ratings	R _{DS} (on) Typ(mΩ)	Qg Typ (nC)	Ciss Typ (pF)	
						V _{GS} =4.5V		
DFN2020	RF9G120BH	40	12	±20	17.5	9.1	520	
HSMT8	AG109DGQ4	40	27	±20	9.4	21	1,170	

Pch MOSFET for BD18353EFV/MUF-M

Package	Part No.	Absolute			R _{DS} (on) Typ(mQ)	Qg Typ (nC)	Ciss Typ
Ŭ		V _{DSS} (V)	I₀(A)	V _{GS} (V)	V _{GS} =10V	V _{GS} =5V	V _{GS} =10V
SOT-346T (TSMT3)	RSR015P06	-60	-1.5	±20	200	10*1	500
SOT-457T (TSMT6)	RSQ015P10	-100	-1.5	±20	350	17	950*2
						*1: V _{GS} =10\	/ *2: V _{GS} =25\

Nch MOSFET for BD18353EFV/MUF-M

Package	Part No.	Absolute	Maximum	Ratings	R _{DS} (on) Typ(mΩ)	Qg Typ (nC)	Ciss Typ (pF)
		V _{DSS} (V)	I _D (A)	V _{GS} (V)			
SOT-323 (UMT3)	RJU003N03	30	0.3	±12	800	-	24

PNP Transistors for the BD1834x series

	Part No.	Abse Maximun	olute n Ratings		
		$V_{CEO}(V)$	I _C (A)		
SOT-89 (MPT3)	2SAR533P	-50	-3	180~450	
TO-252	2SAR573D3	50	-3	190~.450	
(DPAK)	2SAR583D3	-50	-7	160~450	

Nch MOSFET

Single Type

Package	Part No.	Absolute	Maximun	n Ratings	R _{DS} (on) Tvp(mΩ)	Qg Typ (nC)	Ciss Typ (pF)
Ű		V _{DSS} (V)	I _D (A)	V _{GS} (V)	V _{GS} =10V	V _{GS} =5V	
	RSR025N05	45	2.5	±20	70	3.6	260
SOT-346T	RSR030N06	60	3	±20	60	5	380
(TSMT3)	RSR020N06	00	2	±20	120	2.7	180
	RSR010N10	100	1	±20	370	3.5	140
007 4575	RVQ040N05	45	4	±21	38	6.3	530
(TSMT6)	RSQ035N06	60	3.5	±20	50	6.5	430
(100010)	RSQ015N06	00	1.5	±20	210	2	110
SOT-89(MPT3)	RHP020N06	60	2	±20	150	7*1	140
8000	RSS070N05	45	7	±20	18	12	1000
3060	RSS065N06	60	6.5	±20	24	11	900
TO 050	RD3L080SN	60	8	±20	57	9.4*1	380
(D-PAK)	RD3L050SN	00	5	±20	78	8*1	290
	BD3P050SN	100	5	+20	135	14*1	530* ²

Dual Type(Nch+Nch)

Package	Part No.	Absolute	Maximum	n Ratings	R _{DS} (on) Typ(mΩ)	Qg Typ (nC)	Ciss Typ (pF)
		$V_{DSS}(V)$	$I_D(A)$	V _{GS} (V)	V _{GS} =10V	V _{GS} =5V	V _{GS} =10V
	SP8K24	45	6	±20	18	15.4	1400
	SP8K22	45	4.5	±20	33	6.8	550
	SP8K33		5	±20	34	8	Ciss Typ (pF) V _{0S} =10V 1400 550 620 500 250 600 610* ²
SOP8	SP8K32	60	4.5	±20	46	7	500
	SP8K31		3.5	±20	85	3.7	250
	SP8K41	80	3.4	±20	90	6.6	600
	SP8K52	100	3	±20	120	8.5	610* ²

*1: Vgs=10V *2: Vgs=25V

Note1: This page contains a partial except of ROHM's broad lineup and typical characteristics Please refer to ROHM's website for the complete lineup and detailed product characteristics, or contact a ROHM sales representative.

Note2: Indicates the JEDEC package type. (): ROHM Package, $\langle \, \rangle$: GENERAL Code.



High Output High Heat Dissipation Chip LEDs

PSL030x series

PSLED series

Under development

(2.75mm×2.0mm t=0.7mm) Color Lineup Red



Key Features

Compact · High output · High heat dissipation

- High reliability
- Automotive-grade(AEC-Q102 qualified)

Dimensions



		Forward	Measured Current I _F (mA)	Electrical · Optical Characteristics				
Emitting Color	Part No.	Current I _F Max(mA)		Forward Voltage V _F (V)	Dominant Wavelength $\lambda_{ extsf{D}}(nm)$	Luminous Flux ΦV(Im)	Thermal Resistance (°C/W)	
		(Ta=25°C)		Тур	Тур	Тур	Тур	
	☆PSL0301U2T	250	140	2.4	618	26*	19	
Red	☆PSL0304U21T	1,000	350	2.3	618	70	10	
	☆PSL0304U22T	1,000	700	2.6	618	120*	10	
Str. Please note that the specifications may change for products under development								

Concept

Develop compact high power LEDs optimized for a variety of stop lamps, from conventional point light source types to the latest uniform light sources featuring advanced design aesthetics

- ⇒ Adopts a high luminance chip structure
- ⇒ Facilitates designs utilizing products with different brightnesses by unifying package types

Performance

Supports the continuing diversification of stop lamp designs





Design Aligning LEDs Side-by-Side(Point Light Source) (No. of LEDs: Many) Design Combining LEDs with a Light Guide(Uniform Light Source) (No. of LEDs: Few)

Schottky Barrier Diodes for Automotive LED Drivers

Key Features

■ Lineup of all 3 series of automotive grade products from low V_F to ultra-low-I_R types



Package

High heat dissipation packages that contribute to thinner, smaller set are currently under development

(PMDE)	SOD-123FL(PMDU)	SOD-128(PMDTM)	TO-277	TO-252 (DPAK)
New/	•	•	Under Development	.
2.5mm×1.3mm×0.95mm	3.5mm×1.6mm×0.8mm	4.7mm×2.5mm×0.95mm	*Scheduled for mass Production in July 2020	10.0mm×6.5mm×2.2mm

RBxx8 series/Ultra Low I_R Type

Packaga	Part No	Absolute Maximum Ratings		Electrical Characteristic (Tj = 25°C)			
1 ackage	Tattivo.	V _R (V)	lo(A)	V _F Max (V)	I _F (A)	I _R Max (μΑ)	V _R (V)
	☆RB168VWM-40	40	1	0.69	1	0.5	40
(PMDE)	☆RB168VWM-60	60	1	0.76	1	0.5	60
	☆RB168VWM100	100	1	0.84	1	0.3	100
	RB168MM-40	40	1	0.65	1	0.55	10
	RB068MM-40	40	2	0.725	2	0.55	40
SOD-123FL	RB168MM-60	<u> </u>	1	0.68	1	1.5	<u> </u>
(PMDU)	RB068MM-60	60	2	0.765	2	1.5	60
	RB168MM100	100	1	0.81	1	0.4	100
	RB068MM100	100	2	0.87	2	0.4	100
	RB168LAM-40		1	0.69	1	0.5	
	RB068LAM-40	40	2	0.69	2	1.0	40
	RB058LAM-40	40	3	0.69	3	2.5	
	RB088LAM-40		5	0.71	5	3.6	
	RB168LAM-60		1	0.68	1	1.5	
SOD-128	RB068LAM-60		2	0.68	2	2.0	
(PMDTM)	RB058LAM-60	60	3	0.64	3	4.0	60
	RB088LAM-60		5	0.71	5	4.0	
	RB168LAM100		1	0.81	1	0.4	
	RB068LAM100	100	2	0.81	2	1.5	100
	RB058LAM100	100	3	0.81	3	3.0	
	RB088LAM100		5	0.87	5	3.0	
	RB075BM40S		5	0.75	5	5	
	RB098BM-40	40	6	0.77	3	1.5	40
	RB088BM-40		10	0.77	5	3.0	1
	RB098BM-60	<u> </u>	6	0.83	3	1.5	<u></u>
10-252 (DPAK)	RB088BM-60	00	10	0.83	5	3.0	00
	RB098BM100	100	6	0.77	3	3.0	100
	RB088BM100	100	10	0.87	5	5.0	100
	RB088BM200	000	10	0.88	5	7.0	000
	RB218BM200	200	20	0.88	10	10.0	200

RBR series/Low V_F Type

Packaga	Part No.	Absolute Maximum Ratings		Electrical Characteristic (Tj = 25°C)			
Fachage	Fait NU.	V _R (V)	lo(A)	V _F Max (V)	I _F (A)	l _R Max (μΑ)	V _R (V)
	☆RBR1VWM40A	40	1	0.52	1	50	40
	☆RBR2VWM40A	40	2	0.62	2	50	40
	☆RBR1VWM60A	<u> </u>	1	0.53	1	75	<u> </u>
	☆RBR2VWM60A	60	2	0.65	2	75	60
	RBR1MM40A		1	0.52	1	50	
	RBR2MM40A	40	2	0.62	2	50	40
SOD-123FL	RBR3MM40A		3	0.62	3	80	1
(PMDU)	RBR1MM60A		1	0.53	1	75	60
	RBR2MM60A	60	2	0.65	2	75	
	RBR3MM60A		3	0.66	3	100	
	RBR1LAM40A	40	1	0.52	1	50	40
	RBR2LAM40A		2	0.55	2	80	
	RBR3LAM40A		3	0.69	3	50	
SOD-128	RBR5LAM40A		5	0.53	5	200	
(PMDTM)	RBR1LAM60A		1	0.53	1	75	
	RBR2LAM60A	60	2	0.65	2	75	60
	RBR3LAM60A	60	3	0.66	3	100	
	RBR5LAM60A		5	0.55	5	250	
	RBR10BM40A		10	0.62	5	120	
	RBR15BM40A	40	15	0.55	7.5	240	40
TO-252	RBR20BM40A		20	0.55	10	360]
(DPAK)	RBR10BM60A		10	0.65	5	200	
	RBR15BM60A	60	15	0.58	7.5	400	60
	RBR20BM60A		20	0.59	10	600	

☆: Under Development

X: Under Development Note1: This page contains a partial except of ROHM's broad lineup and typical characteristics Please refer to ROHM's website for the complete lineup and detailed product characteristics, or contact a ROHM splex representation.

sales representative. Note2: Please note that the specifications/scheduling may change without prior notice for products under development. Contact a ROHM sales representative for the latest specifications/development status. Note3: Indicates the JEDEC package type. (): ROHM Package, 〈): GENERAL Code.

New Products

Wide Terminal/High Power Chip Resistors(1220 to 2550 size)

Spec

LHR18, LTR10/18/50

LHR series	
LHR18(1632 size)	
LTR series	
LTR10(1220 size)	
I TB18(1632 size)	

Front/LHR18 Back/LHR18 LTR50(2550 size)

Key Features

- Wide terminal configuration improves heat dissipation to the substrate
- Superior junction reliability against temperature cycling
- Higher TCR accuracy through pattern optimization(LHR18)

Concept



Туре	Size mm(inch)	Rated Power (W)		Resistance Range	Temperature Coefficient of Resistance(ppm/°C)	Operating Temp (°C)
New			E(1.02()	$33m\Omega$ to $39m\Omega$	0 to 125	
LHR18	1632	1.25	F(±1.0%)	$47m\Omega$ to $270m\Omega$	0 to 100	
	(0612)		0(10:070)	300mΩ to 1Ω	0 to 75	
	1000	0.5	F(±1.0%) J(±5.0%)	47m Ω to 9.1 Ω	±150	
LTR10	(0508)		D(±0.5%)	10Ω to 1MΩ	±100	
	(0000)	0.25	F(±1%)	1Ω to 1MΩ	±100	
			J(±5%)	1Ω to 1MΩ	±200	
	1632 (0612)		F(±1%) J(±5%)	$10m\Omega$ to $18m\Omega$	0 to 300	
		1.0		$20m\Omega$ to $47m\Omega$	0 to 200	-55 to ±155
1 7010		1.0		$51m\Omega$ to $470m\Omega$	0 to 150	
LIKIO				510mΩ to 1.0Ω	±100	-55 10 +155
			D(±0.5%)	10Ω to 1MΩ	±100	
		0.75	F(±1%)	1Ω to 1MΩ	±100	
			J(±5%)	1Ω to 1MΩ	±200	
		Nou		$10m\Omega$ to $18m\Omega$	0 to 300	
		2.0	F(±1%)	$20m\Omega$ to $47m\Omega$	0 to 200	
	2550	2.0	J(±5%)	$51m\Omega$ to $91m\Omega$	0 to 150	
LTR50	(1020)			100mΩ to 910mΩ	±100	
	(1020)		D(±0.5%)	10Ω to 1MΩ	±100	
		1.0	F(±1%)	1Ω to 1MΩ	±100	
			J(±5%)	1 Ω to 1MΩ	±200	

Perfomance



New High Power Anti-Surge Chip Resistors(1005 to 3225 size) Products

Spec

SDR03/10, ESR01/03/10/18/25

SDR series SDR03(1608 size)

SDR10(2012 size) **ESR** series

ESR01(1005 size) ESR03(1608 size) ESR10(2012 size) ESR18(3216 size) ESR25(3225 size)



Key Features

- Proprietary element technology improves surge resistance
- Guaranteed electrostatic breakdown voltage 2kV to 5kV (EIAJ4701-1 Human Body Model)

	Size mm(inch)	Rated Power (W)		Resistance Range	Temperature Coefficient of Resistance(ppm/C)	Operating Temp (°C)					
			D(±0.5%)	10Ω to 1MΩ(E24/96)	±100						
60002	1608	0.3	E(+1.0%)	1 Ω to 9.76Ω(E24/96)	±200						
SDR03	(0603)	0.5	1 (±1.070)	10Ω to 10MΩ(E24/96)	±100						
			J(±5.0%)	1Ω to 10MΩ(E24)	±200						
lew	0010		D(±0.5%)	10Ω to 1MΩ(E24/96)	±100						
SDB10	(0805)	0.5	F(±1.0%)	1Ω to 10MΩ(E24/96)	±100						
obiiio	(0000)		J(±5.0%)	1Ω to 10MΩ(E24)	±200						
			E(10()	10Ω to 976kΩ(E24/96)	100						
ESR01	1005	0.2	F(±1%)	1MΩ to 2.2MΩ(E24)	±100						
	(0402)		0.2 J (±5%)	1Ω to 9.1Ω(E24)	±500/-250						
				10Ω to 10MΩ(E24)	±200						
			D(±0.5%)	10Ω to 1MΩ(E24/96)	±100	EE to 11EE					
50000	1608	0.25	0.05	0.25	0.25	0.25	0.25	E(. 10/.)	1 Ω to 9.76Ω(E24/96)	±200	-55 10 +155
ESR03	(0603)		F(±1%0)	10Ω to 10MΩ(E24/96)	±100						
			J(±5%)	1Ω to 1MΩ(E24)	±200						
			D(±0.5%)	10Ω to 1MΩ(E24/96)	±100						
ESR10	2012	0.4	F(±1%)	1 Ω to 10MΩ(E24/96)	±100						
	(0003)		J(±5%)	1 Ω to 30MΩ(E24)	±200						
			D(±0.5%)	10Ω to 1MΩ(E24/96)	±100						
ESR18 321 (120	3216	3216 0.5 F(±1%	F(±1%)	1Ω to 10MΩ(E24/96)	±100						
	(1200)		J(±5%)	1Ω to 15MΩ(E24)	±200						
			D(±0.5%)	10Ω to 1MΩ(E24/96)	±100						
ESR25	3225 (1210) 0.66	0.66	F(±1%)	1Ω to 10MΩ(E24/96)	±100						
			J(±5%)	1Ω to 10MΩ(E24)	±200						

Rated Power List

Class-leading power lineup contributes to system miniaturization

Size mm	MCR series Standard Type	ESR series Surge Resistance Type	SDR series High Surge Resistance Type	LTR series High Power Wide Terminal Type	LTR Low Ohmic series High Power Wide Terminal Type	LHR series High Power Low TCR Wide Terminal Type
1005	0.063W	0.2W	-	-	-	-
1608	0.1W	0.25W	0.3W	-	-	-
2012(1220)	0.125W	0.4W	0.5W	0.25W	0.5W	-
3216(1632)	0.25W	0.5W	-	0.75W	1.0W	1.25W
3225	0.25W	0.66W	-	-	-	-
5025(2550)	0.5W	-	-	1.0W	2.0W	-

*LTR Low Ohmic series: $10m\Omega$ to 1Ω , LTR series: 1Ω or more *The size in the () indicates the size of the wide terminal type.

New Products

Anti-Sulfuration Chip Resistors(1005 to 3225 size)

SFR01/03/10/18/25

SFR series SFR01(1005 size) SFR03(1608 size) SFR10(2012 size) SFR18(3216 size) SFR25(3225 size)	Front Back						
Kev Features							
 Lineup offered in the 1005 to 3225 sizes Improved sulfuration resistance (compared with ROHM's conventional products) 							
Concept							
Provides a stable supp anti-sulfuration perform	bly of products that deliver nance						

- ⇒ Add a sulfurization-resistant layer to the inner electrode
- ⇒ Utilize a Ni-Cr metal strong against corrosion for the side electrodes



Spec

	Size mm(inch)	Rated Power		Resistance Range (Ω)	Temperature Coefficient of Resistance(ppm/C)	Operating Temp (°C)
	1005		. =04	1 to 9.1(E24)	+500/-250	
SFR01	1005	0.063W	±3%	10 to 10M(E24)	±200	
	(0402)		±1%	10 to 2.2M(E24/96)	±100	
SFR03	1000		· E0/-	1 to 9.1(E24)	±400	
	(0603)	0.1W	±5%	10 to 10M(E24)	±200	
			±1%	10 to 10M(E24/96)	±100	
	2012 (0805)	0.125W	±5%	1 to 9.1(E24)	±400	
SFR10				10 to 10M(E24)	±200	-55 10 +155
			±1%	10 to 2.2M(E24/96)	±100	
New	2016		. =04	1 to 9.1(E24)	±400	
SFR18	(1206)	0.25W	±370	10 to 10M(E24)	±200	
	(-=)		±1%	10 to 2.2M(E24/96)	±100	
New	3225	0.514	±5%	1 to 1M(E24)	±200	
SFR25	(1210)	0.500	±1%	10 to 1M(E24/96)	±100	

Performance

Disconnection rate during sulfuration testing



Tech Tips Chip Resistor Sulfuration

SFR01/03/10/18/25

Disconnection Due to Sulfuration

Sulfur particles are generally present in the air in various forms, such as automotive exhaust gases and sulfur gas in hot springs. These sulfur components are adsorbed on metallic surfaces and gradually react with the metal itself. Silver(Ag) is typically used for the internal electrode in thick-film chip resistors, and when sulfur components in the gas enters the gap between the protective layer and plating, silver migrates and reacts, gradually forming silver sulfide(Ag₂S) as shown in the below figure for 'Sulfuration Mechanism'. As a result, the internal electrode may become disconnected, causing an open circuit within the resistor. This phenomenon is referred to as disconnection due to sulfuration of the chip resistance. The SFR series adopts a sulfuration-resistant layer on the internal electrode, preventing sulfur erosion.



External appearance of standard chip resistors

Note: E24: Standard products/E96: Custom products



Enlarged photo of sulfurized chip resistor



Noise Characteristics and Immunity

What are noise characteristics?

EMC

⇒ Electromagnetic Compatibility

Refers to the ability to maintain performance even if disturbed by other equipment without interfering with external systems. It is called electromagnetic compatibility due to the need to ensure normal device operation without mutual electromagnetic interference, classified as EMI and EMS, below.

EMI

⇒ Electromagnetic Interference(Emission)

EMI refers to noise generated by the target IC that can interfere with the operation of surrounding ICs and systems. Since EMI may cause peripheral IC and/or systems to malfunction, delicate circuit design is necessary to prevent this phenomenon from occurring.

EMS

⇒ Electromagnetic Susceptibility(Immunity)

EMS is the tendency(ability/tolerance) for equipment to malfunction in the presence of external noise. If sufficient tolerance cannot be secured, the circuit may malfunction or fail to operate, so a robust circuit design is necessary.

Possible issues with improper PCB layout

- The following issues may occur if the PCB layout of the LED driver is inappropriate
 - ⇒ Low EMC and PI performance
 - ⇒ Deterioration in light intensity (i.e. LED current accuracy)
 - ⇒ LED fluctuation due to unstable operation (oscillation, switching waveform breakdown, etc.)

PCB Design Checklist

- Designing a proper PCB layout
- 1 Make the power line as short and wide as possible.
- 2 Keep the MOSFET-diode-capacitor loop short to minimize the AC current path.
- **3** Place the oscillation frequency determination resistor RT close to the GND pin(reference GND).
- 4 Place the decoupling capacitor used for constant voltage within the IC as close to the IC pin as possible.
- 5 Keep the feedback line of the current detection resistor far from noise sources such as switching lines.
 - ⇒ In the case of double-sided mounting, place the power products on the same side as the IC and other components on the backside as an effective countermeasure. At this time do not let the feedback line pass under the inductor.
- 6 Separating the power GND(SBD, input/output capacitor GND) from the reference GND(RT, GND) will minimize the effects of switching noise. Make them common to the GND plane.
- 7 Please refrain from using thermal relief as much as possible.
 ⇒ The high frequency characteristics will degrade.

EMC Issues on the Same Board



EMC Issues from Outside the Board



Precautions Regarding Thermal Relief

Be careful regarding the layout of the capacitor used as a noise countermeasure



With the above layout(thermal relief), since the ESL component of the PCB is added, the resonant frequency moves to the lower frequency side according to the previous equation and the desired noise removal effect cannot be obtained.



Definitions • Applications • Formulas

These definitions conform to JEDEC standard JESD51

Symbol	Definition	Applications	Formula	
θја	The thermal resistance between the junction temperature Tj and ambient temperature Ta when the package is mounted on a PCB	Comparing the heat dissipation performance among packages of different shapes	$ heta_{JA} = (Tj-Ta) \div P$	
Ψյτ	The thermal characteristics parameter between the junction temperature Tj and the temperature of the center of the upper surface of the package T_{T} .	Estimating junction temperature	$\Psi_{JT} = (Tj – T_T) \div P$	
Ө ЈС-ТОР	The thermal resistance between the junction temperature Tj and temperature of the top surface of the package Tc-TOP. The heat dissipation path is only on the top surface; the others are adiabatic.	Used for simulations using the 2-resistance model	$\theta_{\text{JC-TOP}} = (\text{Tj}-\text{Tc-top}) \div \text{P}$	
Ө јс-вот	The thermal resistance between the junction temperature Tj and the temperature of the bottom surface of the package T_{C-BOT} . The heat dissipation path is only on the bottom surface; the others are adiabatic.	Used to estimate the junction temperature, since when the heat dissipation metal at the bottom of the package is exposed, most of the heat flows only through the package bottom.	$\theta_{\text{JC-BOT}} = (\text{Tj}-\text{T}_{\text{C-BOT}}) \div \text{P}$	

Note 1: $\partial JA/\Psi_{JT}$ is the value when mounted on a JEDEC board. Note 2: Conventionally, the value provided as ∂_{JC} is Ψ_{JT} in this definition.

Illustrations for Each Definition



EMC Countermeasure

Market Background

- The increasing number of ECUs and continuing miniaturization (higher frequency) is increasing the number of cases where the internal noise interference worsens.
 - ⇒ Increased risk of malfunction due to noise
 - ⇒ Greater risk of generating noise which can cause malfunctions to surrounding equipment
- Also, upon further investigation the following can be expected.
 - ⇒ With the continuing proliferation of ADAS and automated driving, it has become imperative to prevent malfunctions and control failures due to external noise.
 - ⇒ Eliminating metal body (shield) and reducing body weight to minimize environmental load

EMC countermeasure technology is becoming more important

ROHM EMC Countermeasure Support System

- Established an anechoic chamber (at the Shin-Yokohama Technology Center)
- Recommendations on application countermeasures designed to clear the CISPR25 Class 5 requirements



Note: A fee may be charged depending on the type of support

Thermal Countermeasure

- Validate thermal designs before PCB ordering by performing simulations that include board patterns
- In the event of thermal issues, we can suggest improvements through actual measurements and simulations

Market Background

- The number of cases where the thermal environment for parts is worsening has increased due to mechanical integration and mounting in engine compartments
 - ⇒ Increases the risk of a reduction in the quality and life of electronic components

Heat dissipation design technology is becoming more important

Thermal Simulation Support Case Study



Automotive EMC Test Standard

Applicable EMC standards

Automotive Emission Test								
Test Method	Standard	Frequency						
Radiated Emission	CISPR25 Class5	150kHz to 1GHz						
Conducted Emission	CISPB25 Class5	150kHz to 108MHz						

Automotive Immunity Test

	Test Method	Standard	Frequency	Max	
	BCI Immunity	ISO11452-4	100kHz to 2.1GHz	200mA *≦400MHz: 300mA	
	Transient Immunity	ISO7637-2/3/5	Pulse 1/2a/2b/3a/3b/4/5a/5b		
	Radiated Immunity	ISO11452-2	80MHz to 3GHz	200V/m	
		Radar pulse	1.2 to 1.4GHz	300V/m	
			3.1 to 4.2GHz		
	Near field Antenna Immunity	Custom SPEC	800MHz to 2.4GHz	up to15W	
	TEM CELL Immunity	ISO11452-3	1MHz to 400MHz	200W	

Measurement Example



Thermal Resistance Measurement On the Customer's Board

Prototyping, evaluating mass-produced boards, thermal resistance measurement

 Determine the thermal resistance of the actual board

 Image: state of the actual board</t

Environment

Measurement result

Thermal Simulation with the Customer's Board/ Heat Dissipation Environment



Note: A fee may be charged depending on the type of support

ROHM Manufacturing

Throughout its history cars have continued to evolve in response to the growing awareness for safety, comfort, and the environment, in step towards continued electrification.

In the course of this progress, autonomous driving and smart cities will soon be realized with the advent of next-generation vehicles.

ROHM contributes to the evolution and advancement of the automotive sector and next-generation cars by taking a quality-first approach to manufacturing and ensuring long-term, stable supply of products.



Achieving High Quality and Stable Supply Through a Vertically Integrated Production System

ROHM's vertically integrated production system is the result of its commitment to 'Quality First'. The ROHM Group carries out manufacturing, sales and service - from design and development to wafer fabrication - in-house and continually works on initiatives to improve quality in all processes.



High Quality

Achieving high quality in every process

ROHM continually pursues 'Quality First' as a corporate objective. Through our vertically integrated production system the Group implements production, sales, and service - including design, development, and wafer fabrication - and are working on initiatives to improve quality in all processes. At the same time, excellent traceability is achieved through a system that ensures worry-free use of our products by customers.

Stable Supply

Utilizing the Group's collective power to fulfill supply responsibilities

The ROHM Group is tasked with supplying products that meet market demands. By managing the manufacturing process in-house using our vertically integrated production system, we are able to create a system that is less susceptible to external factors compared with general fabless and foundry manufacturers. We have established a BCM(Business Continuity Management) system that involves securing appropriate inventory and carrying out multi-site production, and endeavor to ensure a stable supply to customers.



In-House Production System

All production equipment were developed completely in-house, making it possible to flexibly and precisely meet customer needs.



All production systems developed in-house

BCM System

ROHM continues to strengthen its BCM system by performing diagnosis based on risk verification at all production sites.



Approach for Automotive-Grade Products

ROHM establishes 'Quality First' as a corporate objective, pursues innovative, high quality manufacturing, and provides greater peace of mind through guaranteed delivery times. ROHM implements a variety of initiatives to ensure high reliability.

Initiative Example

Real-time quality checks

From silicon ingot pulling and wafer fabrication to testing, final assembly, and shipment, ROHM adopted a screening method to check the workmanship at each process.

ROHM original real-time quality checks



Check the workmanship at the same time as performing die bonding

Real Time Work & Check



Check the quality at the same time as performing die bonding

Introducing the PAT System(Conforming to AEC Guidelines)

The PAT system statistically analyzes measurement data and removes out-of-group items even when they are within good product standards. With this method even when a product is determined to be non-defective and within the standard at the time of shipment, if it is out-of-group within the lot distribution it is removed as having the potential of being defective. This allows ROHM to act out of an abundance of caution to prevent the shipment of defective products.

PAT System PAT: Part Average Testing (Parts Averaging Test)



Dedicated automotive product line

Automotive products are manufactured on dedicated lines by certified operators who have passed special tests. Utilizing dedicated Machine and Man results in a higher grade manufacturing environment

Line division and 4M differentiation

The basic elements of ROHM's approach to quality 4M…Man Machine Material Method

All automotive-grade products are manufactured on HR(High Reliability) lines separate from general products.



Initiative Overview(IC Case)

Model Design

Robust design with multiple protection circuits/improved damage resistance/easier testability/characteristics limit evaluation

Wafer Process Management

SPC management/Real-time monitoring/Defect inspection of all chips

Model Test Design

High/normal/low temperature measurement of all chips, HV stress testing, PAT system introduction

Assembly Process Management

Real-time Work & Check at main processing point (s)/Quality guarantee (i.e. internal X-ray inspection reflow screening)/4M establishment

Model Qualification Testing

Based on JEITA. JEDEC, AEC-Q100/AEC-Q101/AEC-Q200 compliance Long-term reliability testing Life prediction based on WLR data Electrostatic breakdown test

Traceability, keep samples, in-process defect analysis, etc.

Important Security Applications All keep samples from all lots are stored for 10 years/In-process defective product analysis(all lots), etc.



Tech Web	+ DEVICE PLUS	Electronics Basics
https://micro.rohm.com/en/techweb	https://www.deviceplus.com	https://www.rohm.com/electronics-basics

Home Page Desig	gn Support Content List		
Item	Overview		
Selection Guide (This Catalog)	A guidebook that simplifies IC selection. Product pickups and sample solutions are provided.		
Datasheet	Contains the most important information provided to customers on ROHM products. Functional characteristics, conditions, and applicable ranges built into the products are listed, along with the scope of warranty. Also provided is application information, including the required external parts, in order to ensure stable operation and maximize performance		
Application Note Example	Switching Type	Capacitor Calculation for Buck Converter ICs Considerations for Multilayer Ceramic Capacitor Used for Buck Converters Inductor Calculation for Buck Converter ICs Considerations for Power Inductors Used for Buck Converters Quick Reference Table for Setting the Output Voltage of Buck Converter ICs PCB Layout Method for Buck Converters Snubber Circuit for Buck Converter ICs Buck Converter Efficiency Calculating Power Loss(Synchronous Rectification Type)	
	Linear Type	Reverse Voltage Protection for Linear Regulators Output Voltage Setting Resistance Table for Linear Regulator ICs Linear Regulator Power Supply ON/OFF Characteristics Simple Stability Experiments for Linear Regulators Thermal Resistance Data of Automotive Linear Regulators	
	General	Phase Margin Measurement Method Using a Frequency Characteristics Analyzer(FRA) Regarding Thermal Resistance About Thermal Resistance and Thermal Characteristics of IC Packages	
SPICE Models	SPICE models are offered that can be used in PSpice simulations. However, since the files are encrypted for security purposes, they are executable only with PSpice		
	Package Information	Implementation specifications, resistance to whiskers	
Desis lafermetian	Package Information Environmental Data	REACH Substances of Very High Concern(SVHC) non-use certificate, UL94 flame retardant class ELV, RoHS Directive certificates of compliance	
Dasic Information	Reliability Information	Report on reliability test results	
	Individual Product Data	List of production facilities	
	Export-Related Information	Regarding the Export Trade Control Order and US Export Regulations	
Support Page	Provides new product information, evaluation boards, and videos		
Technology Information Site Tech Web	Acquire basic knowledge on power supply ICs Archive site on the latest topics on power supply ICs ideal for engineers - TECH INFO		

ROHM Group Locations (Japan)





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	India Design Center
AMERICA	America Design Center (Santa Clara)
EUROPE	Europe Design Center
	Finland Software Development Center

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