

By Category PDF

Category Interface

ICs

# Interface

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## LVDS Interface ICs

27bit LVDS Transmitter 27 : 4 Serializer									
Part No.	Type	bits (bit)	Color Depth	Input Specification	Output Specification	Clock Frequency (MHz)	Supply Voltage (V)	Operating Temperature (°C)	Package
BU90T81	Serializer	27	8	LVC MOS	LVDS Single Link	20 to 112	1.65 to 1.95	-20 to +85	VBGA048W040
27bit LVDS Transmitter 27 : 8 Serializer									
BU90T82	Serializer	27	8	LVC MOS	LVDS Dual Link	10 to 174	1.62 to 1.98/ 1.62 to 3.60	-40 to +85	SBGA072T070A
35bit LVDS Transmitters 35 : 5 Serializer									
BU8254KVT	Serializer	35	10	LVC MOS	LVDS Single Link	8 to 112	3.0 to 3.6	-40 to +85	TQFP64V
BU8254GUW	Serializer	35	10	LVC MOS	LVDS Single Link	8 to 112	3.0 to 3.6	-20 to +85	VBGA099W060
56bit LVDS Transmitter 56 : 8 Serializer									
BU7988KVT	Serializer	56	8	LVC MOS	LVDS Dual Link	8 to 112	3.0 to 3.6	-20 to +85	TQFP100V
35bit LVDS Receiver 5 : 35 Deserializer									
BU90R104	Deserializer	35	10	LVDS Single Link	LVC MOS	8 to 112	2.3 to 3.6	-40 to +85	TQFP64V
56bit LVDS Receiver 8 : 56 Deserializer									
BU7985KVT	Deserializer	56	8	LVDS Dual Link	LVC MOS	20 to 112	3.0 to 3.6	-20 to +85	TQFP100V
67bit LVDS Receiver 10 : 67 Deserializer									
BU90R102	Deserializer	67	10	LVDS Dual Link	LVC MOS	8 to 160	2.3 to 3.6	-40 to +85	HQFP144VM
70bit LVDS Distributor									
BU90RT102	Serializer/ Deserializer	70	10	LVDS	LVDS	20 to 135	3.0 to 3.6	-20 to +85	HTSSOP-C64
4bit LVDS Driver									
BU90LV047A	Driver	4	-	LVC MOS	LVDS	250	3.0 to 3.6	-40 to +85	SSOP-B16
4bit LVDS Receiver									
BU90LV048	Receiver	4	-	LVDS	LVC MOS	250	3.0 to 3.6	-40 to +85	SSOP-B16
4bit LVDS Transceiver									
BU90LV049A	Transceiver	4	-	LVC MOS/LVDS	LVC MOS/LVDS	250	3.0 to 3.6	-40 to +85	SSOP-B16

## Timing Controllers

Timing Controller for FHD, WUXGA									
Part No.	Supply Voltage (V)	Input Specification	Output Specification	Input bits (bit)	Output bits (bit)	Clock Frequency (MHz)	Operating Temperature (°C)	Resolution	Package
BU90AM4-03	1.62 to 3.63/ 1.62 to 2.20/ 1.08 to 1.32	MIPI DSI 4lane	P2P 8lane	8/6	8/6	40 to 200	-20 to +70	up to WUXGA (1920x1200)	UQFN54

# Multiple Input Switch Monitor LSIs

22ch models											
Part No.	Supply Voltage (V)	Switch Input Number	Switch Input Voltage Range (V)	Wetting Current (mA)	Operating Current Intermittent Monitoring 50ms (Max) (μA)	Control I/F	Clock Frequency (MHz)	Operating Temperature (°C)	Package	ComfySIL™ Functional Safety*1	Automotive Grade AEC-Q100
<b>BD3378MUV-M</b>	6.0 to 28.0 (VPUA/VPUB) 3.1 to 5.25 (VDDI)	22	-14 to +40	1/3/5/10/15 (Pull up/Pull down)	100	SPI	up to 4.4	-40 to +125	VQFN48MVCV070	FSs	YES
33ch models											
<b>BD3381EKV-C</b>	6.0 to 28.0 (VPUA/VPUB) 3.1 to 5.25 (VDDI)	33	-14 to +40	1/3/5/10/15 (Pull up/Pull down)	110	SPI	up to 4.4	-40 to +125	HTQFP64BV	FSs	YES
10ch models											
<b>BD3376EFV-C</b>	8.0 to 26.0 (VPUA/VPUB) 3.1 to 5.25 (VDDI)	10	-14 to +40	1/3/5/10/15 (Pull up/Pull down)	100	SPI	up to 4.4	-40 to +125	HTSSOP-B30	FSs	YES

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\*1 For more information about "ComfySIL™ Functional Safety", please refer to the of the cover.

# LIN Transceivers

LIN Transceivers									
Part No.	Supported Standards	Supply Voltage (V)	Operating Temperature (°C)	Absolute Maximum Rating of LIN pin (V)	Baud Rates (Max) (kbps)	Supply Current at Sleep Mode (Typ) (μA)	Package	ComfySIL™ Functional Safety*1	Automotive Grade AEC-Q100
<b>BD41030FJ-C</b>	LIN2.0, LIN2.1, LIN2.2, LIN2.2A	5 to 27	-40 to +125	-27 to +40	20	3	SOP-J8	FSs	YES
<b>BD41030HFN-C</b>	LIN2.0, LIN2.1, LIN2.2, LIN2.2A	5 to 27	-40 to +125	-27 to +40	20	3	HSO8	FSs	YES

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# CAN Transceivers

CAN Transceivers									
Part No.	Supported Standards	Supply Voltage (V)	Operating Temperature (°C)	Absolute Maximum Rating of CAN pin (V)	Data Rates (Max) (Mbps)	Supply Current at Standby Mode (Typ) (μA)	Package	ComfySIL™ Functional Safety*1	Automotive Grade AEC-Q100
<b>BD41041FJ-C</b>	ISO 11898-2: 2016	4.75 to 5.25	-40 to +125	-27 to +40	1	10	SOP-J8	FSs	YES
<b>BD41044FJ-C</b>	ISO 11898-2: 2016	4.75 to 5.25	-40 to +125	-27 to +40	5	10	SOP-J8	FSs	YES

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# CXPI Transceivers

CXPI Transceivers									
Part No.	Supported Standards	Supply Voltage (V)	Operating Temperature (°C)	Absolute Maximum Rating of BUS (V)	Baud Rates (kbps)	Supply Current at Sleep Mode (Typ) (μA)	Package	ComfySIL™ Functional Safety*1	Automotive Grade AEC-Q100
<b>BD41003FJ-C</b>	JASO_D015_3	7 to 18	-40 to +125	-27 to +40	18.8 to 20.0	3	SOPJ-8	FSs	YES

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# USB Type-C Power Delivery

For POWER SOURCE (POWER Role: Source, DATA Role: DFP, Internal Shunt Reg., Variable OCP, Variable OVP, Internal Vconn SW)											
Part No.	Supply Voltage (V)	IO Supply Voltage (V)	Type-C/PD Support	Initial Supply Capable Voltage/Current (V/A)	Tolerant Voltage at CC Pins (V)	Gate Drivers for Nch FET	After OCP Behavior After OVP Behavior	DP Alternate Mode	Operating Temperature (°C)	Package	
<b>BM92A20MWW</b>	3.1 to 20.0	1.7 to 5.5	R1.2/R2.0 (Internal Shunt Reg. for ACDC)	5/3, 12/3.75, 15/3, 16/2.8, 19/2.36, 19.6/2.29, 20/2.25	6.5	For Source: 1pair	OCP: Automatic recovery OVP: Automatic recovery	-	-30 to +105	UQFN40V5050A	
<b>BM92A21MWW</b>			R1.2/R2.0	5/3, 9/3, 12/3, 15/3, 20/3		For Source: 2pair				UQFN40V5050A	
<b>BM92A50MWW</b>			R1.2/R2.0	5/0.5, 12 to 20 Variable/2.25		For Source: 1pair (For Sink: 1pair)				UQFN40V5050A	
<b>BD93F50MWW</b>	3.1 to 22.0		R1.3/R3.0	5V to 20V Selectable	28		OCP: Selectable OVP: Selectable	DP_SINK with Ext-MCU	-30 to +85	UQFN040V5050	
For POWER SOURCE & SINK (POWER Role: Source/Sink/DRP, DATA Role: DFP/UFP/DRD)											
Part No.	Supply Voltage (V)	IO Supply Voltage (V)	Type-C/PD Support	Connected The Required Initial Voltage (V)		CC terminal voltage (V)	Gate Drivers for Nch FET	DP Alternate Mode	Operating Temperature (°C)	Package	
				Dead Battery	Non Dead Battery						
<b>BM92A30MWW</b>	3.1 to 20.0	1.7 to 5.5	R1.2/R2.0	12 to 20 (It requires the maximum voltage had by the source side between 12V to 20V.) <sup>*1</sup>		6.5	For Sink: 1pair For Source: 1pair	DP_SOURCE	-30 to +105	UQFN40V5050A	
<b>BM92A34MWW</b>				9 (Power Delivery communication is contracted only for the stated voltage.) <sup>*1</sup>						5	UQFN40V5050A
For POWER SINK (POWER Role: Sink, DATA Role: UFP)											
Part No.	Supply Voltage (V)	IO Supply Voltage (V)	Type-C/PD Support	Connected The Required Initial Voltage (V) Without Ext-MCU		Start of Automatic Power Receiving Without Ext-MCU	CC terminal voltage (V)	Gate Drivers for Nch FET	DP Alternate Mode	Operating Temperature (°C)	Package
				Without Ext-MCU	Without Ext-MCU						
<b>BM92A12MWW</b>	3.1 to 20.0	1.7 to 5.5	R1.2/R2.0	20 (Power Delivery communication is contracted only for the stated voltage.) <sup>*1</sup>		✓	6.5	For Sink: 1pair For Source: 1pair	-	-30 to +105	UQFN40V5050A
<b>BM92A13MWW</b>				15 (Power Delivery communication is contracted only for the stated voltage.) <sup>*1</sup>							UQFN40V5050A
<b>BM92A14MWW</b>				9 (Power Delivery communication is contracted only for the stated voltage.) <sup>*1</sup>							UQFN40V5050A
<b>BM92A15MWW</b>				5 to 20 (It requires the maximum voltage had by the source side between 5V to 20V.) <sup>*1</sup>							UQFN40V5050A
<b>BM92A19MWW</b>				5 (Power Delivery communication is contracted only for the stated voltage.) <sup>*1</sup>							UQFN40V5050A
<b>BD93F10MWW</b>	3.1 to 22.0		R1.3/R3.0	5V to 20V Selectable		Selectable	28		-30 to +85	UQFN040V5050	
<b>BD91N01NUX</b>	4.0 to 5.5		R1.3/-	Type-C 5V		✓	28	For Sink: 1path		VSON010X3020	

<sup>\*1</sup> If communication is not contracted, source side supply 5V for type-C connected.  
That time, the control for external Nch-FET is not automatically turned on, so an instruction for FET control is required from other the IC.