

V _{DSS}	650V
R _{DS(on)} (Typ.)	70mΩ
Q _G , _{typ} .	5.2nC
I _{D(Tc=25°C)} *1	20A
Q _{oss} @ 400V	44nC
Q _{rr}	0nC

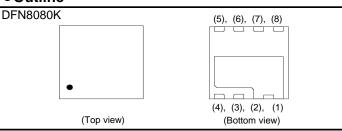
Features

- 650V E-mode GaN FET
- 70mΩ Resistance
- 5.2nC Gate Charge

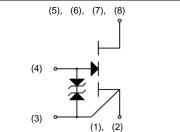
Application

- High switching frequency converter
- · High density converter

Outline



Inner circuit



Source Source Kelvin Source Gate Drain Drain Drain Drain Drain

Packaging specifications

ging opeeniealiene	
Packing	Embossed tape
Reel size (mm)	330
Tape width (mm)	16
Basic ordering unit (pcs)	3500
Taping code	E2
Marking	GNP1070TC
	Packing Reel size (mm) Tape width (mm) Basic ordering unit (pcs) Taping code

●Absolute maximum ratings (T_a = 25°C)

Parameter		Symbol	Value	Unit	
Continuous Drain current	$T_c = 25^{\circ}C$	ı *1	20	А	
Continuous Drain current	$T_c = 125^{\circ}C$	I _D ^{*1}	7.3	Α	
Dulas Drain surrant	$T_c = 25^{\circ}C$	×1*2	66	А	
Pulse Drain current	$T_c = 125^{\circ}C$	D,pulse	24	А	
Drain - Source Voltage		V _{DSS}	650	V	
Transient Drain - Source Voltage		V _{DSS(transient)} *3	750	V	
Gate - Source voltage (DC)		V _{GSS}	-10 to +6	V	
Transient Gate - Source voltage		V _{GSS(transient)} *4	8.5	V	
Power dissipation(Tc=25°C)		P _{tot}	56	W	
Junction temperature		T _j	150	°C	

•Electrical characteristics ($T_a = 25^{\circ}C$)

Doromotor	Symbol	Conditions	Values			Unit	
Parameter	Symbol	ymbol Conditions -		Тур.	Max.	Unit	
Drain - Source breakdown		$V_{GS} = 0V$				V	
voltage	$V_{(BR)DSS}$	T _j = 25°C	650	-	-	V	
		$V_{GS} = 0V, V_{DS} = 650V$					
Zero Gate voltage Drain current	I_{DSS}	T _j = 25°C	-	1.5	150	μA	
		T _j = 150°C	-	90	-		
Gate - Source leakage current	I _{GSS+}	$V_{GS} = 6.0V, VDS = 0V$		0.1	3	mA	
Gate threshold voltage	$V_{GS (th)}$	$V_{DS} = 50 mV, I_{D} = 18 mA$	1	1.45	2.4	V	
		V _{GS} = 5.0V, I _D = 1.9A					
	Rad S	$T_j = 25^{\circ}C$	-	73	103	mΩ	
Static Drain - Source on - state resistance		T _j = 150°C	-	183	-		
		V _{GS} = 5.5V, I _D = 1.9A					
		T _j = 25°C	-	70	98	mΩ	
		T _j = 150°C	-	175	-		
Gate input resistance	R_G	f = 100MHz, open drain	-	0.86	-	Ω	

Thermal resistance

Parameter	Symbol	Values			Unit
Falameter		Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R _{thJA}	-	46.5	-	°C/W
Thermal resistance, junction - case	R _{thJC}	-	2.20	-	°C/W
Reflow soldering temperature	T _{solder} *6	-	-	260	°C

•Electrical characteristics ($T_a = 25^{\circ}C$)

Doromotor	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	200	-	
Output capacitance	C _{oss}	$V_{DS} = 400 V$	-	50	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	0.6	-	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} = 0V V _{DS} = 0V to 400V	-	70	-	pF
Effective output capacitance, time related	C _{o(tr)}	$V_{GS} = 0V$ $V_{DS} = 0V$ to 400V	-	110	-	pF
Output charge	Q _{oss}	$V_{GS} = 0V$ $V_{DS} = 0V$ to 400V	-	44	-	nC
Total Gate charge	Qg ^{*7}	V _{DS} = 400V I _D = 8A	-	5.2	-	
Gate - Source charge	Q _{gs} ^{*7}	$V_{GS} = 6V/0V$	-	0.6	-	nC
Gate - Drain charge	Q _{gd} *7		-	1.2	-	
Gate plateau voltage	V _{plat} *7		-	2.0	-	V
Turn - on delay time	t _{d(on)} *7	V _{DS} = 400V I _D = 8A	-	5.9	-	
Rise time	t _r *7	$V_{GS} = 6V/0V$	-	6.9	-	nc
Turn - off delay time	t _{d(off)} *7	R _G = 10Ω	-	8.0	-	ns
Fall time	t _f *7		-	8.7	-	

GNP1070TC-Z

• Reverse conduction electrical characteristics ($T_a = 25^{\circ}C$)

Parameter	Symbol	Conditions	Values			Unit
Farameler	Symbol Conditions –		Min.	Тур.	Max.	Unit
Source-Drain reverse voltage	V_{SD}	$V_{GS} = 0V, I_{SD} = 1.9A$	-	2.0	-	V
Reverse recovery time	t _{rr} *7		-	0	-	ns
Reverse recovery charge	Q _{rr} *7		-	0	-	nC
Peak reverse recovery current	I _{rrm} *7		-	0	-	А

*1 Limited and calculated by maximum temperature allowed..

*2 V_{GS}=6V,Duty=0.1, t_{pulse} =1µs.

*3 t_{pulse} =1µs, <10 hrs of total time.

*4 t_{pulse<}20ns, <0.5 hr of total time.

*5 Maximum Id applied at FT is 1.9A.

*6 MSL 3.

*7 Pulsed.

•Electrical characteristic curves

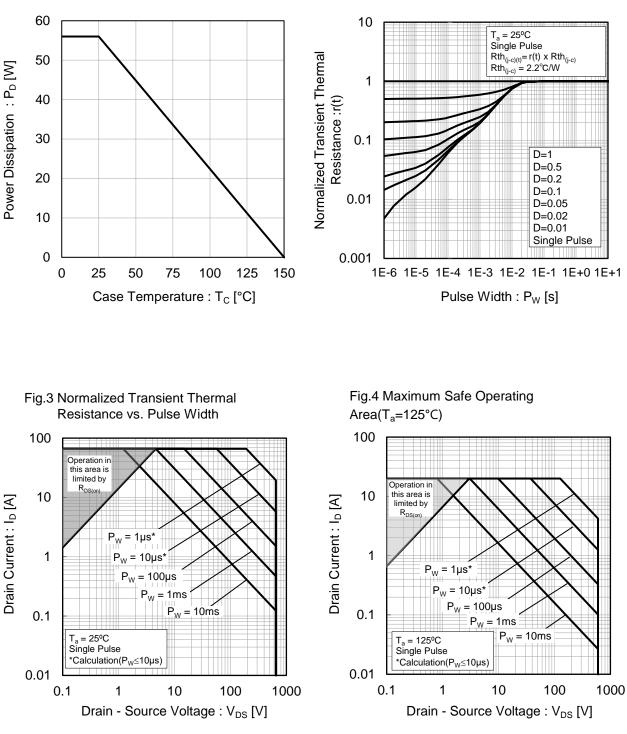


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area(T_a=25°C)

•Electrical characteristic curves

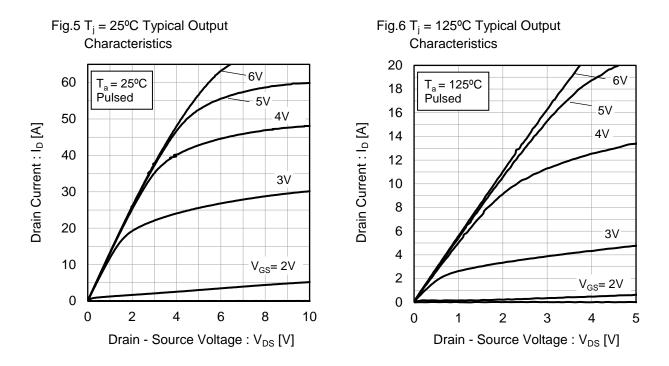
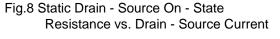
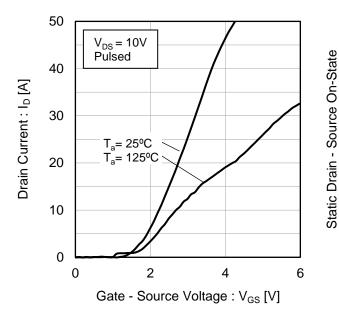
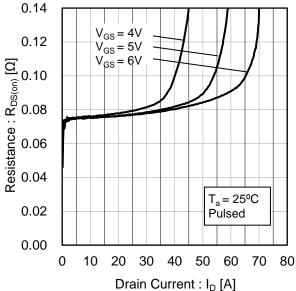


Fig.7 Typical Transfer Characteristics







Electrical characteristic curves

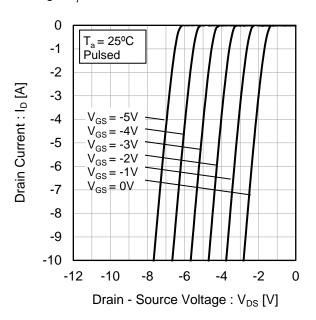
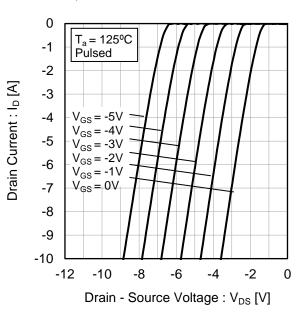
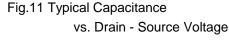


Fig.9 $T_i = 25^{\circ}C$ 3rd Quadrant Characteristics

Fig.10 T_j = 125°C 3rd Quadrant Characteristics





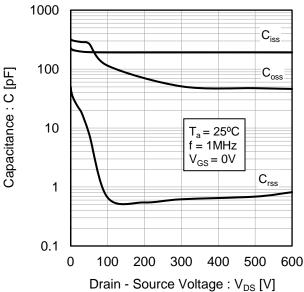
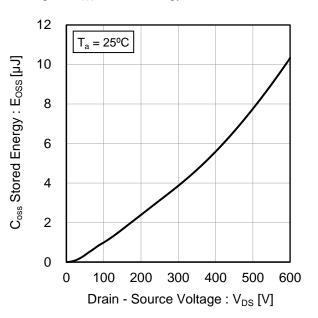


Fig.12 Coss Stored Energy



•Electrical characteristic curves

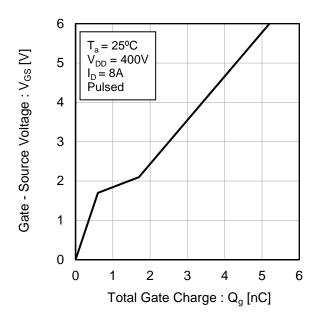
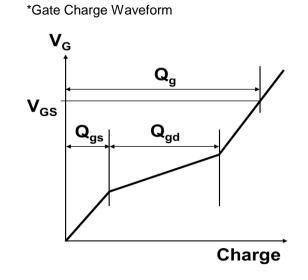


Fig.13 Dynamic Input Characteristics



Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

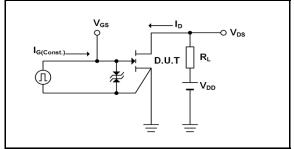


Fig.2-1 Switching Characteristics Measurement Circuit

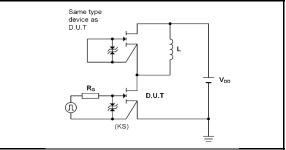


Fig.2-3 Waveforms for Switching Energy Loss

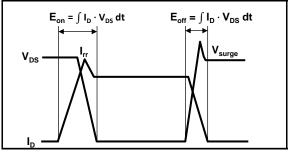
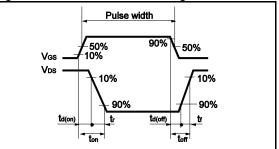


Fig.2-2 Waveforms for Switching Time



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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