

RGCL80TS60GC13

600V 40A Field Stop Trench IGBT

V _{CES}	600V
I _{C(100°C)}	40A
V _{CE(sat) (Typ.)}	1.4V
P _D	148W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Soft Switching

Applications

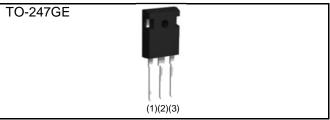
Discharge Circuit

Brake for Inverter

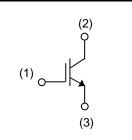
Partial Switching PFC

3) Pb - free Lead Plating ; RoHS Compliant

Outline



Inner Circuit





Packaging Specifications

	Packaging	Tube
	Reel Size (mm)	-
Tupo	Tape Width (mm)	-
Туре	Basic Ordering Unit (pcs)	600
	Taping Code	C13
	Marking	RGCL80TS60

●Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	600	V
Gate - Emitter Voltage		V _{GES}	±30	V
Collector Current	$T_{\rm C} = 25^{\circ}{\rm C}$	۱ _C	65	А
Collector Current	$T_{\rm C} = 100^{\circ}{\rm C}$	Ι _C	40	А
Pulsed Collector Current		I _{CP} ^{*1}	160	А
Power Discinction	$T_{\rm C} = 25^{\circ}{\rm C}$	P _D	148	W
Power Dissipation	$T_{\rm C} = 100^{\circ}{\rm C}$	P _D	74	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T _{stg}	–55 to +175	°C

*1 Pulse width limited by T_{jmax.}

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•Thermal Resistance

Parameter	Symbol	Values		Unit	
Faranielei	Symbol	Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	1.01	°C/W

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit	
Farameter			Min.	Тур.	Max.	Unit	
Collector - Emitter Breakdown Voltage	BV _{CES}	I _C = 10μΑ, V _{GE} = 0V	600	-	-	V	
Collector Cut - off Current	I _{CES}	V _{CE} = 600V, V _{GE} = 0V	-	-	10	μA	
Gate - Emitter Leakage Current	I _{GES}	V_{GE} = ±30V, V_{CE} = 0V	-	-	±200	nA	
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	V _{CE} = 5V, I _C = 30.0mA	4.5	5.5	6.5	V	
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 40A, V_{GE} = 15V$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.4 1.6	1.8 -	V	



•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Devenueter	Symbol	Operativise					
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Input Capacitance	C _{ies}	V _{CE} = 30V	-	2340	-		
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	55	-	pF	
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	43	-		
Total Gate Charge	Q_g	V _{CE} = 300V	-	98	-		
Gate - Emitter Charge	Q_{ge}	I _C = 40A	-	20	-	nC	
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	38	-		
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 40$ A, $V_{\rm CC} = 400$ V	-	53	-		
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	34	-	20	
Turn - off Delay Time	t _{d(off)}	$T_j = 25^{\circ}C$	-	227	-	ns	
Fall Time	t _f	Inductive Load	-	204	-		
Turn - on Switching Loss	E_{on}	*Eon includes diode	-	1.11	-	~ l	
Turn - off Switching Loss	E_{off}	reverse recovery	-	1.68	-	mJ	
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 40$ A, $V_{\rm CC} = 400$ V	-	48	-		
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	66	-	20	
Turn - off Delay Time	t _{d(off)}	T _j = 175°C	-	255	-	ns	
Fall Time	t _f	Inductive Load	-	310	-		
Turn - on Switching Loss	E_{on}	*Eon includes diode	-	1.51	-		
Turn - off Switching Loss	E _{off}	reverse recovery	-	2.30	-	mJ	
		$I_{\rm C} = 160 {\rm A}, V_{\rm CC} = 480 {\rm V}$					
Reverse Bias Safe Operating Area	RBSOA	$V_{P} = 600V, V_{GE} = 15V$	FU	LL SQUA	RE	-	
		$R_{G} = 60\Omega, T_{j} = 175^{\circ}C$					

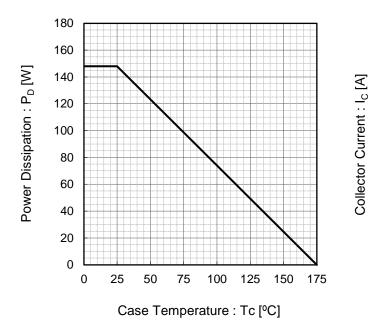


Fig.1 Power Dissipation vs. Case Temperature

Fig.2 Collector Current vs. Case Temperature

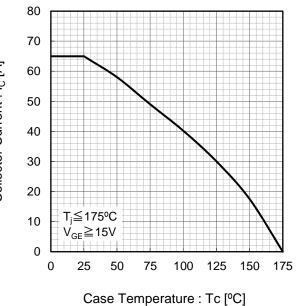
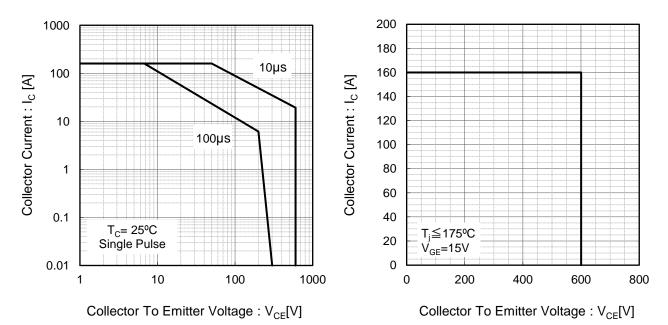


Fig.3 Forward Bias Safe Operating Area

Fig.4 Reverse Bias Safe Operating Area



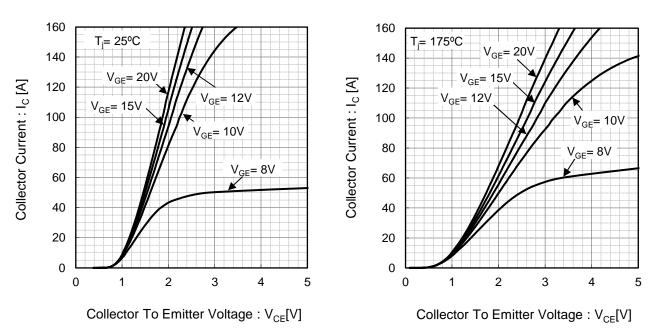


Fig.5 Typical Output Characteristics

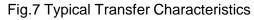
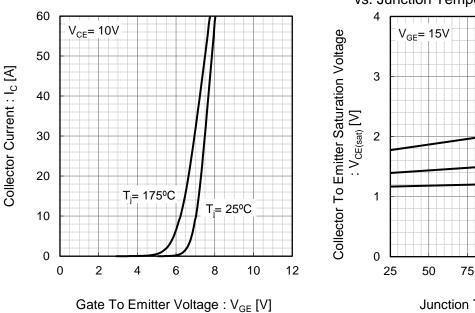


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

Fig.6 Typical Output Characteristics



Junction Temperature : T_j [°C]

100

125

I_C= 80A

 $I_C = 40A$

 $I_{C} = 20A$

150

175

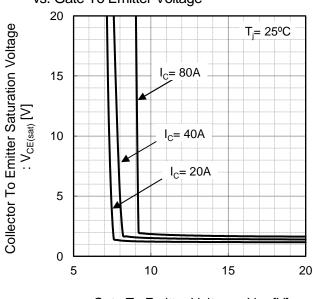


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

Gate To Emitter Voltage : V_{GE} [V]

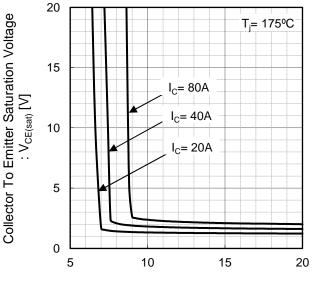
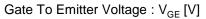


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



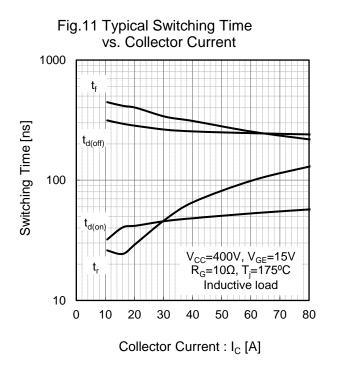


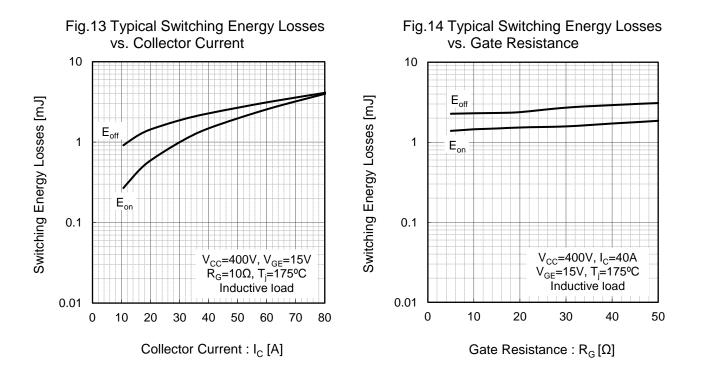
Fig.12 Typical Switching Time vs. Gate Resistance 1000 t_{d(off)} 100 t. t_{d(on)} V_{CC}=400V, I_C=40A V_{GE}=15V, T_j=175⁰C Inductive load 10 10 0 20 30 40 50

Switching Time [ns]

6/9

Gate Resistance : $R_G[\Omega]$

ROHM



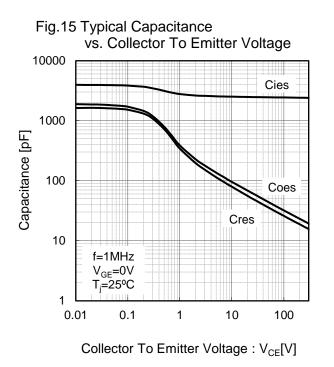
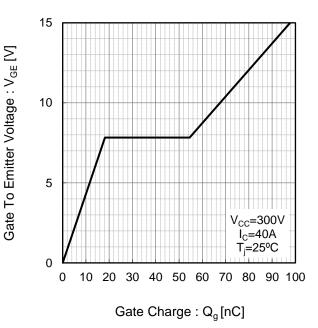


Fig.16 Typical Gate Charge



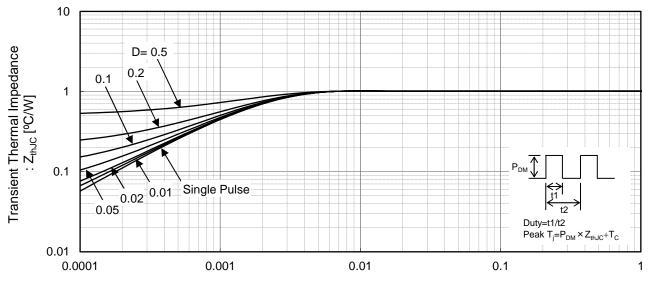


Fig.17 IGBT Transient Thermal Impedance

Pulse Width : t1[s]



●Inductive Load Switching Circuit and Waveform

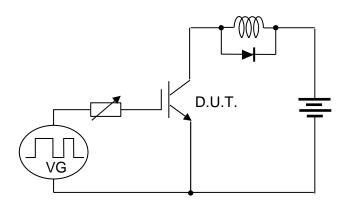


Fig.18 Inductive Load Circuit

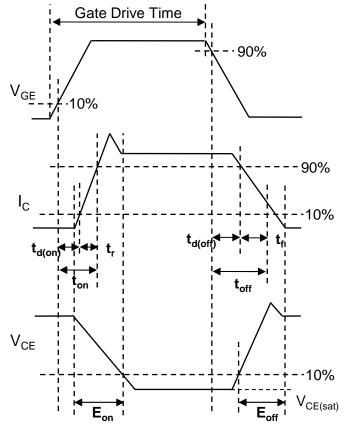


Fig.19 Inductive Load Waveform



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