

V_{CES}	1200V
I_C	57A
$V_{CE(sat)}$ (Typ.)	1.75V
P_D	340W

●Features

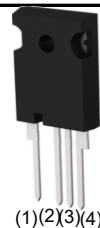
- 1) Low Collector - Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Pb - free Lead Plating ; RoHS Compliant

●Application

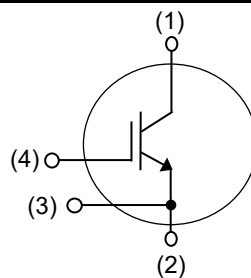
PV Inverter
UPS
PFC
Welding

●Outline

TO-247-4L



●Inner Circuit



- (1) Collector
- (2) Power Emitter
- (3) Kelvin Emitter
- (4) Gate

●Packaging Specifications

Type	Packaging	Tube
	Reel Size (mm)	-
	Tape Width (mm)	-
	Basic Ordering Unit (pcs)	450
	Packing Code	C15
	Marking	RGH00TRX2

●Absolute Maximum Ratings (at $T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V_{CES}	1200	V
Gate - Emitter Voltage		V_{GES}	± 30	V
Collector Current	$T_C = 25^\circ\text{C}$	I_C	92	A
	$T_C = 100^\circ\text{C}$	I_C	57	A
Pulsed Collector Current		I_{CP}^{*1}	200	A
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	340	W
	$T_C = 100^\circ\text{C}$	P_D	170	W
Operating Junction Temperature		T_J	-40 to +175	$^\circ\text{C}$
Storage Temperature		T_{stg}	-55 to +175	$^\circ\text{C}$

*1 Pulse width limited by T_{jmax} .

● Thermal Resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.44	°C/W

● IGBT Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Collector - Emitter Breakdown Voltage	BV_{CES}	$I_C = 10\mu\text{A}$, $V_{GE} = 0\text{V}$	1200	-	-	V
Collector Cut - off Current	I_{CES}	$V_{CE} = 1200\text{V}$, $V_{GE} = 0\text{V}$	-	-	10	μA
Gate - Emitter Leakage Current	I_{GES}	$V_{GE} = \pm 30\text{V}$, $V_{CE} = 0\text{V}$	-	-	± 500	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5\text{V}$, $I_C = 4.7\text{mA}$	4.5	5.5	6.5	V
Collector - Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 50\text{A}$, $V_{GE} = 15\text{V}$, $T_j = 25^\circ\text{C}$	-	1.75	2.15	V
		$T_j = 175^\circ\text{C}$	-	2.45	-	

●IGBT Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	$V_{CE} = 30\text{V}$	-	4373	-	pF
Output Capacitance	C_{oes}	$V_{GE} = 0\text{V}$	-	100	-	
Reverse transfer Capacitance	C_{res}	$f = 1\text{MHz}$	-	36	-	
Total Gate Charge	Q_g	$V_{CE} = 600\text{V}$	-	162	-	nC
Gate - Emitter Charge	Q_{ge}	$I_C = 50\text{A}$	-	27	-	
Gate - Collector Charge	Q_{gc}	$V_{GE} = 15\text{V}$	-	61	-	
Turn - on Delay Time	$t_{d(on)}$	$I_C = 50\text{A}, V_{CC} = 600\text{V},$ $V_{GE} = 15\text{V}, R_G = 10\Omega,$ $T_j = 25^\circ\text{C}$ Inductive Load	-	52	-	ns
Rise Time	t_r		-	17	-	
Turn - off Delay Time	$t_{d(off)}$		-	231	-	
Fall Time	t_f		-	121	-	
Turn-on Switching Loss	E_{on}		-	0.31	-	mJ
Turn-off Switching Loss	E_{off}		-	1.80	-	
Turn - on Delay Time	$t_{d(on)}$	$I_C = 50\text{A}, V_{CC} = 600\text{V},$ $V_{GE} = 15\text{V}, R_G = 10\Omega,$ $T_j = 175^\circ\text{C}$ Inductive Load	-	52	-	ns
Rise Time	t_r		-	21	-	
Turn - off Delay Time	$t_{d(off)}$		-	264	-	
Fall Time	t_f		-	148	-	
Turn-on Switching Loss	E_{on}		-	0.44	-	mJ
Turn-off Switching Loss	E_{off}		-	2.36	-	
Reverse Bias Safe Operating Area	$RBSOA^{*2}$	$I_C = 200\text{A}, V_{CC} < 800\text{V},$ $V_p < 1200\text{V}, V_{GE} = 15\text{V},$ $t_p < 1\mu\text{s}, T_j = 175^\circ\text{C}$	FULL SQUARE			-

*2 Design assurance without measurement

●Electrical Characteristic Curves

Fig.1 Power Dissipation
vs. Case Temperature

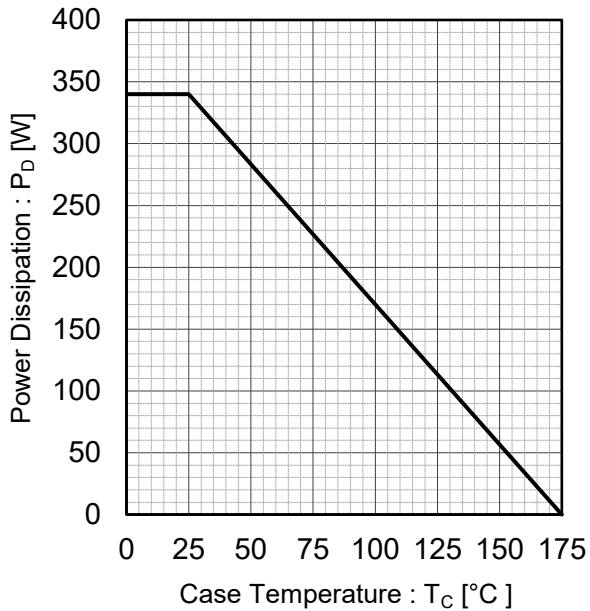


Fig.2 Collector Current
vs. Case Temperature

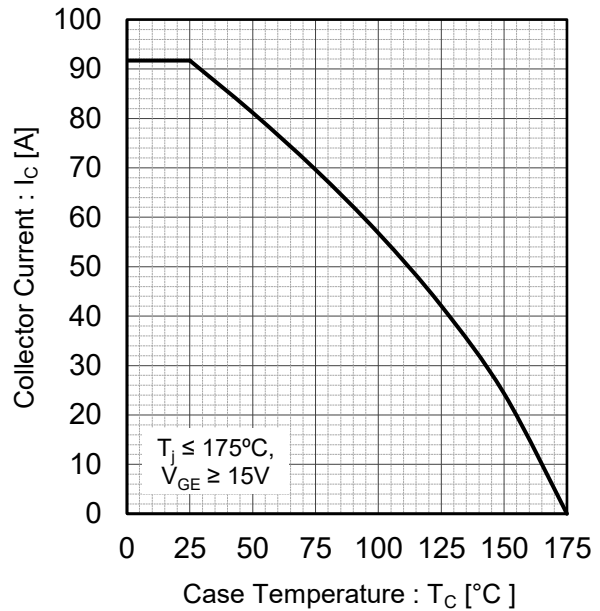


Fig.3 Forward Bias Safe Operating Area

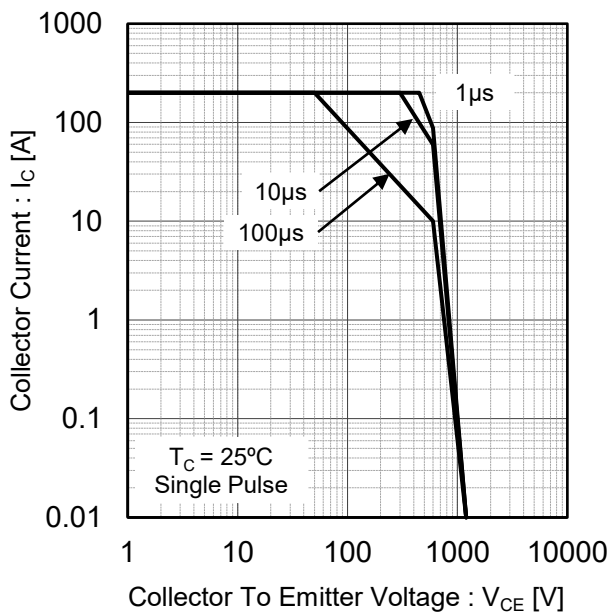
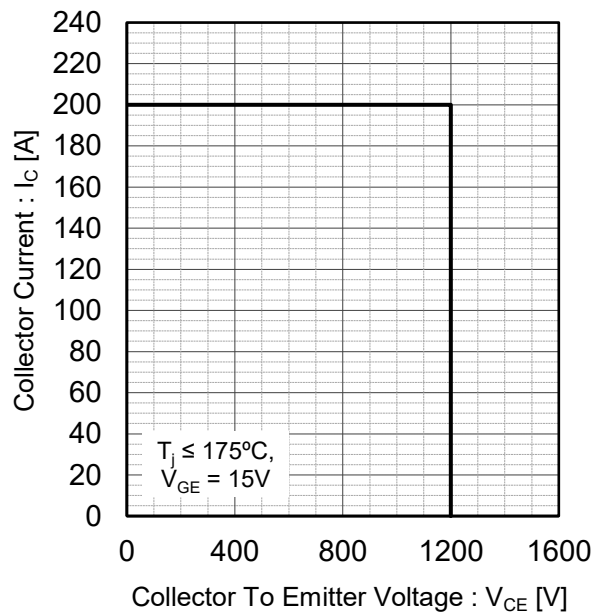


Fig.4 Reverse Bias Safe Operating Area



●Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

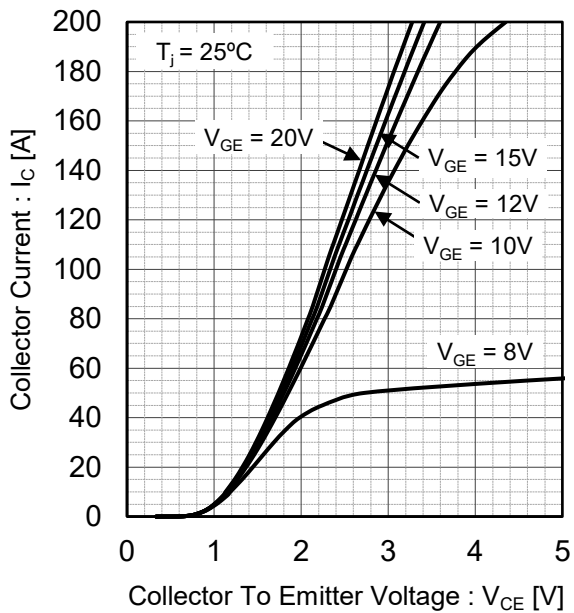


Fig.6 Typical Output Characteristics

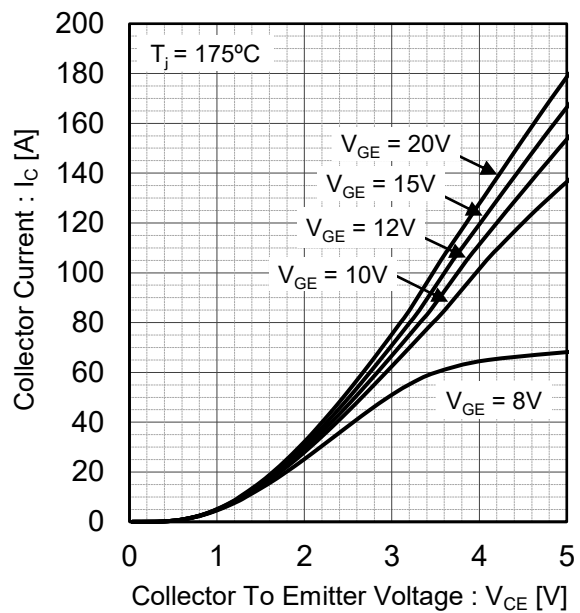


Fig.7 Typical Transfer Characteristics

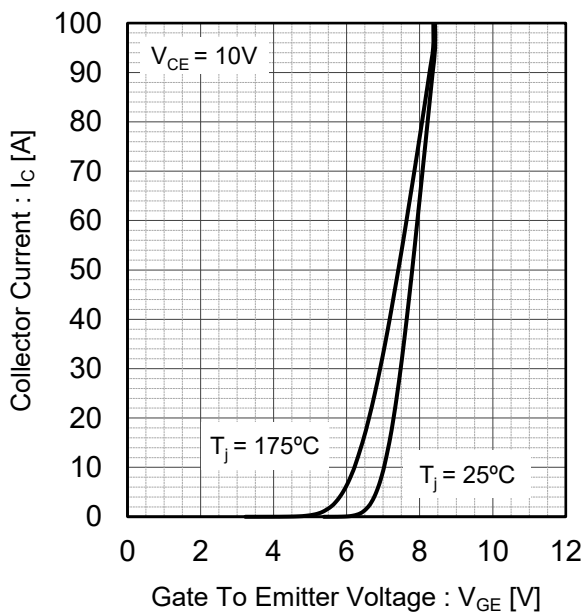
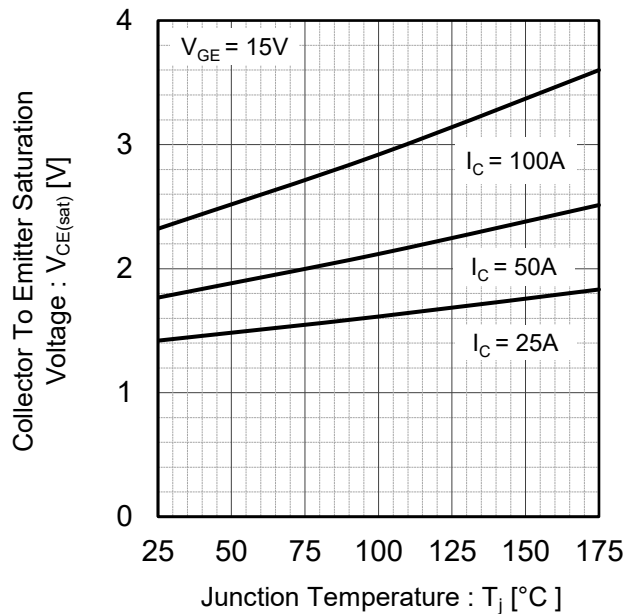


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



●Electrical Characteristic Curves

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

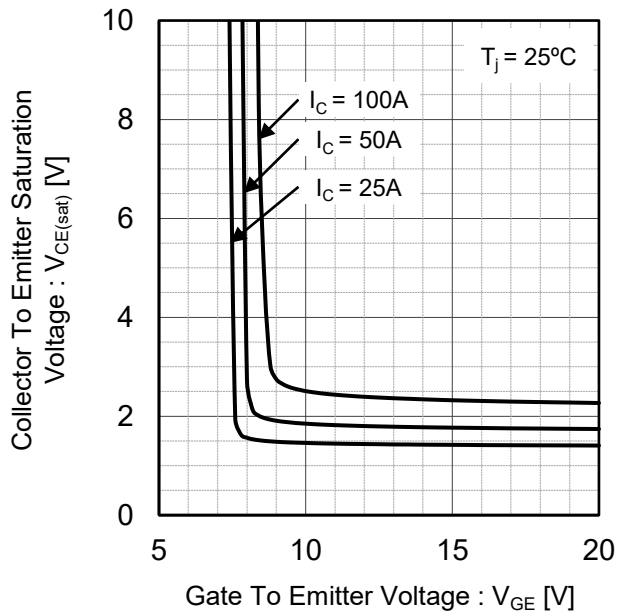


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

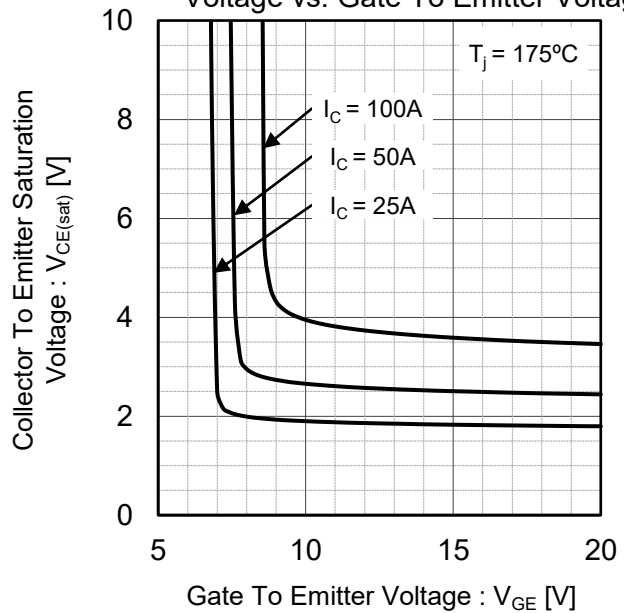


Fig.11 Typical Capacitance vs. Collector To Emitter Voltage

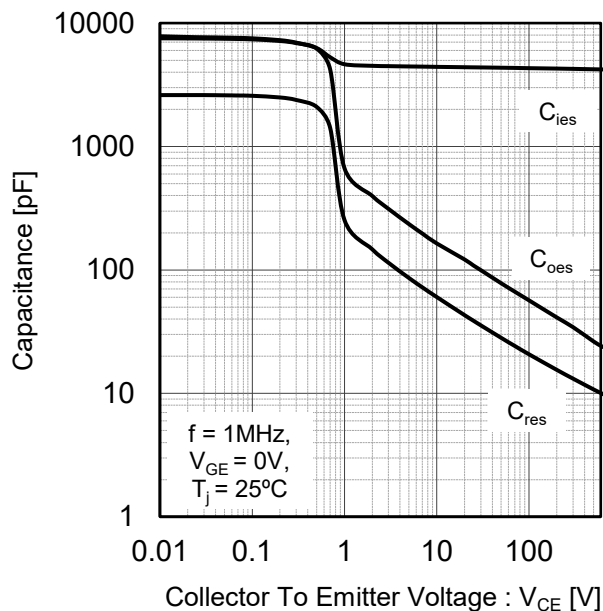
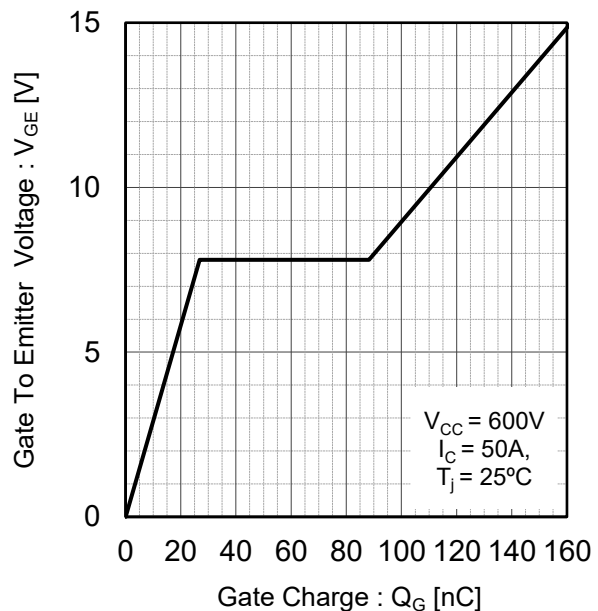


Fig.12 Typical Gate Charge



●Electrical Characteristic Curves

Fig.13 Typical Switching Time
vs. Collector Current

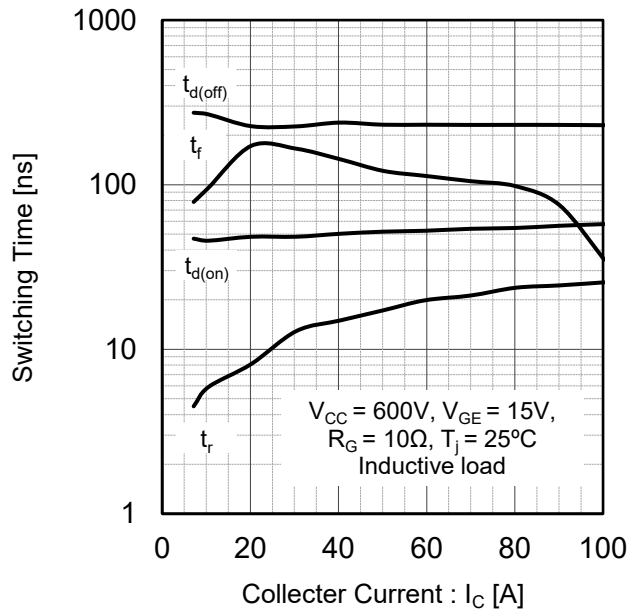


Fig.14 Typical Switching Time
vs. Gate Resistance

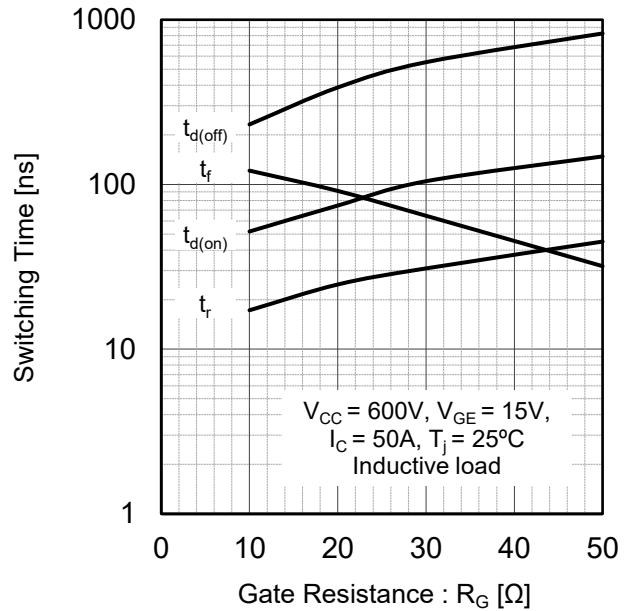


Fig.15 Typical Switching Energy Losses
vs. Collector Current

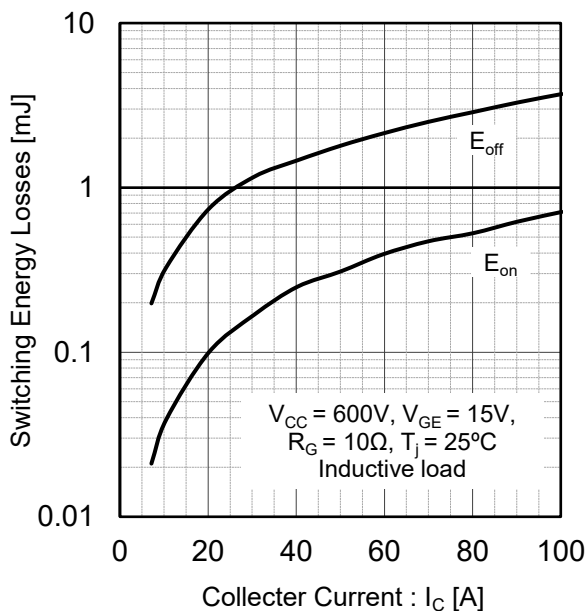
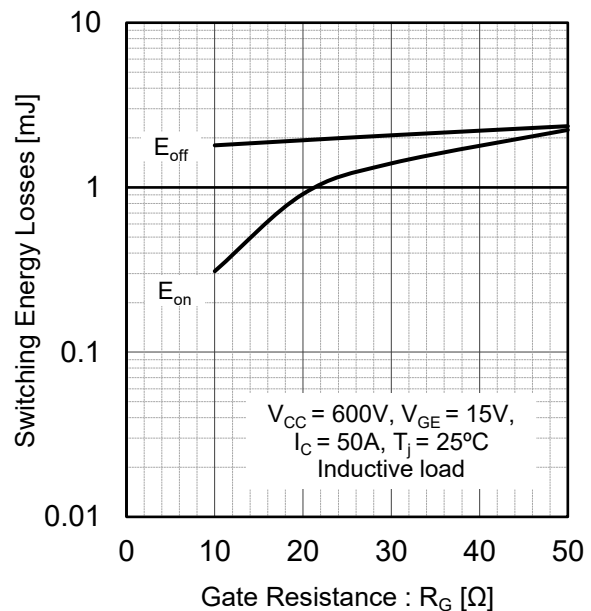


Fig.16 Typical Switching Energy Losses
vs. Gate Resistance



●Electrical Characteristic Curves

Fig.17 Typical Switching Time
vs. Collector Current

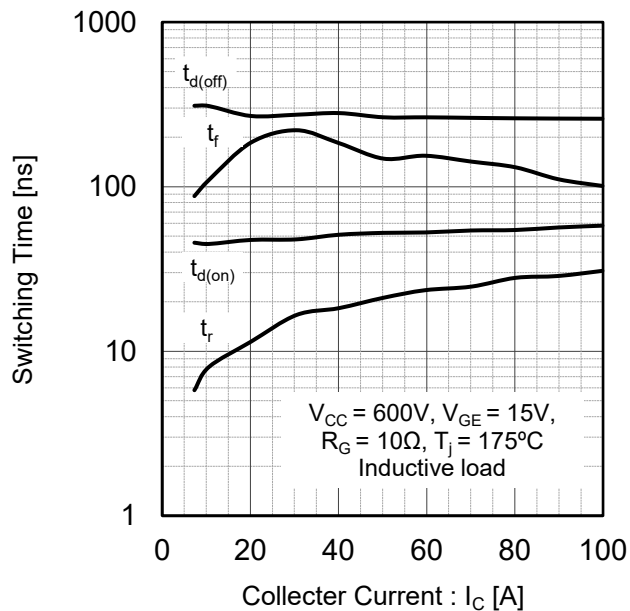


Fig.18 Typical Switching Time
vs. Gate Resistance

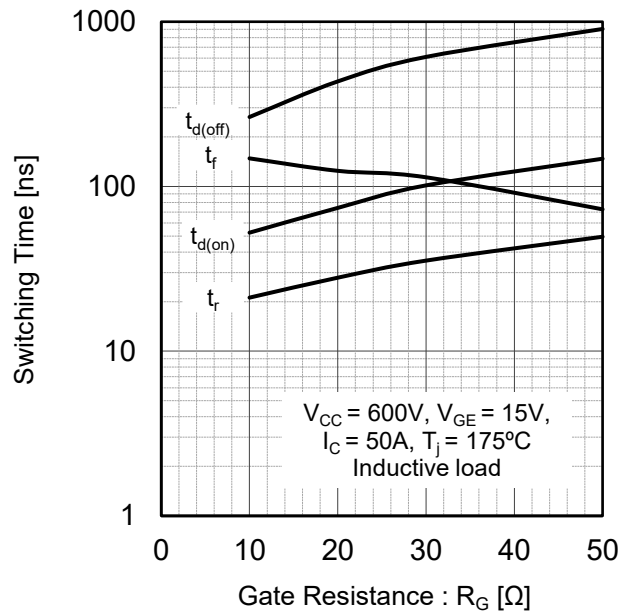


Fig.19 Typical Switching Energy Losses
vs. Collector Current

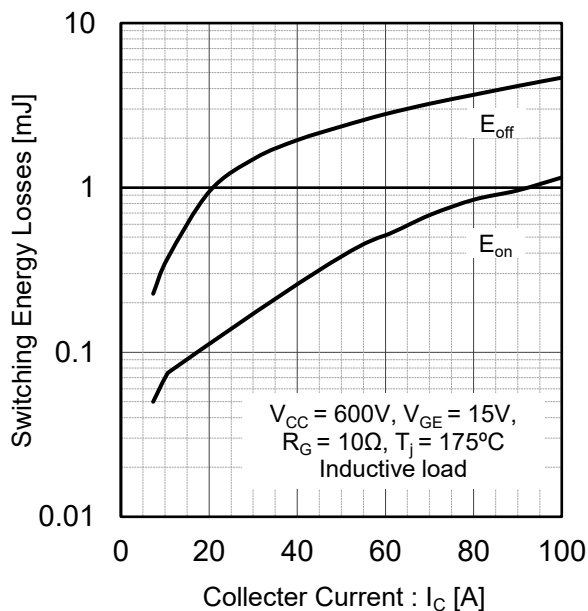
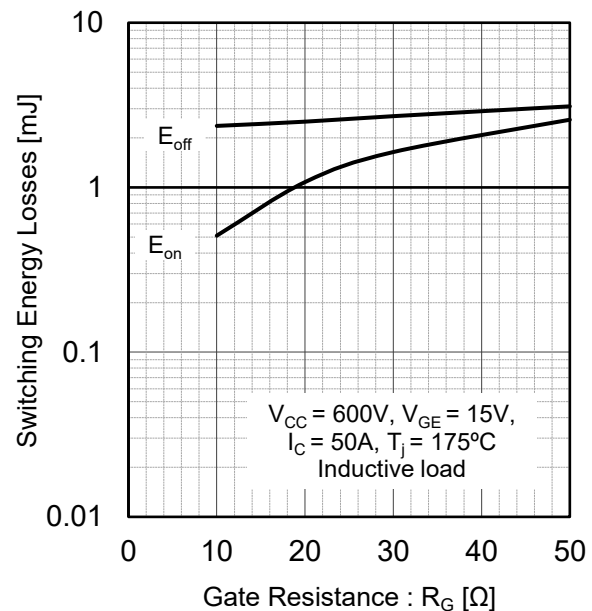
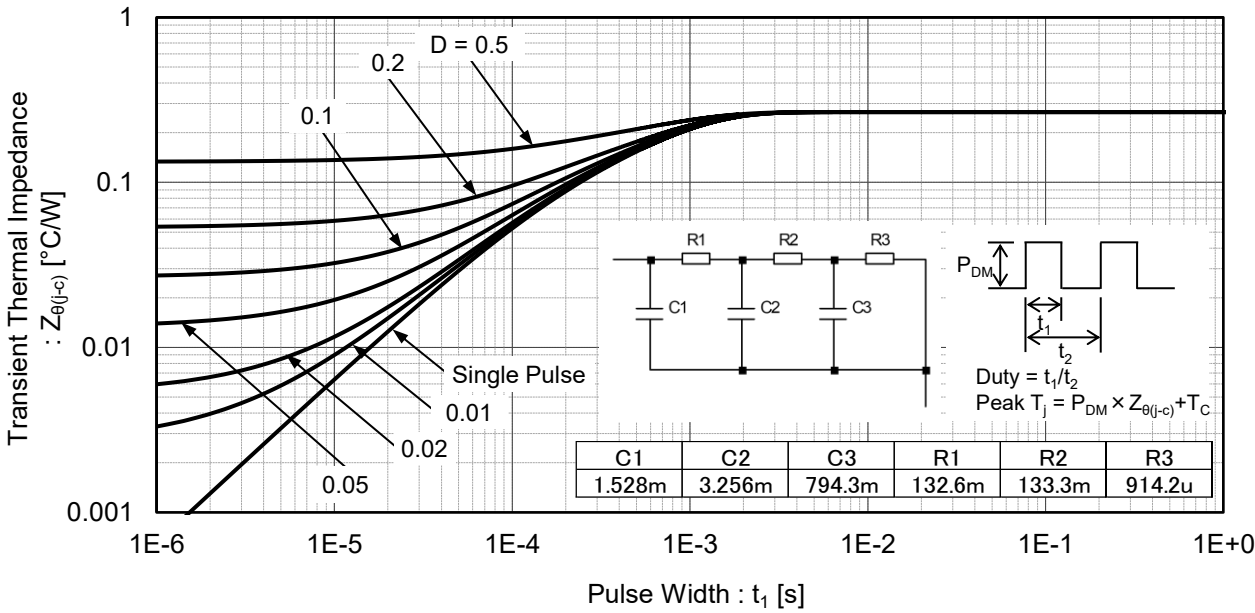


Fig.20 Typical Switching Energy Losses
vs. Gate Resistance



●Electrical Characteristic Curves

Fig.21 Typical IGBT Transient Thermal Impedance



● Inductive Load Switching Circuit and Waveform

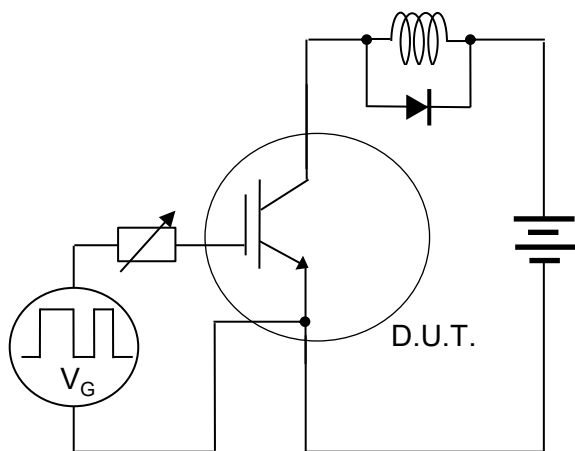


Fig.22 Inductive Load Circuit

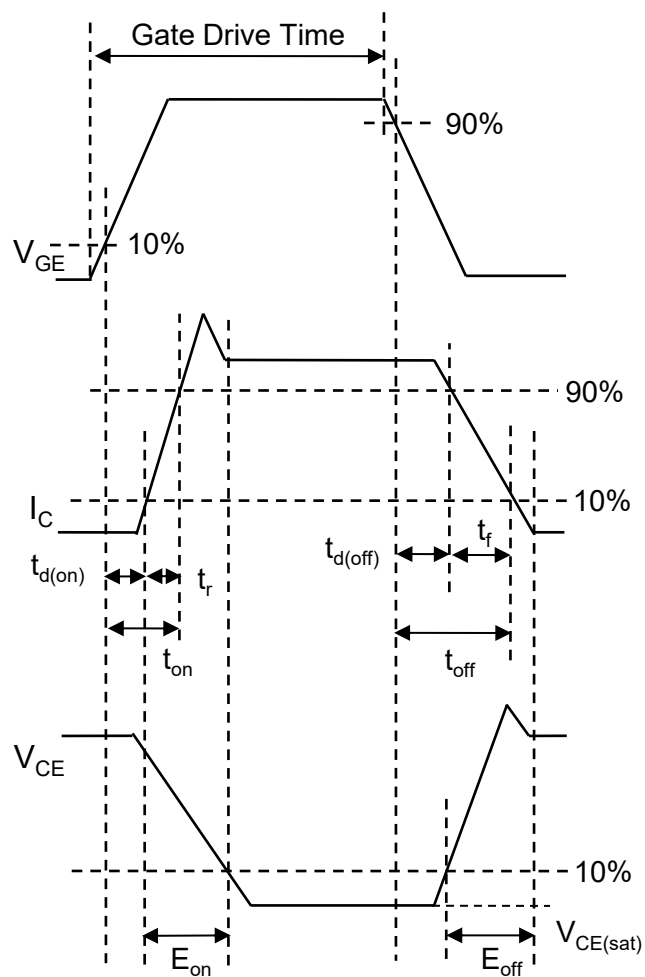


Fig.23 Inductive Load Waveform

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