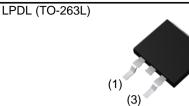


RGPR20NL43HR

430V 20A Ignition IGBT

430±30V
20A
1.6V
250mJ

Outline



Datasheet

Inner circuit



- 1) Low Collector Emitter Saturation Voltage
- 2) High Self-Clamped Inductive Switching Energy
- 3) Built in Gate-Emitter Protection Diode
- 4) Built in Gate-Emitter Resistance
- 5) Qualified to AEC-Q101
- 6) Pb free Lead Plating ; RoHS Compliant

Application

- Ignition Coil Driver Circuits
- Solenoid Driver Circuits

(1) Gate(2) Collector(3) Emitter

(2)

Packaging specifications

	Packing	Taping
	Reel size (mm)	330
Typo	Tape width (mm)	24
Туре	Basic ordering unit (pcs)	1,000
	Taping code	TL
	Marking	RGPR20NL43

•Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

	,				
Parameter		Symbol	Value	Unit	
Collector - Emitter Voltage		V _{CES}	460	V	
Emitter-Collector Voltage ($V_{GE} = 0$ V	′)	V _{EC}	25	V	
Gate - Emitter Voltage		V _{GE}	±10	V	
Collector Current		Ι _C	20	Α	
$T_j = 25^{\circ}C$		E _{AS}	250	mJ	
Avalanche Energy (Single Pulse) $T_j = 150^{\circ}C$		E _{AS} ^{*2}	150	mJ	
Power Dissipation		P _D	107	W	
Operating Junction Temperature		T _j	-40 to +175	°C	
Storage Temperature		T _{stg}	-55 to +175	°C	

RGPR20NL43HR

•Thermal resistance

Parameter	Symbol	Values			Unit
Falanielei	Symbol	Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	0.80	°C/W

•Electrical characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deremeter	Symbol			L Locit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
		$I_{C} = 2mA, V_{GE} = 0V,$				
Collector - Emitter Breakdown Voltage	BV_{CES}	T _j = 25°C	400	430	460	V
· onago		$T_j = -40$ to $175^{\circ}C^{*2}$	395	-	465	
Gate - Emitter Breakdown Voltage	BV _{EC}	I _C = -10mA, V _{GE} = 0V	25	35	-	V
Gate - Emitter Breakdown Voltage	BV _{GES}	$I_G = \pm 5$ mA, $V_{CE} = 0$ V	±12	-	±17	V
		$V_{CE} = 300V, V_{GE} = 0V,$				
Collector Cut - off Current	I _{CES}	T _j = 25°C	-	-	7	μA
		$T_{j} = 150^{\circ}C^{*2}$	-	-	100	μA
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 10V, V_{CE} = 0V$	±0.4	±0.6	±1.2	mA
		$V_{CE} = 5V, I_{C} = 10mA,$				
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	T _j = 25°C	1.3	1.7	2.1	V
		$T_{j} = 150^{\circ}C^{*2}$	-	1.3	-	V
		$I_{C} = 10A, V_{GE} = 5V,$				
Collector - Emitter Saturation Voltage	V _{CE(sat)}	T _j = 25°C	-	1.60	2.00	V
		$T_{j} = 150^{\circ}C^{*2}$	-	1.80	-	V
		$I_{C} = 4A, V_{GE} = 4.5V,$				
Collector - Emitter Saturation Voltage	V _{CE(sat)}	T _j = 25°C	-	1.17	1.50	V
		$T_j = 150^{\circ}C^{*2}$	-	1.13	-	V
Collector Freitten Octuration		$I_{C} = 10A, V_{GE} = 4V,$				
Collector - Emitter Saturation Voltage	V _{CE(sat)}	T _j = 25°C	-	1.70	2.10	V
-		$T_{j} = 150^{\circ}C^{*2}$	-	1.90	-	V

•Electrical characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Devenueter	Current el	Conditions		Values		l la it		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit		
Input Capacitance	C _{ies}	V _{CF} = 10V,	-	1000	-			
Output Capacitance	C _{oes}	$V_{GE} = 0V,$	-	175	-	pF		
Reverse transfer Capacitance	C _{res}	f = 1MHz	-	55	-	1		
Total Gate Charge	Qg	$V_{CE} = 12V, I_C = 10A,$ $V_{GE} = 5V$	-	14	-	nC		
Turn - on Delay Time ^{*1,*2}	t _{d(on)}		0.09	0.17	0.50			
Rise Time ^{*1,*2}	t _r	$I_{C} = 8A, V_{CC} = 300V,$ $V_{GE} = 5V, R_{G} = 100\Omega,$	0.10	0.18	0.50			
Turn - off Delay Time ^{*1,*2}	t _{d(off)}	$L = 5mH, T_i = 25^{\circ}C$	0.8	1.3	4.0	μs		
Fall Time ^{*1,*2}	t _f	,	1.4	2.4	6.0			
Turn - on Delay Time ^{*1}	t _{d(on)}		-	0.16	-			
Rise Time ^{*1}	t _r	$I_{C} = 8A, V_{CC} = 300V,$ $V_{GE} = 5V, R_{G} = 100\Omega,$	-	0.23	-	116		
Turn - off Delay Time ^{*1}	t _{d(off)}	$L = 5mH, T_i = 150°C$	-	1.5	-	μs		
Fall Time ^{*1}	t _f		-	3.9	-			
Avalanche Energy	-	L = 5mH, V_{GE} = 5V, V_{CC} = 30V, R_G = 1k Ω ,						
(Single Pulse)	E _{AS}	T _j = 25°C	250	-	-	mJ		
		$T_{j} = 150^{\circ}C^{*2}$	150	-	-	mJ		
Gate Series Resistance	R _G		70	100	130	Ω		
Gate - Emitter Resistance	R _{GE}		8	16	24	kΩ		

*1) Assurance items according to our measurement definition (Fig.18)

*2) Design assurance items

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Fig.1 Typical Output Characteristics

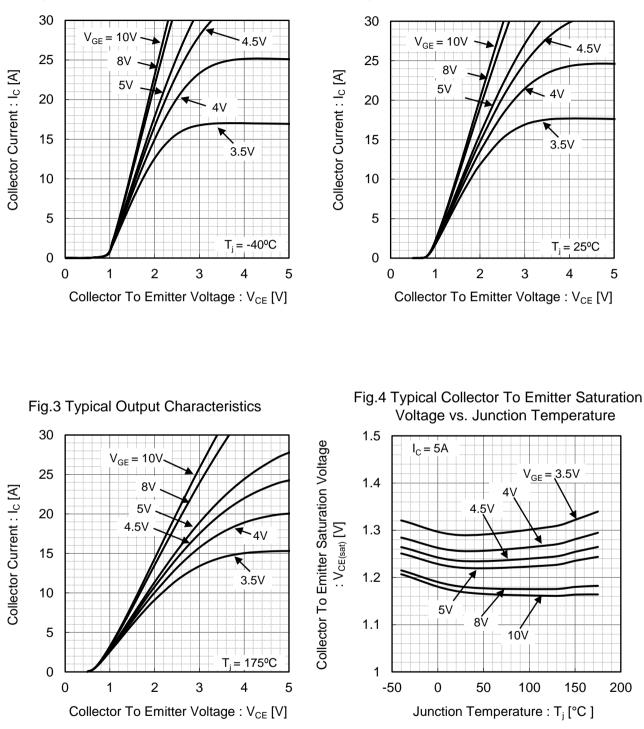
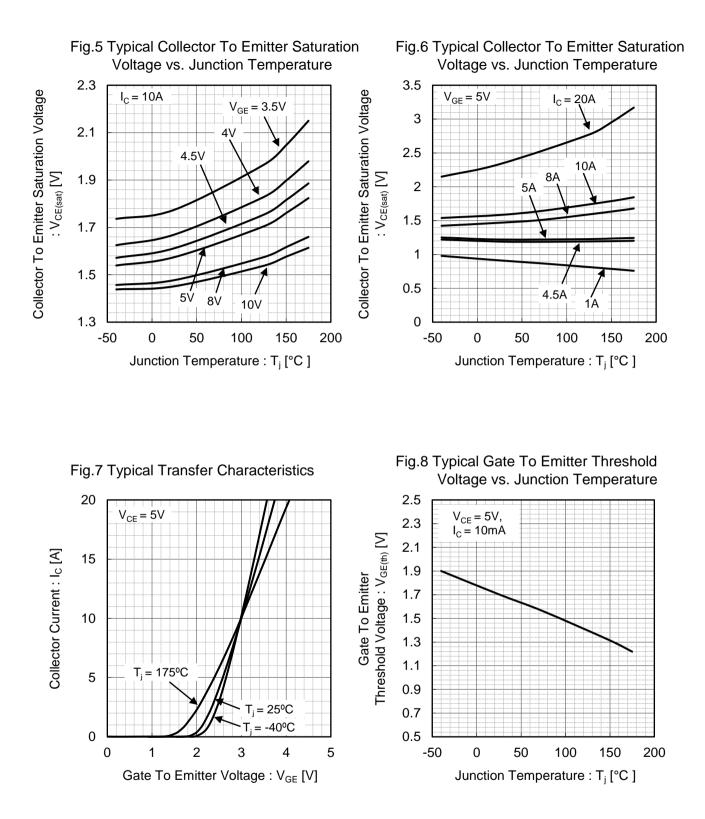
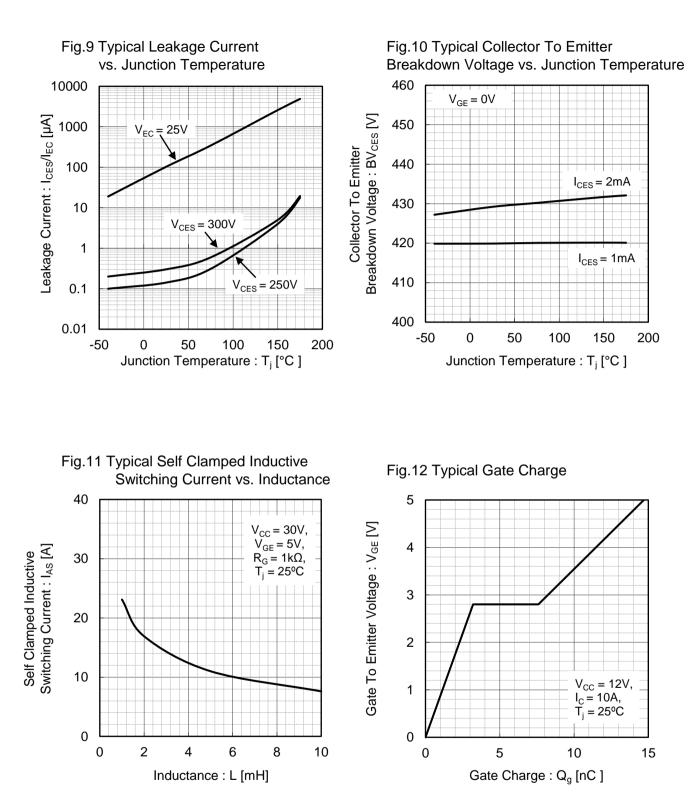


Fig.2 Typical Output Characteristics





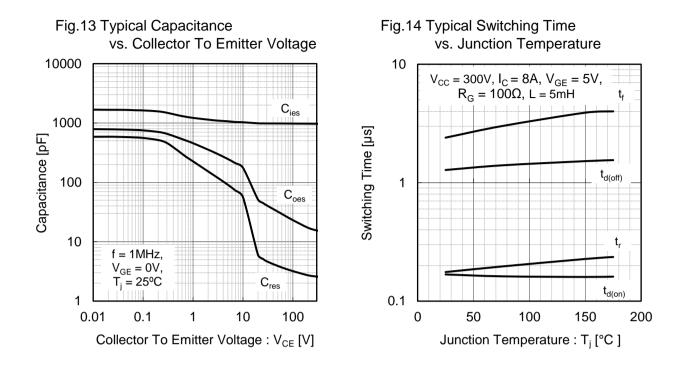
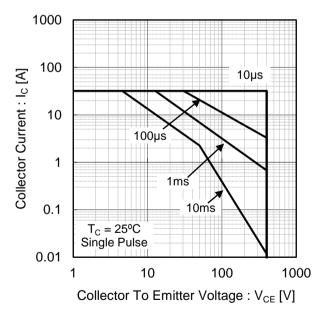
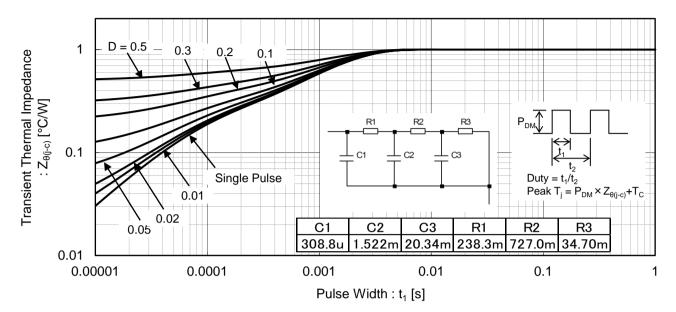


Fig.15 Forward Bias Safe Operating Area











•Inducitve Load Switching Circuit and Waveform

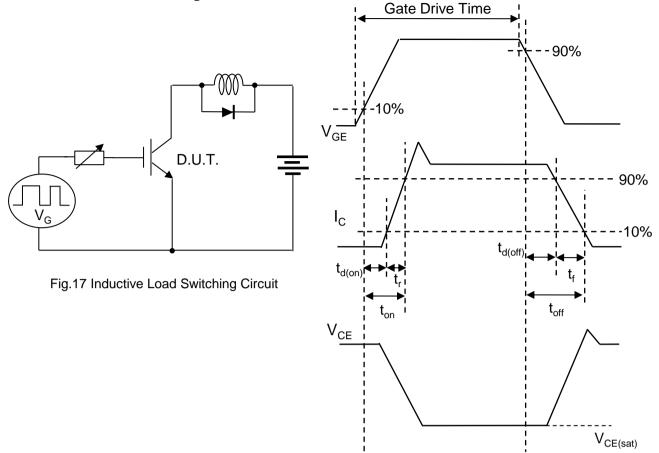


Fig.18 Inductive Load Switching Waveform

•Self Clamped Inductive Switching Circuit and Waveform

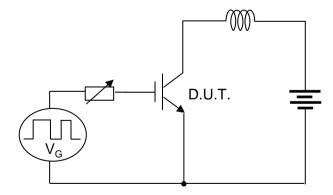


Fig.19 Self Clamped Inductive Switching Circuit

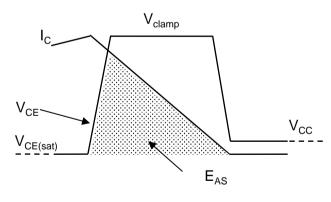


Fig.20 Self Clamped Inductive Switching Waveform

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