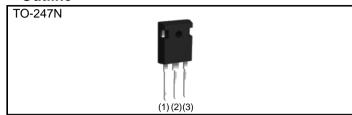
Datasheet

1200V 15A Field Stop Trench IGBT

V _{CES}	1200V
I _{C (100°C)}	15A
V _{CE(sat) (Typ.)}	1.7V
P_{D}	267W

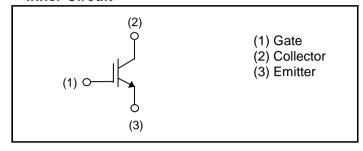
Outline



Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Short Circuit Withstand Time 10µs
- 3) Pb free Lead Plating; RoHS Compliant

●Inner Circuit



Packaging Specifications

or ackaging openioations					
	Packaging	Tube			
	Reel Size (mm)	-			
Tuno	Tape Width (mm)	-			
Туре	Basic Ordering Unit (pcs)	450			
	Packing Code	C11			
	Marking	RGS30TSX2			

Application

PFC

UPS

ΙH

Power Conditioner

● **Absolute Maximum Ratings** (at T_C = 25°C unless otherwise specified)

	. 0	1 /		
Parameter		Symbol	Value	Unit
Collector - Emitter Voltage Gate - Emitter Voltage		V _{CES}	1200	V
		V_{GES}	±30	
Collector Current	T _C = 25°C	I _C	30	А
	T _C = 100°C	I _C	15	Α
Pulsed Collector Current		I _{CP} *1	45	Α
Power Dissipation	T _C = 25°C	P _D	267	W
	T _C = 100°C	P _D	133	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{imax.}

●Thermal Resistance

Parameter	Symbol	Values			Unit
raidilletei	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	ı	0.56	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Unit		
- Farameter	Symbol		Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	1200	-	-	V
		V _{CE} = 1200V, V _{GE} = 0V				
Collector Cut - off Current	I _{CES}	$T_j = 25^{\circ}C$ $T_j = 175^{\circ}C^{*2}$	-	-	10	μΑ
		$T_j = 175^{\circ}C^{*2}$	-	1	ı	mA
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	ı	±500	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 2.3mA$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage		$I_C = 15A, V_{GE} = 15V$				
	V _{CE(sat)}	T _j = 25°C	-	1.70	2.10	V
		T _j = 175°C	-	2.20	-	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Davamatas	Symbol	Conditions		Linit		
Parameter			Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	V _{CE} = 30V	-	1272	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	66	-	pF
Reverse transfer Capacitance	C _{res}	f = 1MHz	-	7.6	-	
Total Gate Charge	Q_g	V _{CE} = 500V	-	41	-	
Gate - Emitter Charge	Q_{ge}	I _C = 15A	-	11	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	17	-	
Turn - on Delay Time	t _{d(on)}		-	30	-	
Rise Time	t _r	$I_C = 15A, V_{CC} = 600V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	8.5	-	20
Turn - off Delay Time	t _{d(off)}	$T_i = 25^{\circ}C$	-	70	-	ns
Fall Time	t _f	Inductive Load	-	128	-	
Turn-on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.74	-	mJ
Turn-off Switching Loss	E _{off}	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	-	0.6	-	
Turn - on Delay Time	t _{d(on)}		-	29	-	ns
Rise Time	t _r	$I_C = 15A, V_{CC} = 600V,$ $V_{GE} = 15V, R_G = 10\Omega,$	-	10	-	
Turn - off Delay Time	t _{d(off)}	$T_i = 175^{\circ}C$	-	69	-	
Fall Time	t _f	Inductive Load	-	120	-	
Turn-on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.81	-	m l
Turn-off Switching Loss	E _{off}	Tovoldo Todovoly	-	0.65	-	mJ
Reverse Bias Safe Operating Area	RBSOA	$I_C = 45A$, $V_{CC} = 1050V$ $V_p = 1200V$, $V_{GE} = 15V$ $R_G = 50\Omega$, $T_j = 175^{\circ}C$	FULL SQUARE		-	
Short Circuit Withstand Time	t _{sc}	$V_{CC} \le 600V$ $V_{GE} = 15V, T_j = 25^{\circ}C$	10	-	-	μs
Short Circuit Withstand Time	t _{sc} *2	$V_{CC} \le 600V$ $V_{GE} = 15V, T_j = 150^{\circ}C$	8	-	-	μs

^{*2} Design assurance without measurement

Electrical Characteristic Curves

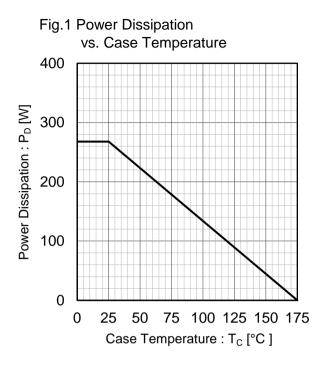
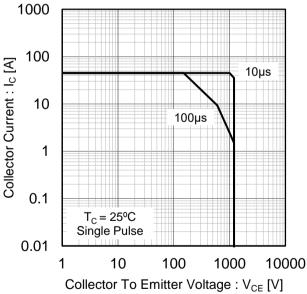


Fig.2 Collector Current vs. Case Temperature 40 Collector Current: Ic [A] 30 20 10 $T_j \le 175^{\circ}C$ $V_{GE} \ge 15V$ 0 25 50 75 100 125 150 175 0 Case Temperature : T_C [°C]

Fig.3 Forward Bias Safe Operating Area



60 Collector Current : Ic [A] 45 30 15 $T_i \le 175^{\circ}C$ $V'_{GE} = 15V$ 0 300 600 900 1200 1500

Fig.4 Reverse Bias Safe Operating Area

Collector To Emitter Voltage: V_{CE} [V]

• Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

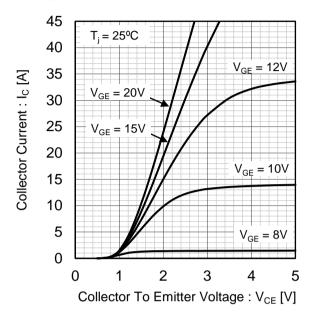


Fig.6 Typical Output Characteristics

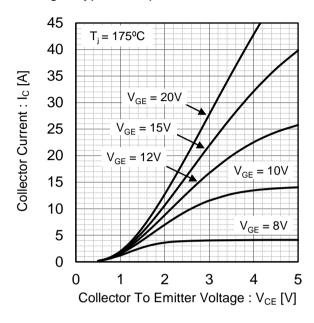


Fig.7 Typical Transfer Characteristics

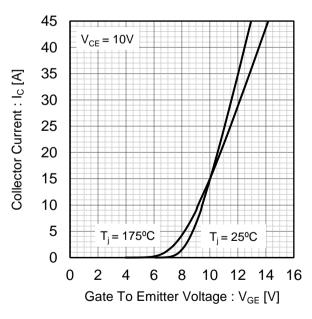
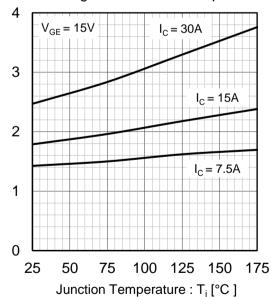


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



Collector To Emitter Saturation

Voltage: V_{CE(sat)} [V]



● Electrical Characteristic Curves

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

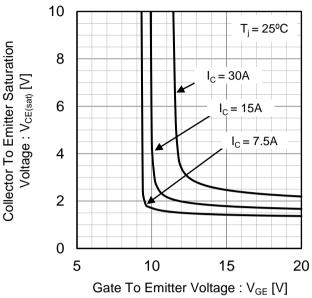


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

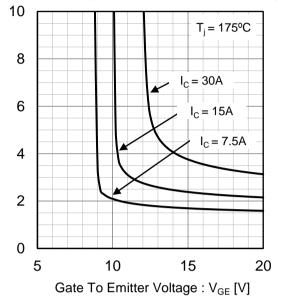


Fig.11 Typical Switching Time vs. Collector Current

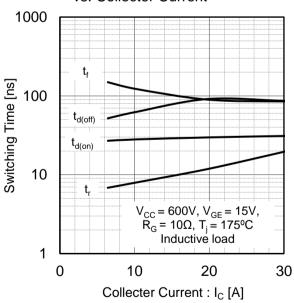
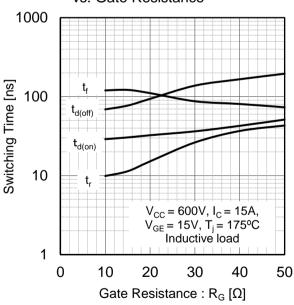


Fig.12 Typical Switching Time vs. Gate Resistance



Collector To Emitter Saturation

Voltage: V_{CE(sat)} [V]

• Electrical Characteristic Curves

Fig.13 Typical Switching Energy Losses vs. Collector Current

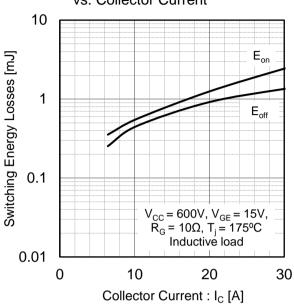


Fig.14 Typical Switching Energy Losses vs. Gate Resistance

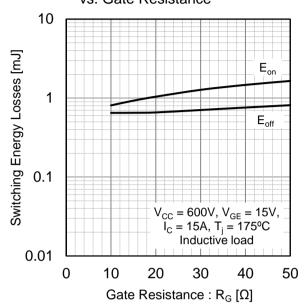


Fig.15 Typical Capacitance vs. Collector To Emitter Voltage

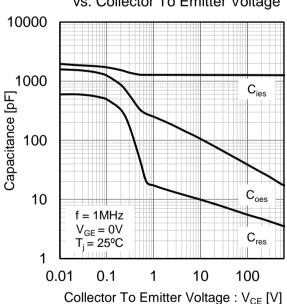
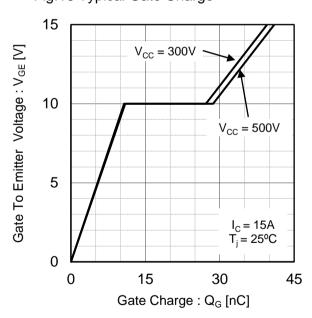
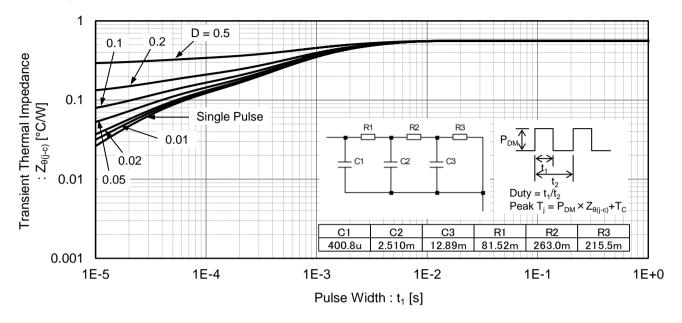


Fig.16 Typical Gate Charge



• Electrical Characteristic Curves

Fig.17 IGBT Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

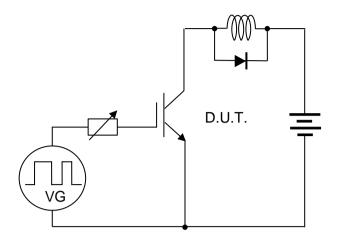


Fig.18 Inductive Load Circuit

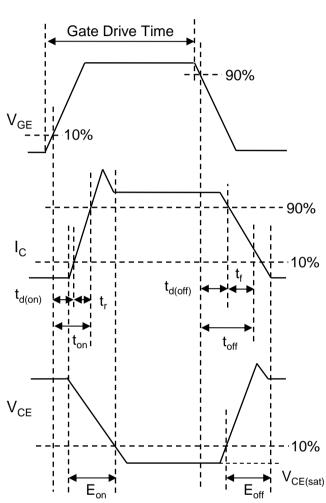


Fig.19 Inductive Load Waveform

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