RGS50NL65HRBTL

650V 25A Field Stop Trench IGBT

Datasheet

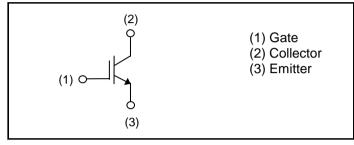
V _{CES}	650V
I _C	25A
V _{CE(sat) (Typ.)}	1.65V
P_{D}	206W

Outline LPDL (TO-263L) (1) (3)

Features

- 1) Qualified to AEC-Q101
- 2) Low Collector Emitter Saturation Voltage
- 3) Short Circuit Withstand Time 8µs
- 4) Pb free Lead Plating; RoHS Compliant

●Inner Circuit



Application

General Inverter

for Automotive and Industrial Use

Heater for Automotive

Packaging Specifications

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	Packaging	Taping				
	Reel Size (mm)	330				
Type	Tape Width (mm)	24				
Type	Basic Ordering Unit (pcs)	1,000				
	Packing Code	TL				
	Marking	RGS50NL65				

● **Absolute Maximum Ratings** (at T_C = 25°C unless otherwise specified)

	0	1 /		
Parameter		Symbol	Value	Unit
Collector - Emitter Voltage Gate - Emitter Voltage		V_{CES}	650	V
		V_{GES}	±30	
Collector Current	$T_C = 25^{\circ}C$	I _C	50	Α
Collector Current	T _C = 100°C	I _C	34	Α
Pulsed Collector Current	Collector Current		75	Α
Power Dissipation	T _C = 25°C	$T_C = 25^{\circ}C$ P_D		W
	T _C = 100°C	P _D	103	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by $T_{jmax.}$

●Thermal Resistance

Parameter	Symbol	Values			Unit
Falametei	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	ı	0.73	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Unit		
- raiailletei	Talameter Symbol Conditions		Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10\mu A, V_{GE} = 0V$	650	-	-	V
		$V_{CE} = 650V, V_{GE} = 0V,$				_
Collector Cut - off Current	I _{CES}	T _j = 25°C	-	-	10	μΑ
		Tj = 175°C	-	0.1	-	mA
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 1.25mA$	5.0	6.0	7.0	V
		$I_C = 25A, V_{GE} = 15V,$				_
Collector - Emitter Saturation Voltage	V _{CE(sat)}	T _j = 25°C	-	1.65	2.10	V
		T _j = 175°C	-	2.15	-	V

•IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Davamatas	Symbol	Conditions		l lmit		
Parameter			Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	$V_{CE} = 30V$,	-	968	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$,	-	66	-	pF
Reverse transfer Capacitance	C_{res}	f = 1MHz	-	9	-	
Total Gate Charge	Q_g	V _{CE} = 400V,	-	31	1	
Gate - Emitter Charge	Q_{ge}	I _C = 25A,	-	9	1	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	13	-	
Turn - on Delay Time	t _{d(on)}		-	28	-	
Rise Time	t _r	$I_C = 25A, V_{CC} = 400V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	15	-	
Turn - off Delay Time	t _{d(off)}	$T_i = 25^{\circ}C$	-	91	-	ns
Fall Time	t _f	Inductive Load	-	97	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.81	-	mJ
Turn - off Switching Loss	E _{off}	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	-	0.65	-	
Turn - on Delay Time	t _{d(on)}		-	28	-	ns
Rise Time	t _r	$I_C = 25A, V_{CC} = 400V,$ $V_{GE} = 15V, R_G = 10\Omega,$	-	18	-	
Turn - off Delay Time	t _{d(off)}	$T_i = 175^{\circ}C$	-	109	-	
Fall Time	t _f	Inductive Load	-	129	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.86	-	m l
Turn - off Switching Loss	E _{off}	Teverse recovery	-	0.87	-	mJ
		$I_C = 75A, V_{CC} = 520V,$	FULL SQUARE		-	
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650V, V_{GE} = 15V,$				
		$R_G = 50\Omega, T_j = 175^{\circ}C$				1
Short Circuit Withstand Time	t _{sc}	$V_{CC} \le 360V$, $V_{GE} = 15V$, $T_j = 25^{\circ}C$	8	-	-	μs
Short Circuit Withstand Time	t _{sc} *2	$V_{CC} \le 360V$, $V_{GE} = 15V$, $T_j = 150$ °C	6	-	-	μs

^{*2} Design assurance without measurement

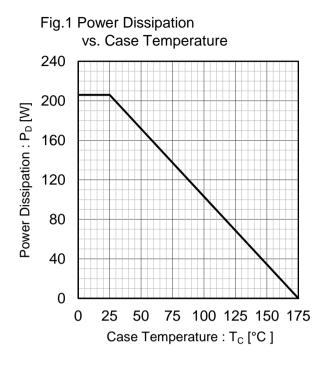


Fig.2 Collector Current vs. Case Temperature 60 50 Collector Current : Ic [A] 40 30 20 10 T_j ≤ 175°C V_{GE} ≥ 15V 0 25 50 75 100 125 150 175 0 Case Temperature : T_C [°C]

1000 1µs 100 Collector Current : I_C [A] 10µs 100µs 10 1 0.1 $T_{\rm C} = 25^{\circ}{\rm C}$ Single Pulse 0.01 10 100 1000 Collector To Emitter Voltage: V_{CE} [V]

Fig.3 Forward Bias Safe Operating Area

100

| Variable | Var

Fig.4 Reverse Bias Safe Operating Area

4/10



Fig.5 Typical Output Characteristics

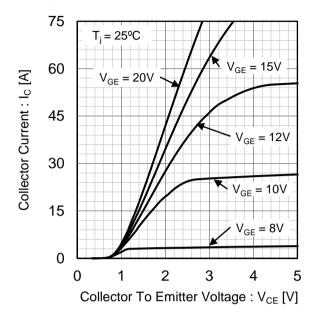


Fig.6 Typical Output Characteristics

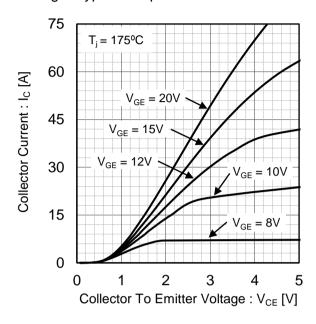


Fig.7 Typical Transfer Characteristics

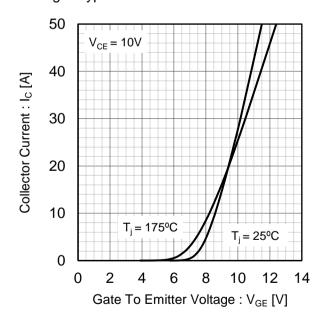
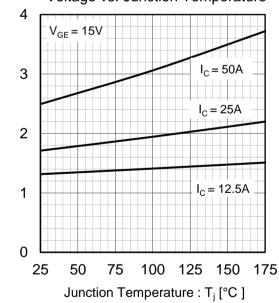


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



Collector To Emitter Saturation

Voltage: V_{CE(sat)} [V]



Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage 20 $T_{i} = 25^{\circ}C$ Collector To Emitter Saturation $I_{\rm C} = 50A$ 15 Voltage: V_{CE(sat)} [V] $I_C = 25A$ $I_{\rm C} = 12.5A$ 10 5 0 5 10 15 20 Gate To Emitter Voltage: V_{GE} [V]

Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

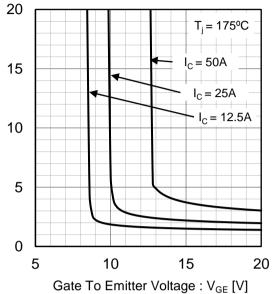
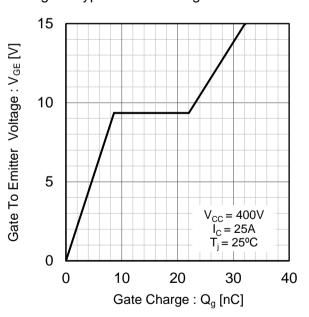


Fig.11 Typical Capacitance vs. Collector to Emitter Voltage 10000 \mathbf{C}_{ies} 1000 Capacitance [pF] Coes 100 10 f = 1MHz $V_{GE} = 0V$ $T_i = 25^{\circ}C$ C_{res} 0.01 0.1 1 10 100 Collector To Emitter Voltage: V_{CE} [V]

Fig.12 Typical Gate Charge



Collector To Emitter Saturation

Voltage: V_{CE(sat)} [V]

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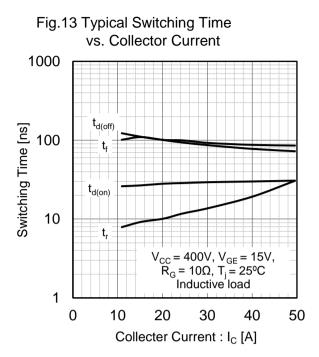


Fig.14 Typical Switching Time vs. Gate Resistance 1000 t_f Switching Time [ns] 100 $t_{d(off)}$ t_{d(on)} 10 $V_{CC} = 400V, V_{GE} = 15V,$ $I_{C} = 25A, T_{j} = 25^{\circ}C$ Inductive load 1 0 10 20 30 50 Gate Resistance : $R_G[\Omega]$

Datasheet

Fig.15 Typical Switching Energy Losses vs. Collector Current 10

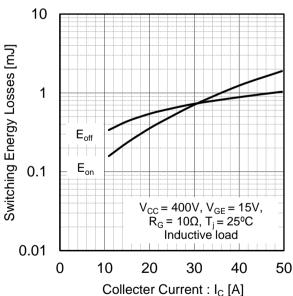


Fig.16 Typical Switching Energy Losses vs. Gate Resistance 10

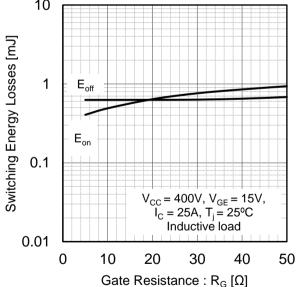


Fig.17 Typical Switching Time vs. Collector Current 1000 $t_{\rm f}$ Switching Time [ns] 100 $t_{d(off)}$ $t_{d(on)}$ 10 $V_{CC} = 400V, V_{GE} = 15V,$ $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 1 0 10 20 30 50 Collecter Current : I_C [A]

Fig.18 Typical Switching Time

vs. Collector Current 10 Switching Energy Losses [mJ] 1 $\mathsf{E}_{\mathsf{off}}$ E_{on} 0.1 $V_{CC} = 400V, V_{GE} = 15V,$ $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 0.01 0 10 20 30 40 50 Collecter Current : I_C [A]

Fig.19 Typical Switching Energy Losses

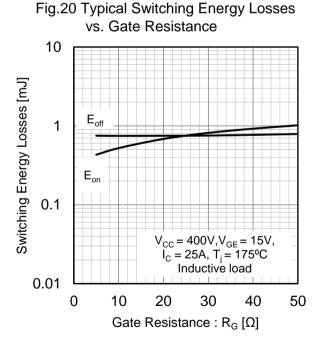
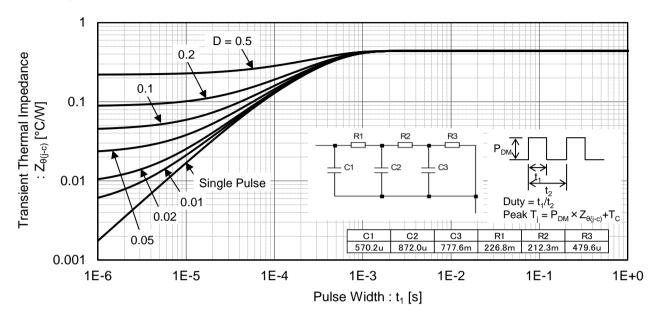


Fig.21 Typical IGBT Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

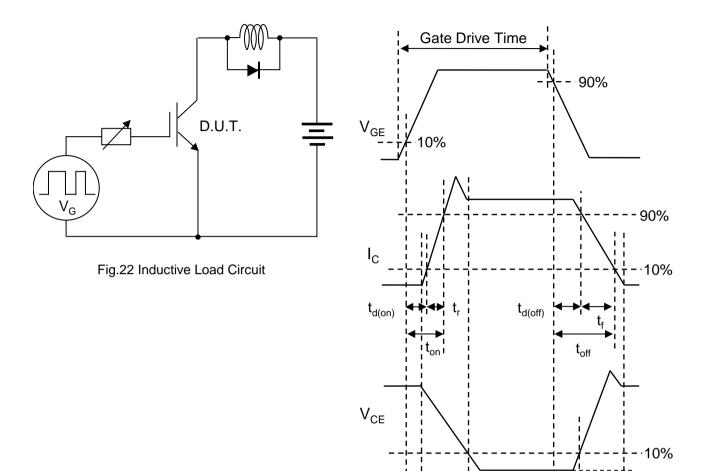


Fig.23 Inductive Load Waveform

 E_{on}

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