

650V 40A Field Stop Trench IGBT

V _{CES}	650V
Ι _{C (100°C)}	40A
V _{CE(sat) (Typ.)}	1.5V
P _D	214W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating ; RoHS Compliant

Applications

PFC

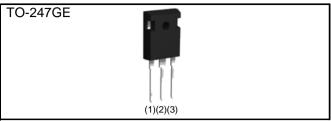
UPS

Welding

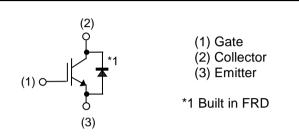
Solar Inverter

IH

Outline



Inner Circuit



Packaging Specifications

	Packaging	Tube
	Reel Size (mm)	-
Tupo	Tape Width (mm)	-
Туре	Basic Ordering Unit (pcs)	600
	Packing Code	C13
	Marking	RGW80TS65D

•Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

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Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V _{GES}	±30	V
Collector Current	$T_{\rm C} = 25^{\circ}{\rm C}$	Ι _C	78	А
Collector Current	$T_{\rm C} = 100^{\circ}{\rm C}$	Ι _C	40	А
Pulsed Collector Current		I _{CP} *1	160	А
Diada Forward Current	$T_{\rm C} = 25^{\circ}{\rm C}$	١ _F	40	А
Diode Forward Current	$T_{\rm C} = 100^{\circ}{\rm C}$	١ _F	20	А
Diode Pulsed Forward Current		I _{FP} ^{*1}	160	А
Power Dissipation	$T_{\rm C} = 25^{\circ}{\rm C}$	P _D	214	W
Power Dissipation	$T_{\rm C} = 100^{\circ}{\rm C}$	P _D	107	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C
*1 Dulas width limited by T		<u>.</u>	-	

*1 Pulse width limited by T_{jmax} .

RGW80TS65DGC13

•Thermal Resistance

Parameter	Symbol	Values			Unit
Parameter		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	0.70	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	1.62	°C/W

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit	
Faranieler	Symbol	Conditions	Min.	Тур.	Max.		
Collector - Emitter Breakdown Voltage	BV _{CES}	I _C = 10μΑ, V _{GE} = 0V	650	-	-	V	
Collector Cut - off Current	I _{CES}	V _{CE} = 650V, V _{GE} = 0V	-	-	10	μA	
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA	
Gate - Emitter Threshold Voltage	V _{GE(th)}	$V_{CE} = 5V, I_{C} = 26.0 \text{mA}$	5.0	6.0	7.0	V	
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 40A, V_{GE} = 15V$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.5 1.85	1.9 -	V	

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

	Ourseland I						
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Input Capacitance	C _{ies}	V _{CE} = 30V	-	3320	-		
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	83	-	pF	
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	60	-		
Total Gate Charge	Qg	V _{CE} = 400V	-	110	-		
Gate - Emitter Charge	Q _{ge}	I _C = 40A	-	23	-	nC	
Gate - Collector Charge	Q _{gc}	V _{GE} = 15V	-	41	-		
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 40$ A, $V_{\rm CC} = 400$ V	-	44	-		
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	17	-	20	
Turn - off Delay Time	t _{d(off)}	$T_j = 25^{\circ}C$	-	143	-	ns	
Fall Time	t _f	Inductive Load	-	34	-		
Turn - on Switching Loss	Eon	*E _{on} includes diode	-	0.76	-	mJ	
Turn - off Switching Loss	E _{off}	reverse recovery	-	0.72	-	IIIJ	
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 40$ A, $V_{\rm CC} = 400$ V	-	41	-		
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	18	-	20	
Turn - off Delay Time	t _{d(off)}	T _j = 175°C	-	158	-	ns	
Fall Time	t _f	Inductive Load	-	70	-		
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	0.76	-	~ I	
Turn - off Switching Loss	E _{off}	reverse recovery	-	0.91	-	mJ	
		$I_{\rm C} = 160$ A, $V_{\rm CC} = 520$ V		-	-		
Reverse Bias Safe Operating Area	RBSOA	$V_{P} = 650V, V_{GE} = 15V$	FU	LL SQUA	RE	-	
		$R_{G} = 100\Omega, T_{j} = 175^{\circ}C$					

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•FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol		Min.	Тур.	Max.	Unit
Diode Forward Voltage	V _F	$I_F = 20A$ $T_j = 25^{\circ}C$	-	1.45	1.9	v
		T _j = 175°C	-	1.55	-	
Diode Reverse Recovery Time	t _{rr}		-	92	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	I _F = 20A V _{CC} = 400V	-	6.7	-	А
Diode Reverse Recovery Charge	Q _{rr}	di _F /dt = 200A/µs T _j = 25°C	-	0.34	-	μC
Diode Reverse Recovery Energy	Err		-	14.1	-	μJ
Diode Reverse Recovery Time	t _{rr}		-	123	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	$I_F = 20A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 175^{\circ}C$	-	7.8	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	0.59	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	30.7	-	μJ

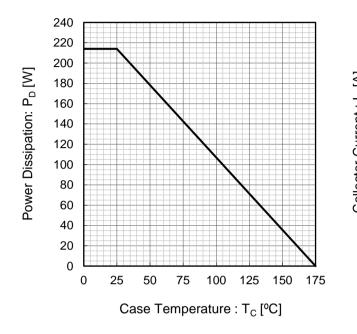


Fig.1 Power Dissipation vs. Case Temperature

Fig.2 Collector Current vs. Case Temperature

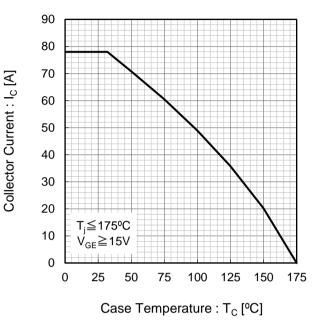
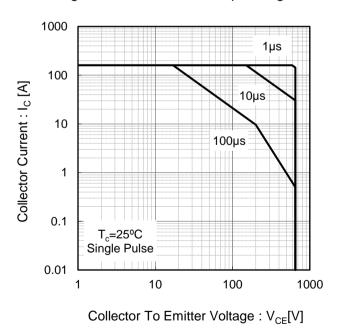
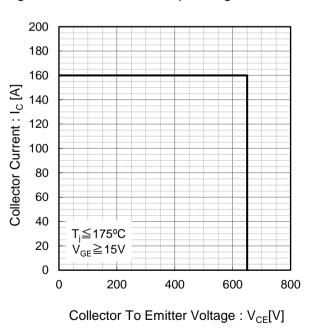


Fig.3 Forward Bias Safe Operating Area

Fig.4 Reverse Bias Safe Operating Area





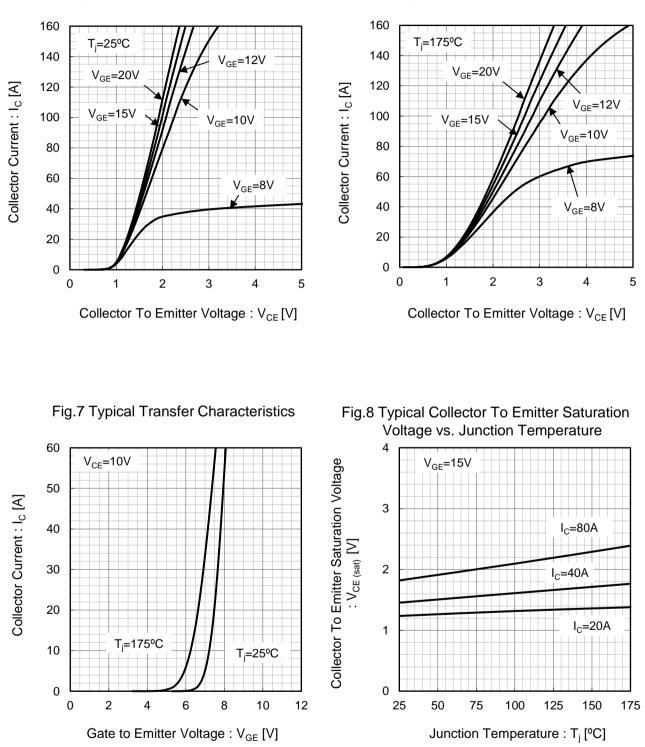
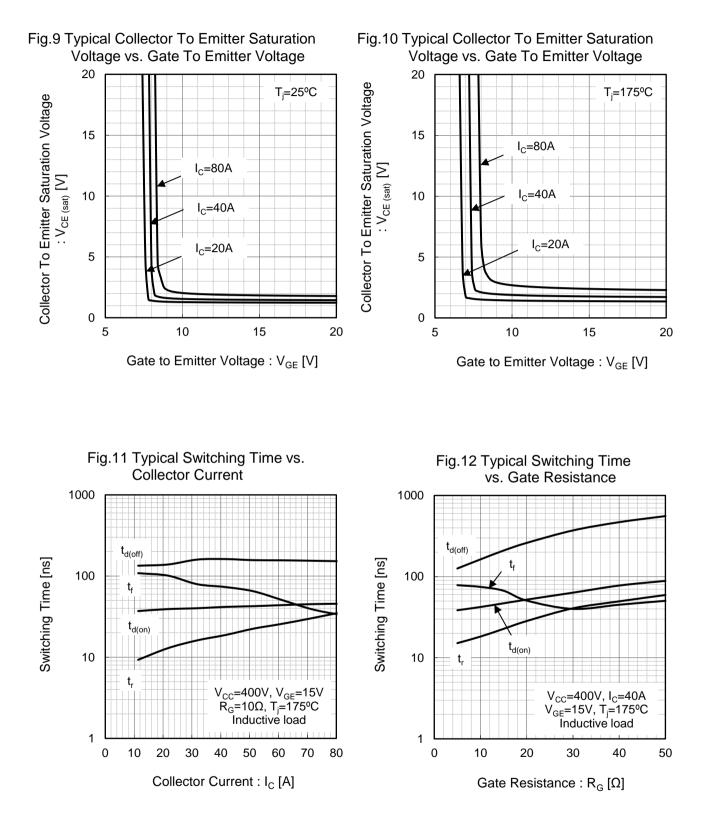
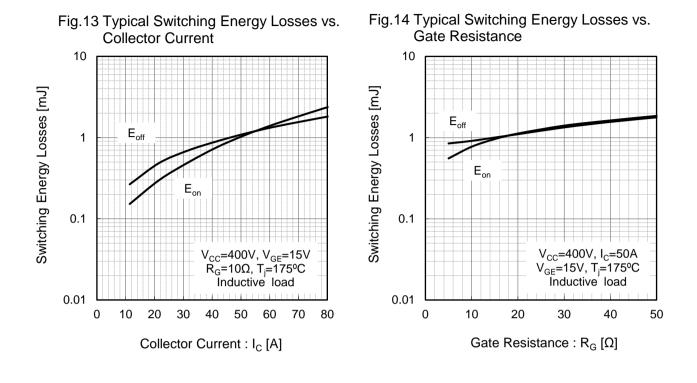


Fig.5 Typical Output Characteristics

Fig.6 Typical Output Characteristics





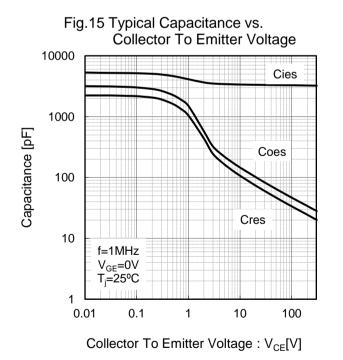
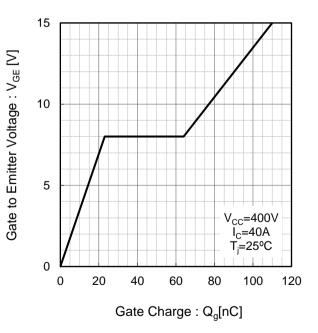
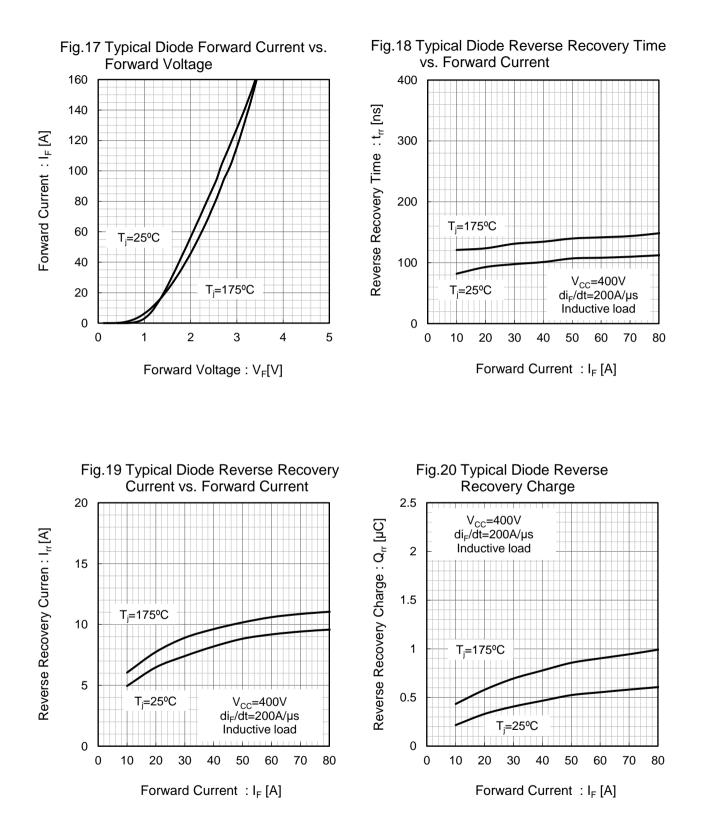


Fig.16 Typical Gate Charge





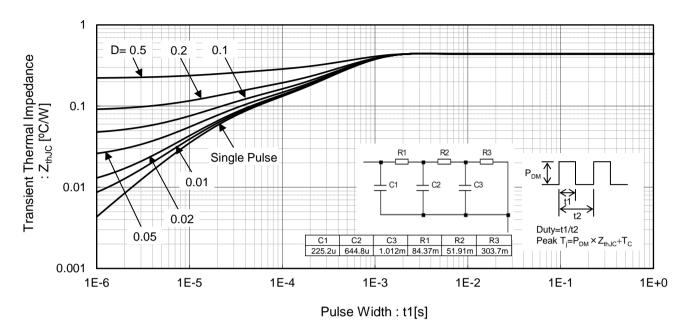
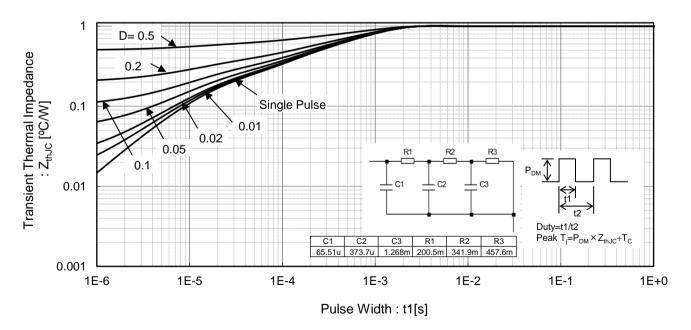


Fig.21 Typical IGBT Transient Thermal Impedance

Fig.22 Typical Disode Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

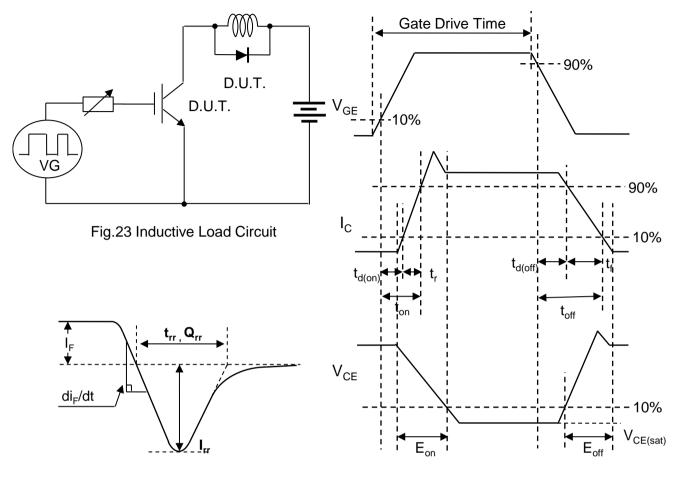
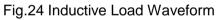


Fig.25 Diode Reverce Recovery Waveform





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