

RGWS60TS65

650V 30A Field Stop Trench IGBT

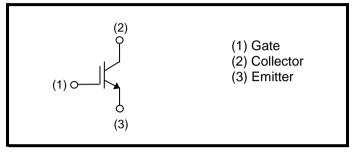
V _{CES}	650V
I _{C (100°C)}	30A
V _{CE(sat) (Typ.)}	1.6V
P_{D}	156W

Outline TO-247GE

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Pb free Lead Plating; RoHS Compliant

●Inner Circuit



Application

PFC

Solar converters

Mid to high switching frequency converters

Packaging Specifications

	. .	
	Packaging	Tube
	Reel Size (mm)	-
Type	Tape Width (mm)	-
Type	Basic Ordering Unit (pcs)	600
	Packing Code	C13
	Marking	RGWS60TS65

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
Collector Current	T _C = 25°C	I _C	51	А
	T _C = 100°C	I _C	32	А
Pulsed Collector Current		I _{CP} *1	90	Α
Power Dissipation	T _C = 25°C	P_{D}	156	W
	T _C = 100°C	P_{D}	78	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{imax}.

●Thermal Resistance

Dorameter	Symbol	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	ı	0.96	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Onit
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	ı	-	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	1	1	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V$, $V_{CE} = 0V$	1	ı	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 13.3mA$	5.0	6.0	7.0	V
Collector - Emitter Saturation	Var	$I_{C} = 30A, V_{GE} = 15V,$ $T_{i} = 25^{\circ}C$	_	1.6	2.0	V
Voltage	V CE(sat)	T _j = 175°C	-	2.0	-	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Doromotor	Symbol	Conditions	Values			Linit
Parameter			Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	$V_{CE} = 30V$,	-	1680	-	,
Output Capacitance	C _{oes}	$V_{GE} = 0V$,	-	47	-	pF
Reverse transfer Capacitance	C _{res}	f = 1MHz	-	31	-	
Total Gate Charge	Q_g	V _{CE} = 400V,	-	58	-	
Gate - Emitter Charge	Q_ge	$I_{\rm C} = 30A$,	-	12	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	24	-	
Turn - on Delay Time	t _{d(on)}		-	32	-	
Rise Time	t _r	$I_C = 30A, V_{CC} = 400V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	12	-	ns
Turn - off Delay Time	t _{d(off)}	$T_i = 25^{\circ}C$	-	91	-	
Fall Time	t _f	Inductive Load	-	46	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.50	-	
Turn - off Switching Loss	E _{off}	,	-	0.45	-	mJ
Turn - on Delay Time	t _{d(on)}		-	31	-	
Rise Time	t _r	$I_C = 30A, V_{CC} = 400V,$	-	13	-	20
Turn - off Delay Time	t _{d(off)}	$V_{GE} = 15V$, $R_{G} = 10\Omega$, $T_{j} = 175^{\circ}C$ Inductive Load *E _{on} include diode reverse recovery	-	101	-	ns
Fall Time	t _f		-	74	-	
Turn - on Switching Loss	E _{on}		-	0.51	-	
Turn - off Switching Loss	E _{off}		-	0.56	-	mJ
Reverse Bias Safe Operating Area	DRSO^4	$I_C = 90A, V_{CC} = 520V$ $V_P = 650V, V_{GE} = 15V$		11 80114	DE	_
	NBOUA	$R_G = 100\Omega, T_j = 175^{\circ}C$	FULL SQUARE			

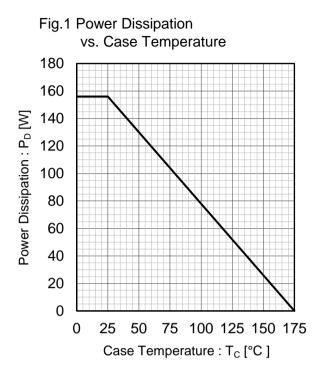


Fig.2 Collector Current vs. Case Temperature 60 50 Collector Current : Ic [A] 40 30 20 10 ≤ 175°C _{GE} ≥ 15V 0 50 75 100 125 150 175 25 0 Case Temperature : T_C [°C]

Fig.3 Forward Bias Safe Operating Area

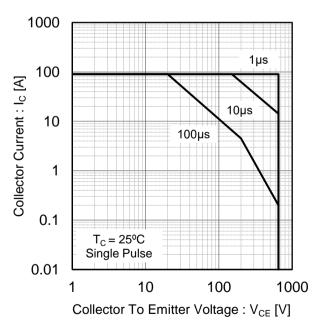


Fig.4 Reverse Bias Safe Operating Area

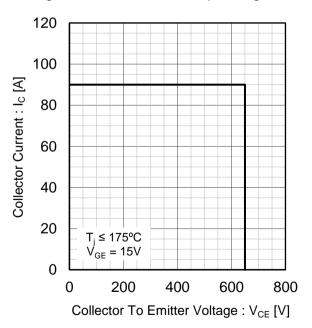


Fig.5 Typical Output Characteristics

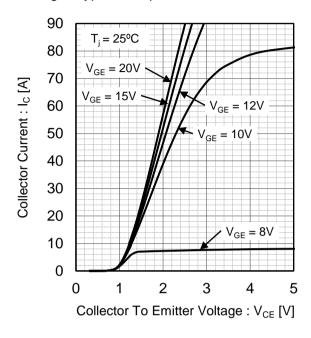


Fig.6 Typical Output Characteristics

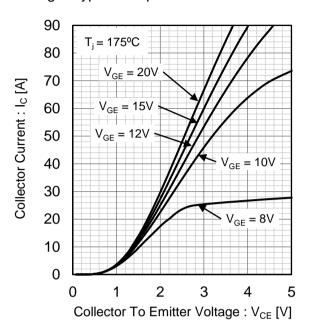


Fig.7 Typical Transfer Characteristics

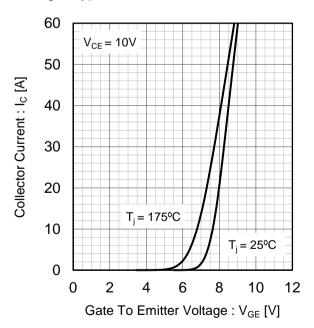
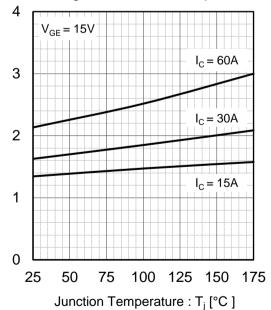


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



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Collector To Emitter Saturation

Voltage: V_{CE(sat)} [V]

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

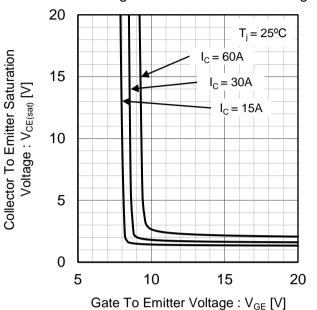


Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

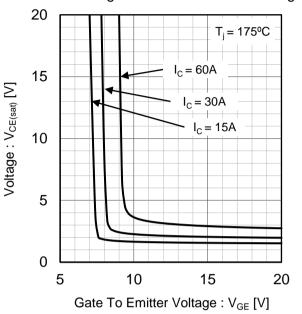


Fig.11 Typical Switching Time vs. Collector Current

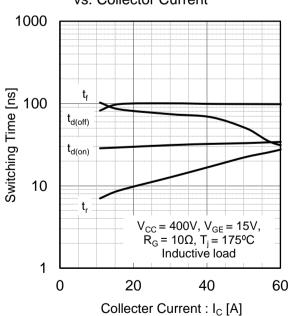
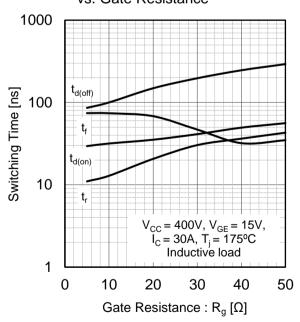


Fig.12 Typical Switching Time vs. Gate Resistance



Collector To Emitter Saturation

 E_{on}

0.01

0

Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 E_{off} 0.1

20

 V_{CC} = 400V, V_{GE} = 15V, R_{G} = 10 Ω , T_{j} = 175°C Inductive load

40

Collecter Current : I_C [A]

60

Fig.14 Typocal Switching Energy Losses vs. Gate Resistance 10

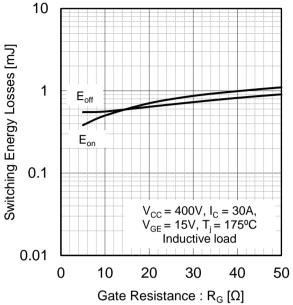
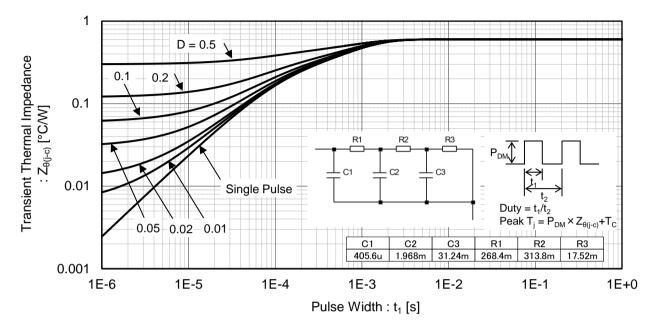


Fig.15 Typical Capacitance vs. Collector to Emitter Voltage 10000 \mathbf{C}_{ies} 1000 Capacitance [pF] 100 10 $\mathsf{C}_{\mathsf{res}}$ f = 1MHz $V_{GE} = 0V$ $T_i = 25^{\circ}C$ 1 0.01 0.1 1 10 100 Collector To Emitter Voltage: V_{CE} [V]

Fig.16 Typical Gate Charge 15 Gate To Emitter Voltage: VGE [V] 10 5 $V_{CC} = 400V$ $\tilde{l}_{C} = 30A$ $T_i = 25^{\circ}C$ 0 0 20 40 60 Gate Charge: Qq [nC]

Fig.17 Typical IGBT Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

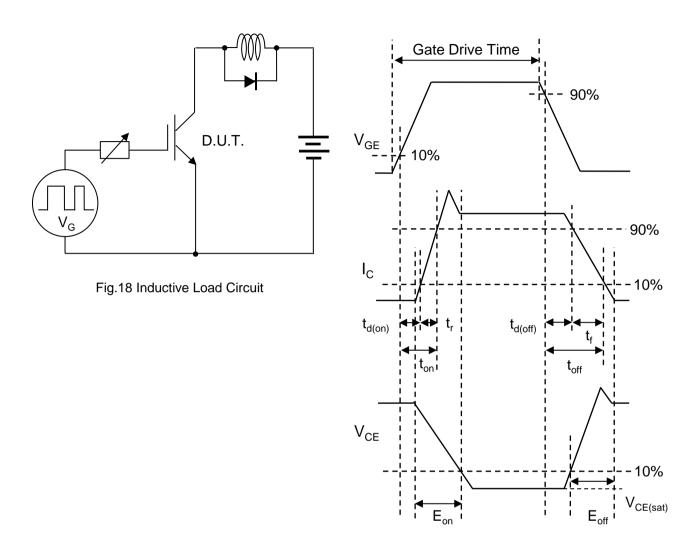


Fig.19 Inductive Load Waveform

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