

## 1200V 15A Insulated Gate Bipolar Transistor

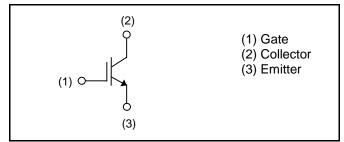
$V_{CES}$	1200V
I <sub>C (Nominal)</sub>	15A
V <sub>CE(sat) (Typ.)</sub>	1.55V
Max. Possible Chips per Wafer	1318pcs

# Outline Wafer (W05) Unsawn on foil (U03)

#### Features

- 1) Trench Light Punch Through Type
- 2) Low Collector Emitter Saturation Voltage
- 3) Short Circuit Withstand Time 10µs

#### ●Inner Circuit



## Application

General Inverter

for Automotive and Industrial Use

Heater for Automotive

**Exciter for Automotive** 

## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Collector - Emitter Voltage, T <sub>j</sub> = 25°C	$V_{CES}$	1200	V
Gate - Emitter Voltage	$V_{GES}$	±30	V
Collector Current	Ic*1	*1)	Α
Pulsed Collector Current	I <sub>CP</sub> *2	45	А
Operating Junction Temperature	T <sub>j</sub>	-40 to +175	°C

<sup>\*1</sup> Depending on thermal properties of assembly

<sup>\*2</sup> Pulse width limited by T<sub>imax.</sub>

## ●Design Assurance

Parameter Symbol Conditions	Conditions	Values			Unit	
- Farameter	Symbol Conditions		Min.	Тур.	Max.	Offit
		$V_{GE} = 15V, T_j = 25^{\circ}C$				
Short Circuit Withstand Time	$t_{\rm sc}^{*3}$	V <sub>CC</sub> ≤ 800V	10	-	-	μs
		V <sub>CC</sub> ≤ 850V	9	-	-	
Short Circuit Withstand Time		$V_{GE} = 15V, T_j = 150^{\circ}C$				
	$t_{\rm sc}^{*3}$	V <sub>CC</sub> ≤ 800V	8	-	-	μs
		V <sub>CC</sub> ≤ 850V	7	-	-	
		$I_C = 45A, V_{CC} = 940V,$				
Reverse Bias Safe Operating Area	RBSOA*3	$V_P = 1200V, V_{GE} = 15V,$	FULL SQUARE		-	
		$R_G = 50\Omega, T_j = 175^{\circ}C$				

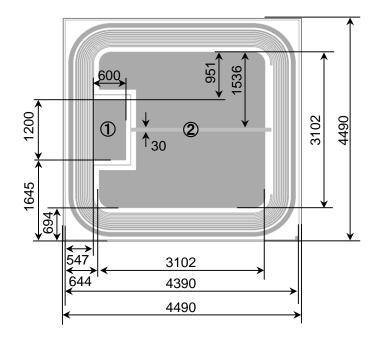
<sup>\*3</sup> Design assurance without measurement

# ●Electrical Characteristics (at T<sub>i</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Symbol Conditions		Values		
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_{C} = 10 \mu A, V_{GE} = 0 V$	1200	-	-	V
Collector Cut - off Current	I <sub>CES</sub>	$V_{CE} = 1200V, V_{GE} = 0V$	-	-	10	μA
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, V_{CE} = 0V$	ı	1	±500	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 1.8mA$	5.5	6.3	7.1	V
		$I_C = 15A, V_{GE} = 15V,$				
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub> *3	T <sub>j</sub> = 25°C	-	1.55	1.95	V
Vollago		T <sub>j</sub> = 150°C	-	1.95	-	
Input Capacitance	C <sub>ies</sub>	$V_{CE} = 30V$ ,	-	2712	-	
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$ ,	-	62	-	pF
Reverse transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	21	-	
Total Gate Charge	$Q_g$	$V_{CE} = 600V,$	-	98	-	
Gate - Emitter Charge	$Q_ge$	I <sub>C</sub> = 15A,	-	20	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	47	-	
Gate Input Resistance	R <sub>G(int)</sub>	f = 1MHz, open collector	ı	1.2	-	Ω
Integrated Gate Resistor	$R_{G}$	f = 1MHz, open collector	-	None	-	Ω

<sup>\*3</sup> Design assurance without measurement

# **●Chip Information**



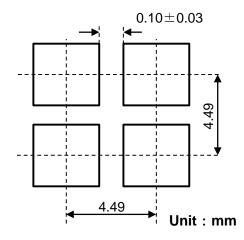
Unit: µm

: Pad Area

1 : Gate Bonding Pad

② : Emitter Bonding Pad

Backside: Collector



200mm
0.13±0.01mm
4.49mm×4.49mm
0.10±0.03mm
AlCu:4.4µm
Ti/Ni:0.4µm/Au:0.05µm
Polyimide
9.92mm <sup>2</sup>

#### •Further Electrical Characteristics

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

This chip data sheet refers to the device data sheet	-
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Technology planning to qualify in TO-247-4L package.

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