

# SH2102WN

650V 50A Fast Recovery Diode

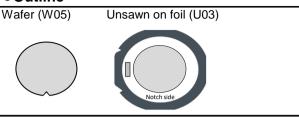
(1) Anode

(2) Cathode

V <sub>RM</sub>	650V
I <sub>F (Nominal)</sub>	50A
V <sub>F (Typ.)</sub>	1.45V
Max. Possible Chips per Wafer	1976pcs

## Outline

Inner Circuit



(1) Q

(2)

#### Features

- 1) Light Punch Through Type
- 2) Low Forward Voltage
- 3) Very Fast & Soft Recovery
- 4) Low Recovery Loss

#### Application

Free Wheeling

#### Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Repetitive Peak Reverse Voltage, $T_j = 25^{\circ}C$	V <sub>RM</sub>	650	V
Forward Current	I <sub>F</sub> <sup>*1</sup>	*1)	A
Pulsed Forward Current	I <sub>FP</sub> <sup>*2</sup>	200	A
Operating Junction Temperature	Tj	-40 to +175	°C

\*1 Depending on thermal properties of assembly

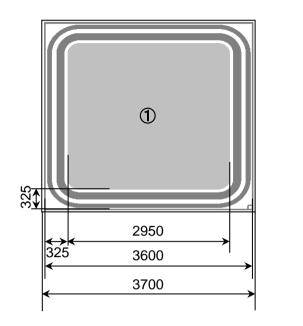
\*2 Pulse width limited by  $T_{jmax.}$ 

### •Electrical Characteristics (at T<sub>i</sub> = 25°C unless otherwise specified, in case of TO-247N package)

Parameter	Symbol	Conditions	Values			Unit
	Symbol		Min.	Тур.	Max.	Unit
Breakdown Voltage	BV	Ι <sub>R</sub> = 10μΑ	650	-	-	V
Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 650V	-	-	10	μA
		$I_F = 50A,$ $T_j = 25^{\circ}C$				
Forward Voltage	V <sub>F</sub> <sup>*3</sup>	T <sub>j</sub> = 25°C	-	1.45	1.9	V
		T <sub>j</sub> = 175°C	-	1.55	-	

\*3 Design assurance without measurement

#### Chip Information

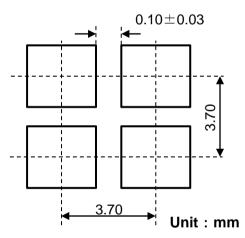




: Pad Area

① : Anode Bonding Pad

Backside : Cathode



Wafer Size	200mm
Wafer Thickness	0.07±0.01mm
Chip Size	3.70mm×3.70mm
Cut Line Width	0.10±0.03mm
Top Side Metallization	AlSiCu:5.0µm
Back Side Metallization	Ti/Ni:0.4µm/Au:0.05µm
Passivation	Polyimide

#### •Further Electrical Characteristics

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

This chip data sheet refers to the device data sheet

RGTV00TS65D



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