39A^{*1}



V_{DSS}	650V
R _{DS(on)} (Typ.)	$60 {\sf m}\Omega$

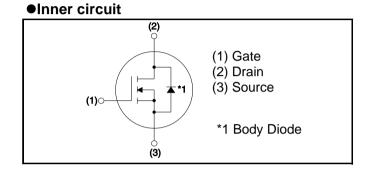
S4008

Features

- 1) Low on-resistance
- 2) Fast switching speed

 I_D

- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive



Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

● Absolute maximum ratings (T_a = 25°C)

Parameter		Symbol	Value	Unit	
Drain - Source voltage		V_{DSS}	650	V	
Continuous drain current	T _c = 25°C	I _D *1	39	А	
Pulsed drain current		I _{D,pulse} *2			
Gate - Source voltage (DC)		V _{GSS}	−4 to +22	V	
Gate-Source Surge Voltage (t _{surge} < 300nsec)		V _{GSS_surge} *3	−4 to +26	V	
Recommended Drive Voltage		V _{GS_op} *4	0 / +18	V	
Junction temperature		T _j	175	°C	
Range of storage temperature		T _{stg}	-55 to +175	°C	

•Electrical characteristics ($T_a = 25$ °C)

Parameter	Cumbal	Conditions	Values			l loit
	Symbol		Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	650	-	-	V
		$V_{DS} = 650V, V_{GS} = 0V$				
Zero gate voltage drain current	I_{DSS}	T _j = 25°C	-	1	10	μΑ
didiii odiioiit		T _j = 150°C	-	2	-	
Gate - Source leakage current	I_{GSS^+}	$V_{GS} = +22V, V_{DS} = 0V$	ı	-	100	nA
Gate - Source leakage current	I _{GSS} _	$V_{GS} = -4V$, $V_{DS} = 0V$	ı	-	-100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V, I_D = 6.67 \text{mA}$	2.7	-	5.6	V
		$V_{GS} = 18V, I_D = 13A$				
Static drain - source on - state resistance	R _{DS(on)} *5	T _j = 25°C	-	60	75	mΩ
2 2		T _j = 125°C	-	79.2	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	12	-	Ω

• Electrical characteristics ($T_a = 25$ °C)

Davamatav	Cumphal	Conditions	Values			Llmit
Parameter	Symbol		Min.	Тур.	Max.	Unit
Transconductance	g fs *5	$V_{DS} = 10V, I_D = 13A$	-	4.9	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	852	-	
Output capacitance	C _{oss}	V _{DS} = 500V	-	55	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	24	-	
Effective output capacitance, energy related	$C_{o(er)}$	$V_{GS} = 0V$ $V_{DS} = 0V$ to 300V	-	126	-	pF
Turn - on delay time	t _{d(on)} *5	$V_{DD} = 300V, I_D = 13A$	-	19	-	
Rise time	t _r *5	$V_{GS} = 18V/0V$	-	37	-	no
Turn - off delay time	t _{d(off)} *5	$R_L = 23\Omega$	-	34	ı	ns
Fall time	t _f *5	$R_G = 0\Omega$	-	21	-	
Turn - on switching loss	E _{on} *5	$V_{DD} = 300V, I_{D}=13A$ $V_{GS} = 18V/0V$	-	70	-	1
Turn - off switching loss	E _{off} *5	$R_G = 0\Omega L=500\mu H$ * E_{on} includes diode reverse recovery	-	10	-	μJ

•Gate Charge characteristics ($T_a = 25$ °C)

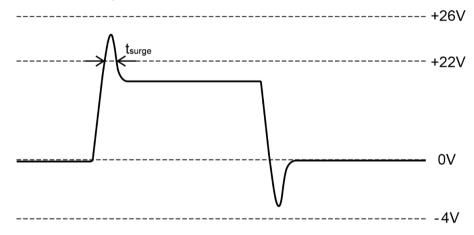
Parameter Sym	Cumbal	Symbol Conditions -	Values			Unit
	Symbol		Min.	Тур.	Max.	Uniii
Total gate charge	Q_g^{*5}	V _{DD} = 300V	-	58	-	
Gate - Source charge	Q _{gs} *5	I _D = 13A	-	15	-	nC
Gate - Drain charge	${\sf Q_{gd}}^{*5}$	V _{GS} = 18V	-	23	-	
Gate plateau voltage	V _(plateau)	$V_{DD} = 300V, I_D = 13A$	-	9.6	-	V

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter Sys	Symbol	Conditions	Values			Linit
	Symbol		Min.	Тур.	Max.	Unit
Inverse diode continuous, forward current	l _S *1	T _c = 25°C	-	1	39	А
Inverse diode direct current, pulsed	I _{SM} *2		-	-	97	А
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_{S} = 13A$	-	3.2	-	V
Reverse recovery time	t _{rr} *5	I _F = 13A, V _R = 300V di/dt = 1100A/μs	-	15	ı	ns
Reverse recovery charge	Q _{rr} *5		-	55	-	nC
Peak reverse recovery current	I _{rrm} *5		-	8	-	А

^{*1} For T_j=175°C and thermal dissiparion to ambience of 165W or more. Limited only by maximum temperature allowed.

^{*3} Example of acceptable Vgs waveform

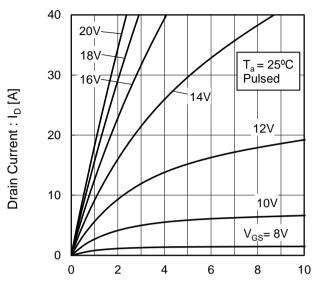


 $^{^{*}4}$ Please be advised not to use SiC-MOSFETs with V_{gs} below 13V as doing so may cause thermal runaway.

*5 Pulsed

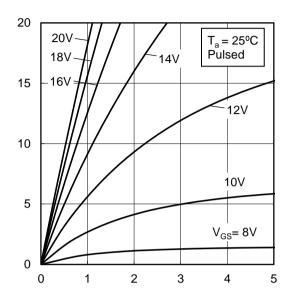
^{*2} PW \leq 10 μ s, Duty cycle \leq 1%

Fig.1 Typical Output Characteristics(I)



Drain - Source Voltage : V_{DS} [V]

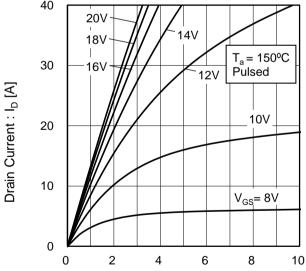
Fig.2 Typical Output Characteristics(II)



Drain Current : I_D [A]

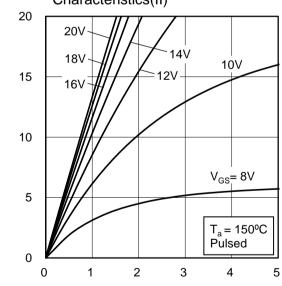
Drain - Source Voltage : V_{DS} [V]

Fig.3 T_j = 150°C Typical Output Characteristics(I)



Drain - Source Voltage : V_{DS} [V]

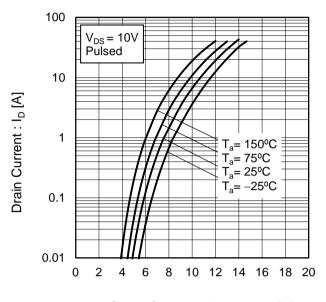
Fig.4 T_j = 150°C Typical Output Characteristics(II)



Drain - Source Voltage: V_{DS} [V]

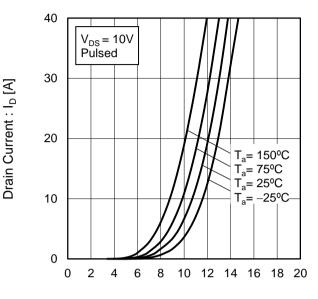
Drain Current : I_D [A]

Fig.5 Typical Transfer Characteristics (I)



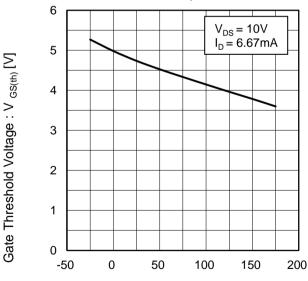
Gate - Source Voltage : V_{GS} [V]

Fig.6 Typical Transfer Characteristics (II)



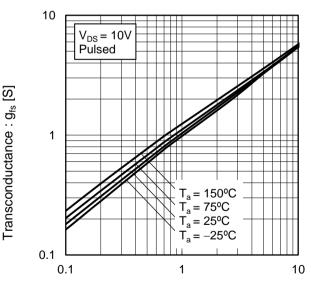
Gate - Source Voltage : V_{GS} [V]

Fig.7 Gate Threshold Voltage vs. Junction Temperature

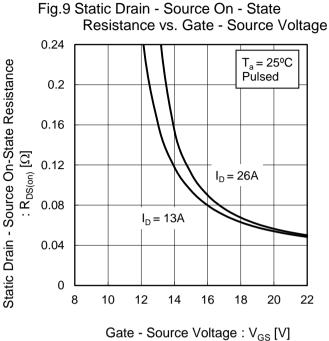


Junction Temperature : T_j [°C]

Fig.8 Transconductance vs. Drain Current



Drain Current : I_D [A]



Resistance vs. Junction Temperature 0.24 $V_{GS} = 18V$ Static Drain - Source On-State Resistance Pulsed 0.2 0.16 $R_{DS(on)}\left[\Omega\right]$ 0.12 $I_D = 26A$ 0.08 $I_D = 13A$ 0.04 0 0 50 100 150 200 -50 Junction Temperature : T_i [°C]

Fig.10 Static Drain - Source On - State

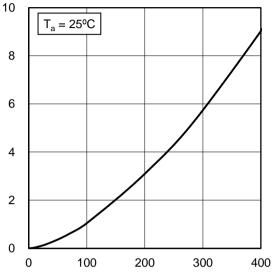
Fig.11 Static Drain - Source On - State Resistance vs. Drain Current 1 Static Drain - Source On-State Resistance 0.1 = 150°C $T_a = 125^{\circ}C$ Ta = 75°C $V_{GS} = 18V$ $T_a^{\circ} = 25^{\circ}C$ Pulsed $T_a = -25^{\circ}C$ 0.01 1 10 100 Drain Current: ID [A]

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Fig.12 Typical Capacitance vs. Drain - Source Voltage 10000 1000 Capacitance: C [pF] 100 C_{rs} 10 = 25°C f = 1MHz_{GS} = 0V 0.1 10 100 1000

 $T_a = 25^{\circ}C$

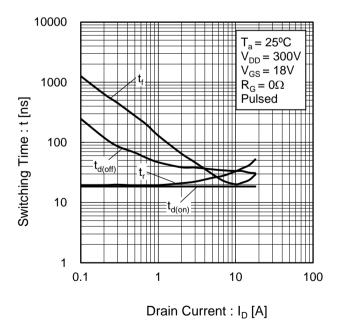
Fig.13 Coss Stored Energy



Drain - Source Voltage : V_{DS} [V]

Drain - Source Voltage : V_{DS} [V]

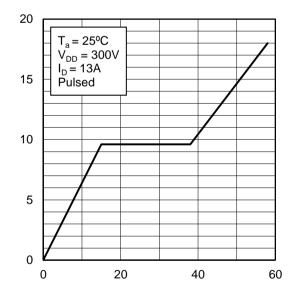
Fig.14 Switching Characteristics



Gate - Source Voltage : V_{GS} [V]

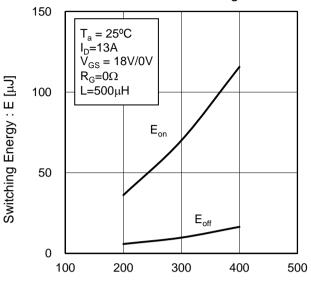
Coss Stored Energy : E_{OSS} [μJ]

Fig.15 Dynamic Input Characteristics



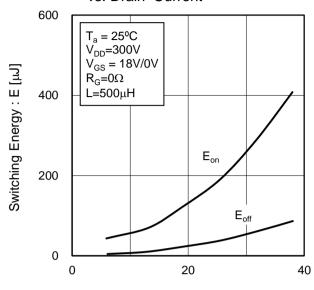
Total Gate Charge : Q_g [nC]

Fig.16 Typical Switching Loss vs. Drain - Source Voltage



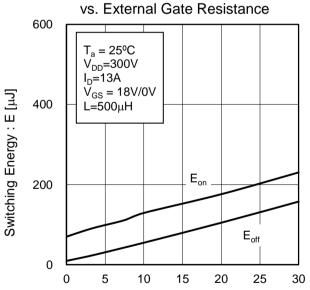
Drain - Source Voltage : V_{DS} [V]

Fig.17 Typical Switching Loss vs. Drain Current



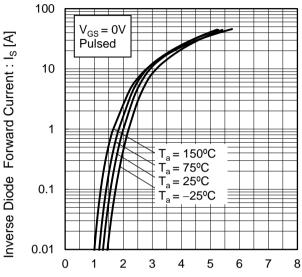
Drain Current : I_D [A]

Fig.18 Typical Switching Loss



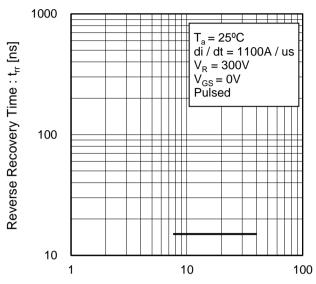
External Gate Resistance : $R_G[\Omega]$

Fig.19 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Fig.20 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

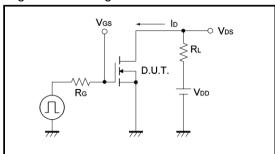


Fig.2-1 Gate Charge Measurement Circuit

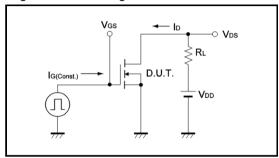


Fig.3-1 Switching Energy Measurement Circuit

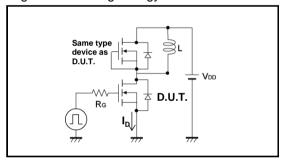


Fig.4-1 Reverse Recovery Time Measurement Circuit Fig.4-2 Reverse Recovery Waveform

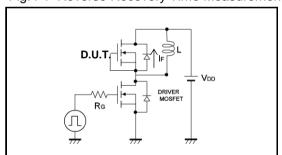


Fig.1-2 Switching Waveforms

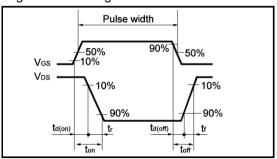


Fig.2-2 Gate Charge Waveform

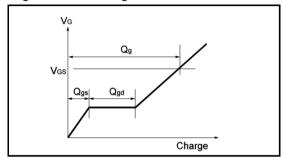
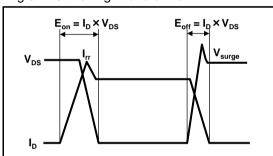
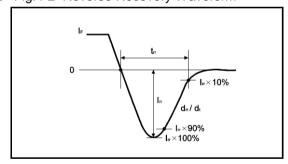


Fig.3-2 Switching Waveforms





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