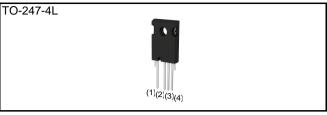


SCT4026DRHR

Automotive Grade N-channel SiC power MOSFET

V _{DSS}	750V
R _{DS(on)} (Typ.)	26mΩ
I_{D}^{*1}	56A
P _D	176W

Outline



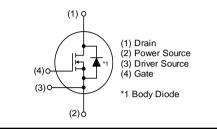
Features

- 1) Qualified to AEC-Q101
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Fast reverse recovery
- 5) Easy to parallel
- 6) Simple to drive
- 7) Pb-free lead plating ; RoHS compliant

Application

- Automobile
- · Switch mode power supplies

Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

Packaging specifications

Packing	Tube
Reel size (mm)	-
Tape width (mm)	-
Basic ordering unit (pcs)	30
Taping code	C15
Marking	SCT4026DR
	Reel size (mm) Tape width (mm) Basic ordering unit (pcs) Taping code

•Absolute maximum ratings (T_{vj} = 25°C unless otherwise specified.)

Parameter		Symbol	Value	Unit	
Drain - source voltage		V _{DSS}	750	V	
Continuous drain		$T_c = 25^{\circ}C$	• I _D , I _S ^{*1}	56	А
and source current	$V_{GS} = V_{GS_{on}}$	$T_c = 100^{\circ}C$		39	А
Pulsed drain current	$V_{GS} = V_{GS_{on}}$	$T_c = 25^{\circ}C$	I _{D,pulse} *2	91	Α
Body diode pulsed forwa	ard current	T _c = 25°C	1,*3 ^{*1,*3}	56	Α
Body diode surge forward current		$V_{GS} = 0 V$	*1,*4 S,pulse	91	Α
Gate - source voltage (DC)		$V_{GSS_{DC}}$	-4 to +21	V	
Gate - source surge vol	tage (t _{surge} < 300)ns)	V_{GSS_surge} *5	-4 to +23	V
Recommended turn-on gate - source drive voltage		V _{GS_on} *6	+15 to +18	V	
Recommended turn-off gate - source drive voltage		V _{GS_off}	0	V	
Virtual junction temperature		T _{vj}	175	°C	
Range of storage temperature		T _{stg}	-40 to +175	°C	

•Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

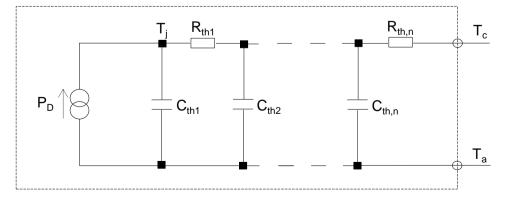
Deremeter	Cumphal	Symbol Conditions –		Values			
Parameter	Зушрог			Тур.	Max.	Unit	
Drain - Source breakdown	V	$V_{GS} = 0 V, I_{D} = 9.2 mA$				M	
voltage	V _{(BR)DSS}	T _{vj} = 25°C	750	-	-	V	
		$V_{GS} = 0 V, V_{DS} = 750V$					
Zero Gate voltage Drain current	I_{DSS}	T _{vj} = 25°C	-	1	80	μA	
		T _{vj} = 150°C	-	10	-		
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +21V$, $V_{DS} = 0V$	-	-	100	nA	
Gate - Source leakage current		$V_{GS} = -4V$, $V_{DS} = 0V$	-	-	-100	nA	
Gate threshold voltage	$V_{GS(th)}{}^{*7}$	$V_{DS} = 10V, I_{D} = 15.4mA$	2.8	-	4.8	V	
		$V_{GS} = 18V, I_{D} = 29A$					
Static Drain - Source on - state resistance	${\sf R}_{\sf DS(on)}$ *8	T _{vj} = 25°C	-	26	34	mΩ	
		T _{vj} = 150°C	-	44	-		
Gate input resistance	R_G	f = 1MHz, open drain	-	1	-	Ω	

Thermal resistance

Parameter	Symbol	Values			Unit
Faranielei	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	${\sf R_{thJC}}^{*9}$	-	0.65	0.85	K/W

•Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	4.9 ×10 ⁻²		C _{th1}	8.7 ×10 ⁻⁴	
R _{th2}	3.0 ×10 ⁻¹	K/W	C _{th2}	4.0 ×10 ⁻³	Ws/K
R _{th3}	3.0 ×10 ⁻¹		C _{th3}	5.2 ×10 ⁻²	





•Electrical characteristics (T_{vj} = 25°C unless otherwise specified)

Devemeter	Currence of	Conditions	Values			1.1	
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Transconductance	g _{fs} *8	$V_{DS} = 10V, I_{D} = 29A$	-	16	-	S	
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	2320	-		
Output capacitance	C _{oss}	V _{DS} = 500V	-	111	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	9	-		
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$ $V_{DS} = 0V$ to 500V	-	143	-	pF	
Total Gate charge	Q _g *8	$V_{DS} = 500V$ $I_{D} = 29A$	-	94	-		
Gate - Source charge	Q _{gs} *8	V _{GS} = 18V	-	20	-	nC	
Gate - Drain charge	Gate - Drain charge Q_{gd}^{*8} See Fig. 1-1, 1-2.		-	23	-		
Turn - on delay time	t _{d(on)} *8	$V_{DS} = 500V$ $I_{D} = 29A$	-	9.5	-		
Rise time	t _r *8	V _{GS} = +18V / 0V	-	22	-	ns	
Turn - off delay time	t _{d(off)} *8	$R_G = 6.8\Omega$, L = 250µH E _{on} includes diode	-	45	-	115	
Fall time	t _f *8	reverse recovery $L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF	-	13	-		
Turn - on switching loss	E _{on} *8	See Fig. 2-1, 2-2, 2-3.	-	213	-		
Turn - off switching loss	E _{off} *8		-	73	-	μJ	
V _{GS(on)} = +15∖ Short-circuit	t _{sc} *9	V _{DS} ≤ 400V V _{DS,peak} ≤ 750V	-	12.0	-	μs	
withstand time $V_{GS(on)} = +18V$		$T_{vj(start)} = 25^{\circ}C$ $R_{G} = 2.2\Omega$	-	11.5	-	μs	



•Body diode electrical characteristics (Source-Drain) (T_{vi} = 25°C unless otherwise specified)

Doromotor	Symbol Conditions		Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward voltage	V_{SD}^{*8}	$V_{GS} = 0V, I_S = 29A$	-	3.3	-	V
Reverse recovery time	t _{rr} *8	$I_F = 29A$ $V_R = 500V$	-	12	-	ns
Reverse recovery charge	Q _{rr} *8	di/dt = 2700A/µs	-	141	-	nC
Peak reverse recovery current	I _{rrm} *8	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	24	-	A

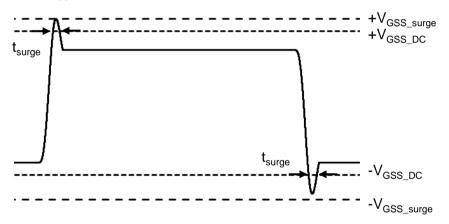
*1 Limited by maximum T_{vi} and for Max. R_{thJC} .

*2 Pulse width and duty cycle are limited by $T_{vj,max}$.

*3 Only for body-diode, Repititive pulse, PW \leq 1.5µs, Duty cycle \leq 5%

*4 When used as a protective function, PW \leq 10µs

*5 Example of acceptable V_{GS} waveform



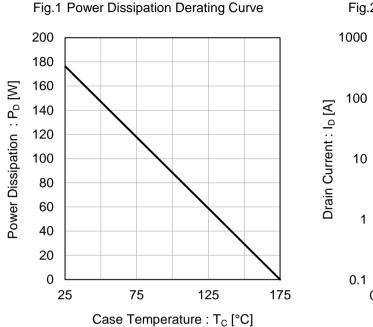
Please note especially when using driver source that V_{GSS_surge} must be in the range of absolute maximum rating.

- *6 Please be advised not to use SiC-MOSFETs with V_{GS} below 10V as doing so may cause thermal runaway.
- *7 Tested after applying $V_{GS} = 21V$ for 100ms.
- *8 Pulsed
- *9 The value is based on TO-247 package. Single Pulsed.
- *10 Measured conformable to JESD51-14.

See the application note "rthjc_measurement_and_usage_an-e.pdf". Link

URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc_measurement_and_usage_an-e.pdf





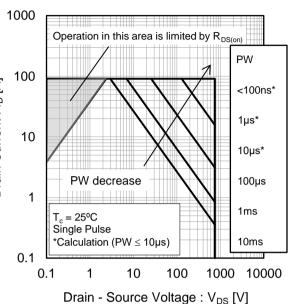
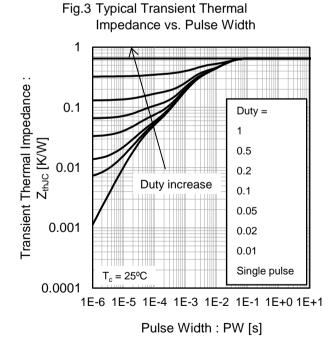
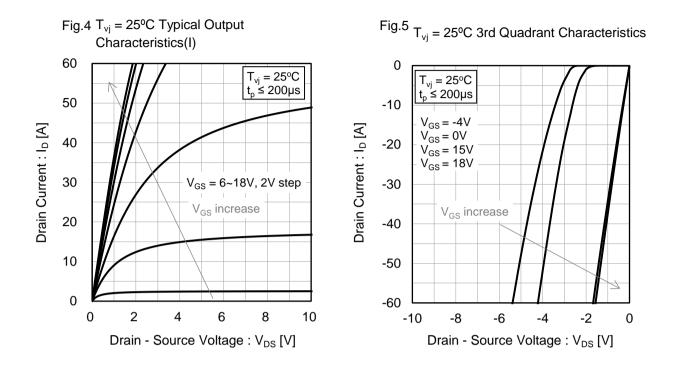


Fig.2 Maximum Safe Operating Area

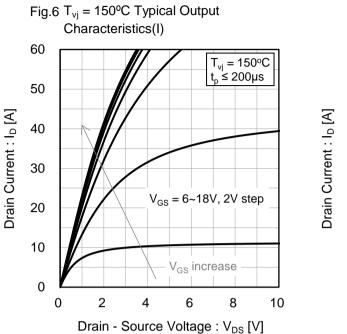


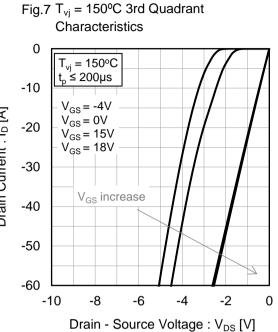


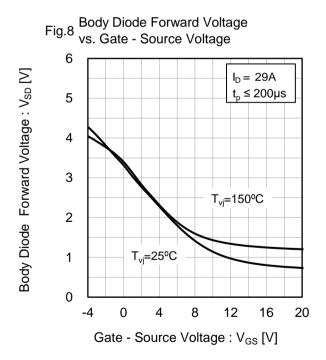












7/15



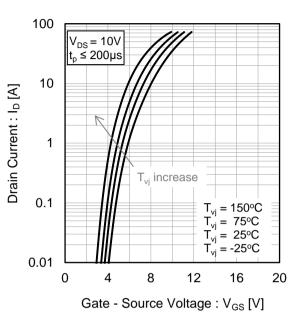
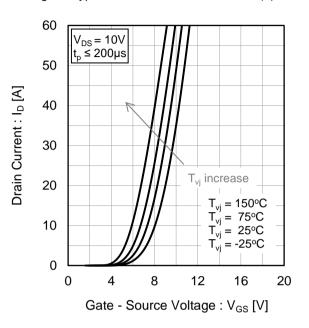


Fig.9 Typical Transfer Characteristics (I)

Fig.10 Typical Transfer Characteristics (II)



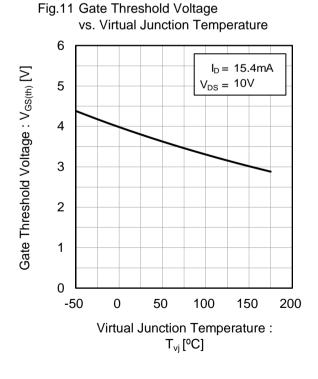
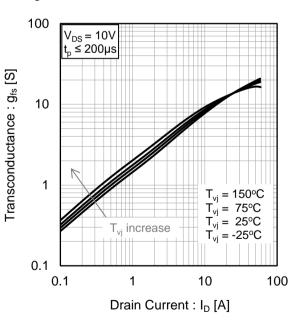
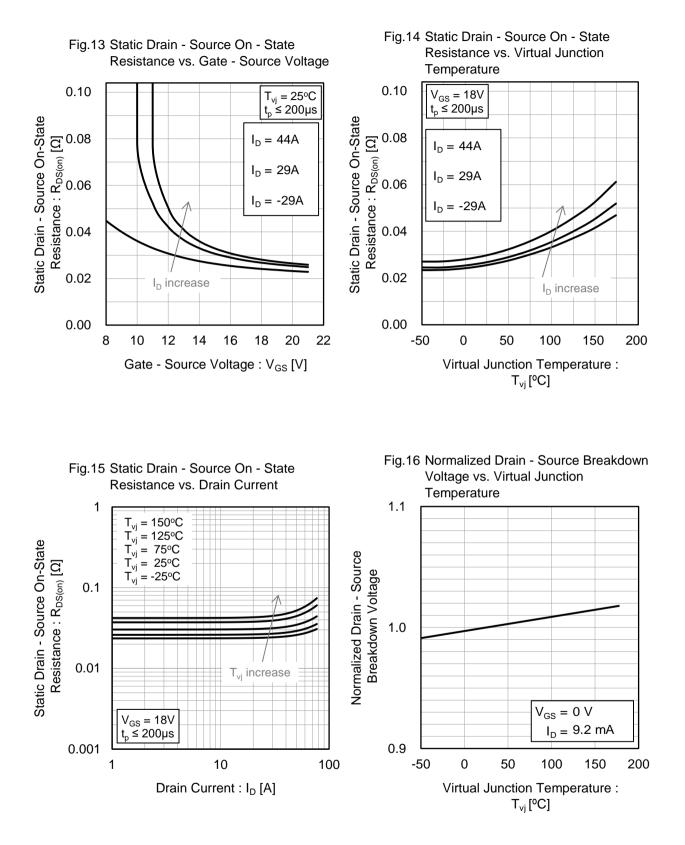


Fig.12 Transconductance vs. Drain Current









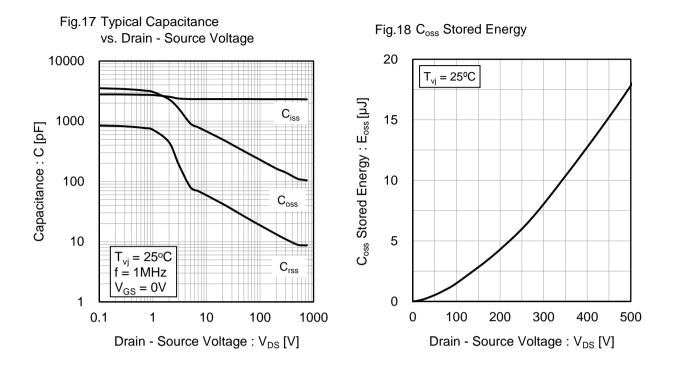
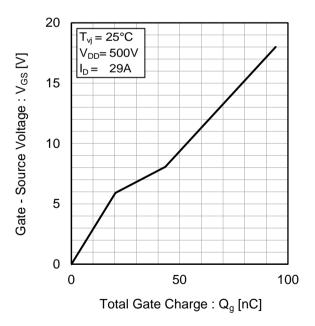
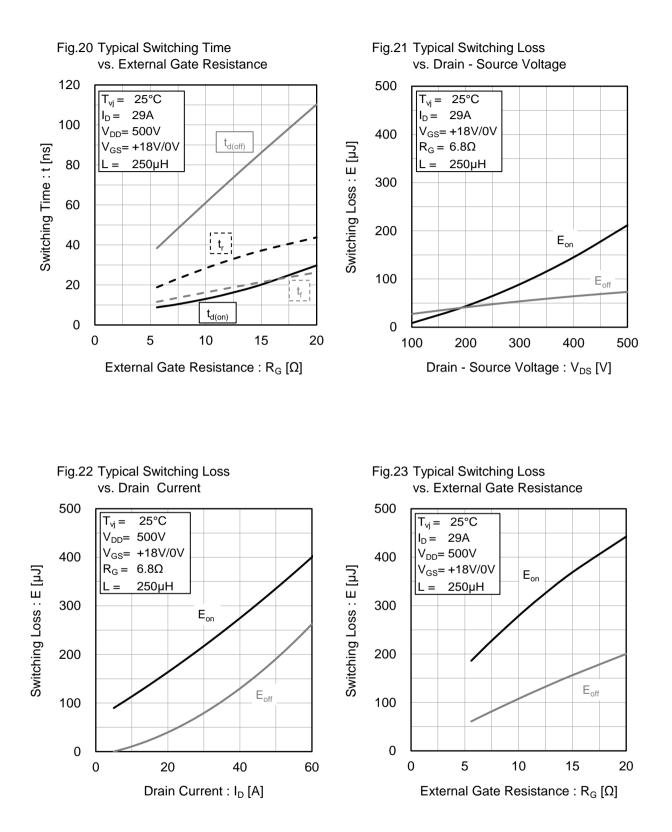


Fig.19 Dynamic Input Characteristics

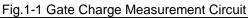






ROHM

Measurement circuits and waveforms



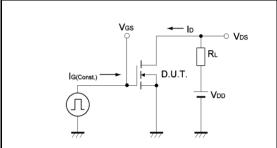


Fig.2-1 Switching Characteristics Measurement Circuit

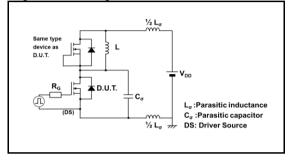


Fig.2-3 Waveforms for Switching Energy Loss

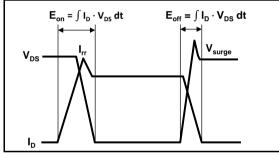


Fig.3-1 Reverse Recovery Time Measurement Circuit

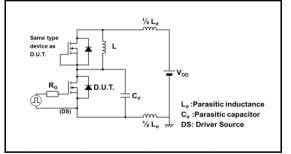


Fig.1-2 Gate Charge Waveform

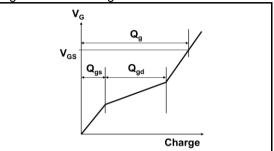


Fig.2-2 Waveforms for Switching Time

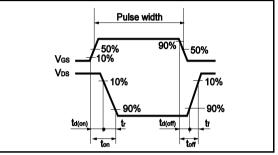
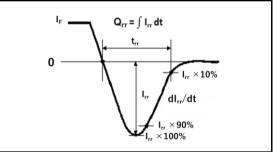
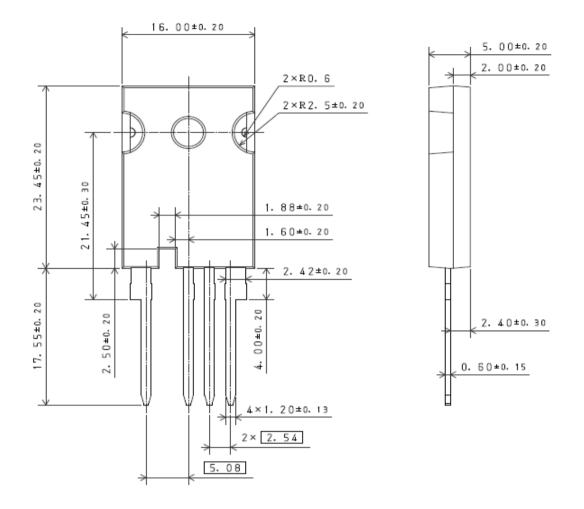


Fig.3-2 Reverse Recovery Waveform



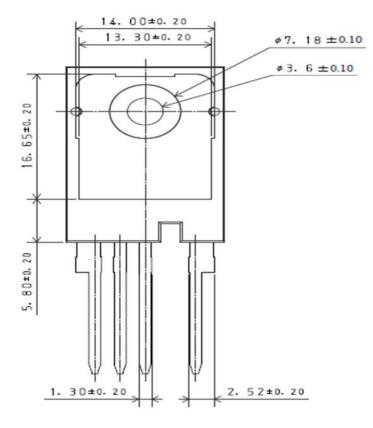


Package Dimensions



Unit: mm



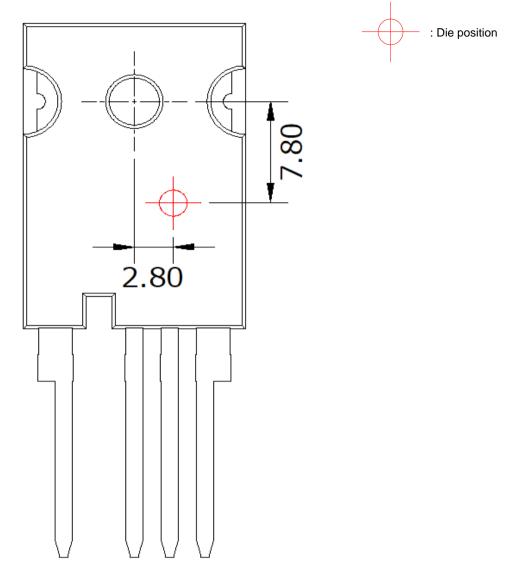


Unit: mm





Die Bonding Layout



•Front view of the packaging.

•Dimensions are design values.

·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm



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