

# SCT4065DEHR

## **Automotive Grade N-channel SiC power MOSFET**

Datasheet

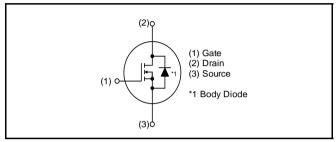
$V_{DSS}$	750V
R <sub>DS(on)</sub> (Typ.)	65mΩ
I <sub>D</sub> *1	25A
$P_D$	88W

# Outline TO-247N

#### Features

- 1) Qualified to AEC-Q101
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Fast reverse recovery
- 5) Easy to parallel
- 6) Simple to drive
- 7) Pb-free lead plating; RoHS compliant

#### ●Inner circuit



#### Application

- Automobile
- Switch mode power supplies

#### Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Type	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT4065DE

## ● **Absolute maximum ratings** (T<sub>vj</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit	
Drain - source voltage		$V_{DSS}$	750	V	
Continuous drain	$V_{GS} = V_{GS\_on}$	$T_c = 25^{\circ}C$	I <sub>D</sub> , I <sub>S</sub> *1	25	А
and source current	V <sub>GS</sub> = V <sub>GS_on</sub>	T <sub>c</sub> = 100°C	I <sub>D</sub> , I <sub>S</sub>	17	А
Pulsed drain current	$V_{GS} = V_{GS\_on}$	$T_c = 25^{\circ}C$	I <sub>D,pulse</sub> *2	58	А
Body diode pulsed forward	ard current	$T_c = 25^{\circ}C$	I <sub>S,pulse</sub> *1,*3	25	А
Body diode surge forward current $V_{GS} = 0 \text{ V}$		$V_{GS} = 0 V$	I <sub>S,pulse</sub> *1,*4	58	А
Gate - source voltage (DC)		$V_{GSS\_DC}$	-4 to +21	V	
Gate - source surge voltage (t <sub>surge</sub> < 300ns)		V <sub>GSS_surge</sub> *5	-4 to +23	V	
Recommended turn-on gate - source drive voltage		${\sf V_{GS\_on}}^{*6}$	+15 to +18	V	
Recommended turn-off gate - source drive voltage		$V_{GS\_off}$	0	V	
Virtual junction temperature		$T_{vj}$	175	°C	
Range of storage temperature		$T_{stg}$	-40 to +175	°C	

## ullet Electrical characteristics (T<sub>vj</sub> = 25°C unless otherwise specified)

Doromotor	Symbol	Conditions	Values	Unit		
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	W	$V_{GS} = 0 \text{ V}, I_{D} = 3.7 \text{mA}$				V
	V (BR)DSS	$T_{vj} = 25^{\circ}C$	750	-	-	V
		$V_{GS} = 0 \text{ V}, V_{DS} = 750 \text{V}$				
Zero Gate voltage Drain current	I <sub>DSS</sub>	$T_{vj} = 25^{\circ}C$	-	1	80	μΑ
Diam current		T <sub>vj</sub> = 150°C	-	10	-	
Gate - Source leakage current	I <sub>GSS+</sub>	$V_{GS} = +21V , V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current		$V_{GS} = -4V$ , $V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	$V_{GS(th)}^{*7}$	$V_{DS} = 10V, I_D = 6.15 \text{mA}$	2.8	ı	4.8	V
		$V_{GS} = 18V, I_{D} = 12A$				
Static Drain - Source on - state resistance	R <sub>DS(on)</sub> *8	$T_{vj} = 25^{\circ}C$	-	65	85	mΩ
on one of the order		T <sub>vj</sub> = 150°C	-	111	-	
Gate input resistance	$R_{G}$	f = 1MHz, open drain	-	4	-	Ω

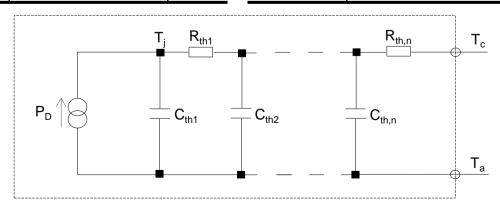
#### ●Thermal resistance

Parameter	Symbol -	Values			Unit
Falametei		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R <sub>thJC</sub> *9	-	1.3	1.7	K/W

● Typical Transient Thermal Characteristics

Symbol	Value	Unit
R <sub>th1</sub>	1.8 ×10 <sup>-1</sup>	
R <sub>th2</sub>	6.7 ×10 <sup>-1</sup>	K/W
R <sub>th3</sub>	4.8 ×10 <sup>-1</sup>	

Symbol	Value	Unit
$C_{th1}$	4.2 ×10 <sup>-4</sup>	
$C_{th2}$	1.9 <b>×</b> 10 <sup>-3</sup>	Ws/K
C <sub>th3</sub>	4.8 ×10 <sup>-2</sup>	



# ullet Electrical characteristics (T<sub>vj</sub> = 25°C unless otherwise specified)

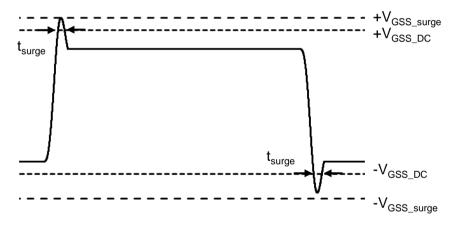
Dorometer	Cymphol	Conditions		Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	g <sub>fs</sub> *8	$V_{DS} = 10V, I_{D} = 12A$	-	5.7	-	S
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1066	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 500V	-	65	-	pF
Reverse transfer capacitance	e C <sub>rss</sub>	f = 1MHz	-	7	-	
Effective output capacitance, energy related	C <sub>o(er)</sub>	$V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 500V$	-	85	-	pF
Total Gate charge	Q <sub>g</sub> *8	$V_{DS} = 500V$ $I_{D} = 12A$	-	48	-	
Gate - Source charge	Q <sub>gs</sub> *8	$V_{GS} = 18V$	-	13	-	nC
Gate - Drain charge	Q <sub>gd</sub> *8	See Fig. 1-1, 1-2.	-	15	-	
Turn - on delay time	t <sub>d(on)</sub> *8	$V_{DS} = 500V$ $I_{D} = 12A$	-	4.4	ı	
Rise time	t <sub>r</sub> *8	$V_{GS} = +18V / 0V$	-	20	ı	ns
Turn - off delay time	t <sub>d(off)</sub> *8	$R_G = 0\Omega$ , L = 250 $\mu$ H $E_{on}$ includes diode	-	17	-	115
Fall time	t <sub>f</sub> *8	reverse recovery $L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF	-	10	1	
Turn - on switching loss	E <sub>on</sub> *8	See Fig. 2-1, 2-2, 2-3.	-	190	1	μJ
Turn - off switching loss	E <sub>off</sub> *8		-	10	-	μυ
$V_{GS(on)} = +15^{\circ}$	/ t <sub>sc</sub> *10	$V_{DS} \le 400V$ $V_{DS,peak} \le 750V$	-	12.0	-	μs
withstand time $V_{GS(on)} = +18^{\circ}$		$T_{vj(start)} = 25^{\circ}C$ $R_G = 2.2\Omega$	-	11.5	-	μs

## ullet Body diode electrical characteristics (Source-Drain) ( $T_{vj} = 25^{\circ}$ C unless otherwise specified)

Darameter	Cymbal	Sumbol Conditions	Values			l lmit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward voltage	V <sub>SD</sub> *8	$V_{GS} = 0V, I_{S} = 12A$	-	3.3	ı	V
Reverse recovery time	t <sub>rr</sub> *8	$I_F = 12A$ $V_R = 500V$	ı	26	ı	ns
Reverse recovery charge	Q <sub>rr</sub> *8	di/dt = 3100A/µs	-	120	-	nC
Peak reverse recovery current	I <sub>rrm</sub> *8	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	ı	10	ı	Α

<sup>\*1</sup> Limited by maximum  $T_{vj}$  and for Max.  $R_{thJC}$ .

#### \*5 Example of acceptable V<sub>GS</sub> waveform



- \*6 Please be advised not to use SiC-MOSFETs with V<sub>GS</sub> below 10V as doing so may cause thermal runaway.
- \*7 Tested after applying  $V_{GS} = 21V$  for 100ms.
- \*8 Pulsed
- \*9 Measured conformable to JESD51-14.

See the application note "rthjc\_measurement\_and\_usage\_an-e.pdf". Link

URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc\_measurement\_and\_usage\_an-e.pdf

\*10 Single pulsed.

<sup>\*2</sup> Pulse width and duty cycle are limited by  $T_{v_j,max}$ .

<sup>\*3</sup> Only for body-diode, Repetitive pulse, PW ≤ 1.5µs, Duty cycle ≤ 5%

<sup>\*4</sup> When used as a protective function, PW  $\leq$  10 $\mu$ s

Fig.1 Power Dissipation Derating Curve

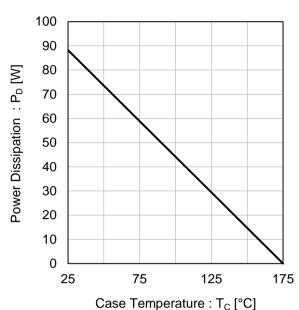


Fig.2 Maximum Safe Operating Area

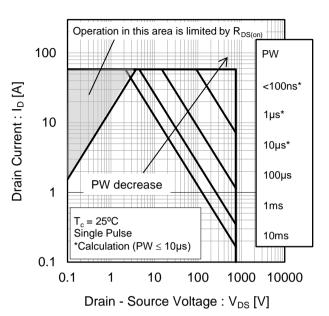
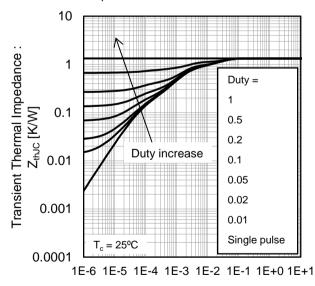


Fig.3 Typical Transient Thermal Impedance vs. Pulse Width



Pulse Width: PW [s]

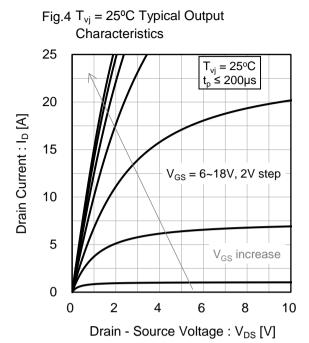
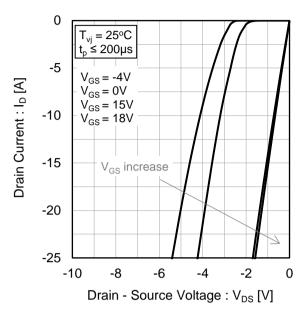
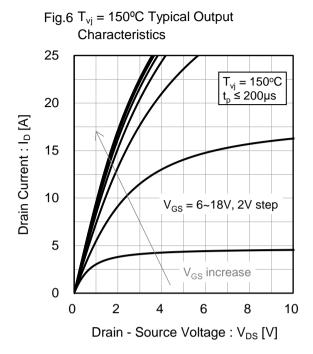


Fig.5  $T_{vj}$  = 25°C 3rd Quadrant Characteristics



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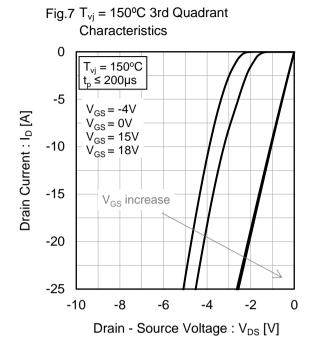


Fig.8 Body Diode Forward Voltage vs. Gate - Source Voltage 6 Body Diode Forward Voltage: V<sub>SD</sub> [V] I<sub>D</sub> = 12A t<sub>n</sub> ≤ 200µs 5 4 3 T<sub>vj</sub>=150°C 2 1 T<sub>vi</sub>=25°C 0 0 4 20 12 16 Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.9 Typical Transfer Characteristics (I)

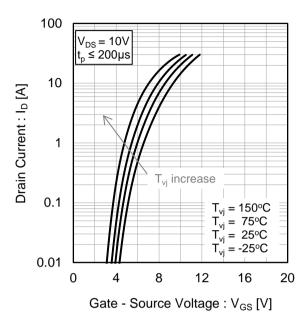


Fig.10 Typical Transfer Characteristics (II)

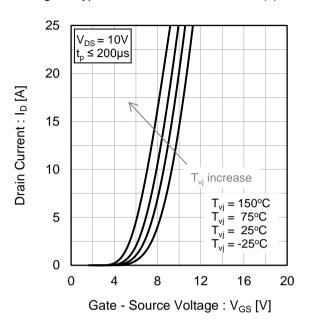


Fig.11 Gate Threshold Voltage vs. Virtual Junction Temperature

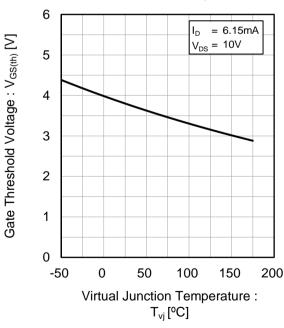
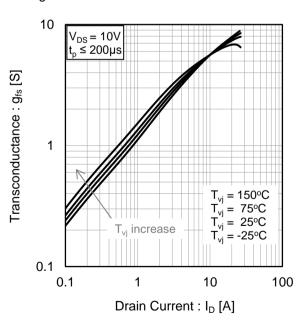
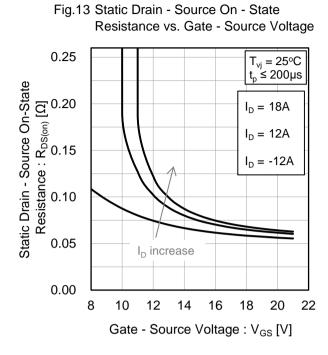


Fig.12 Transconductance vs. Drain Current

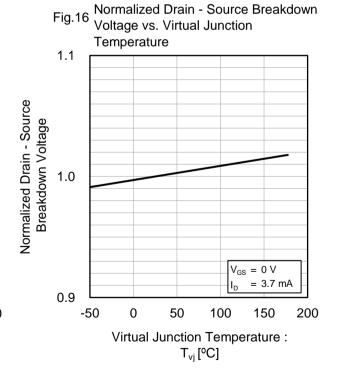




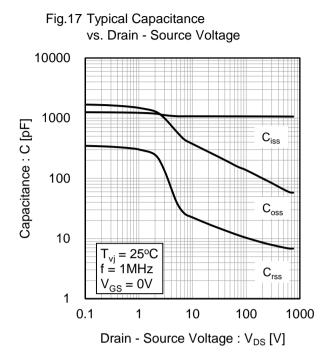
Resistance vs. Virtual Junction Temperature 0.25  $V_{GS} = 18V$   $t_p \le 200 \mu s$ Static Drain - Source On-State Resistance : R<sub>DS(on)</sub> [Ω] 0.10 0.10  $I_{D} = 18A$ = 12A $I_{D} = -12A$ 0.05 I<sub>D</sub> increase 0.00 -50 0 50 100 150 200 Virtual Junction Temperature: T<sub>vi</sub> [°C]

Fig.14 Static Drain - Source On - State

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current 1 T<sub>vj</sub> = 150°C  $T_{vj} = 125^{\circ}C$ Static Drain - Source On-State  $T_{vj}^{vj} = 75^{\circ}C$   $T_{vj} = 25^{\circ}C$ Resistance: R<sub>DS(on)</sub> [Ω]  $T_{vj}^{vj} = -25^{\circ}C$ 0.1 T<sub>vi</sub> increase  $V_{GS} = 18V$   $t_p \le 200 \mu s$ 0.01 10 100 Drain Current: I<sub>D</sub> [A]



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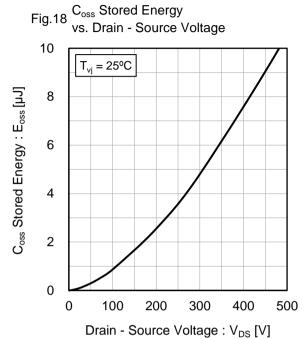


Fig.19 Dynamic Input Characteristics

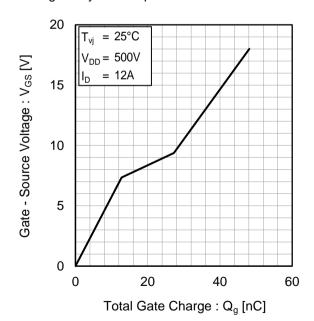


Fig.20 Typical Switching Time vs. External Gate Resistance

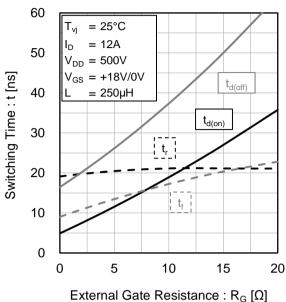
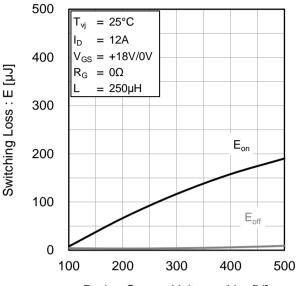


Fig.21 Typical Switching Loss vs. Drain - Source Voltage



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.22 Typical Switching Loss vs. Drain Current

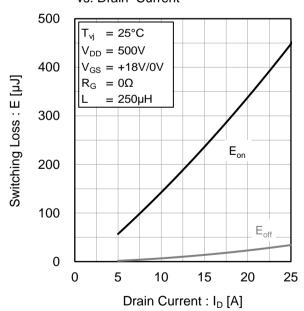
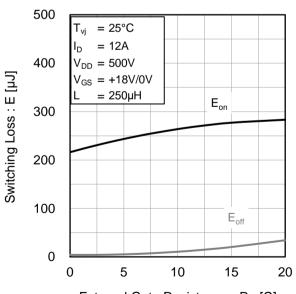


Fig.23 Typical Switching Loss vs. External Gate Resistance



#### Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

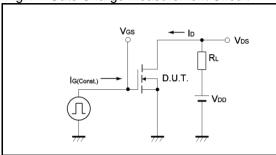


Fig.2-1 Switching Characteristics Measurement Circuit

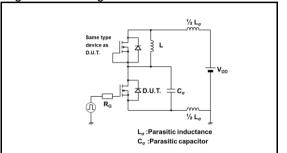


Fig.2-3 Waveforms for Switching Energy Loss

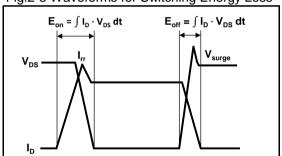


Fig.3-1 Reverse Recovery Time Measurement Circuit

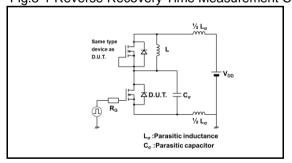


Fig.1-2 Gate Charge Waveform

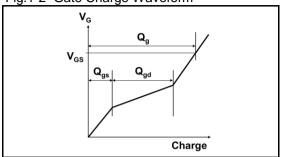


Fig.2-2 Waveforms for Switching Time

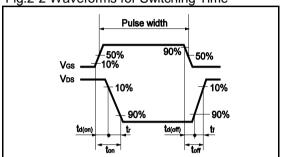
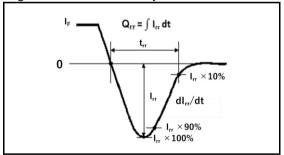
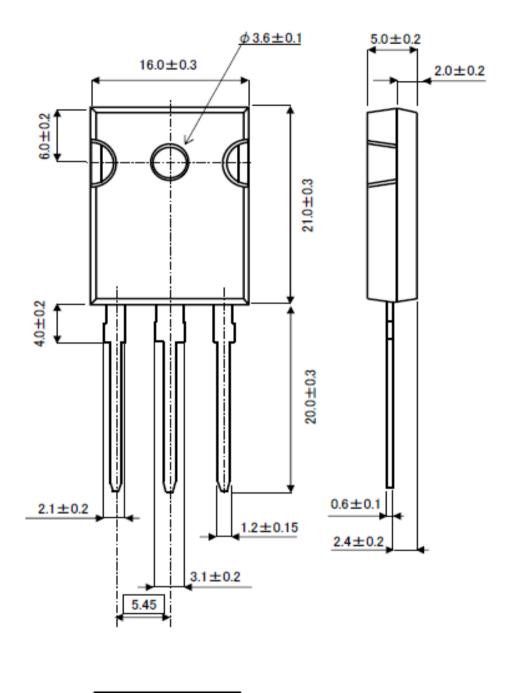


Fig.3-2 Reverse Recovery Waveform

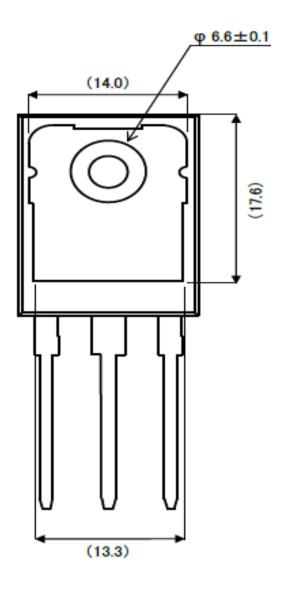


### ●Package Dimensions





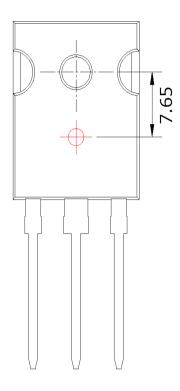
Unit: mm



Unit: mm

### ●Die Bonding Layout





- •Front view of the packaging.
- •Dimensions are design values.
- ·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

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