

### Features

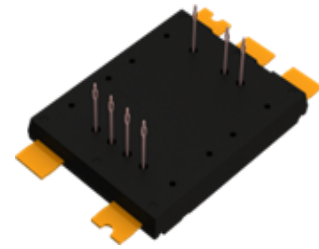
- TRCDRIVE pack™ with the 4th Generation SiC-MOSFET
- $V_{DSS} = 1200V$
- Low  $R_{DS(on)}$
- High-speed switching possible
- Low switching losses
- Low stray inductance - 5.7nH
- $T_{vjmax} = 175^{\circ}C$
- Compact design
- High power density
- Press-fit contact technology
- Integrated NTC temperature sensor
- Mountable on heatsink with thermal interface material (TIM)
- Weldable power terminals
- Cu clip technology
- Ag sinter technology for die mounting
- Higher power cycling capability
- 4.2kV DC 1s insulation

### Construction

The power module is a half bridge module which implements SiC-MOSFETs.

### Application

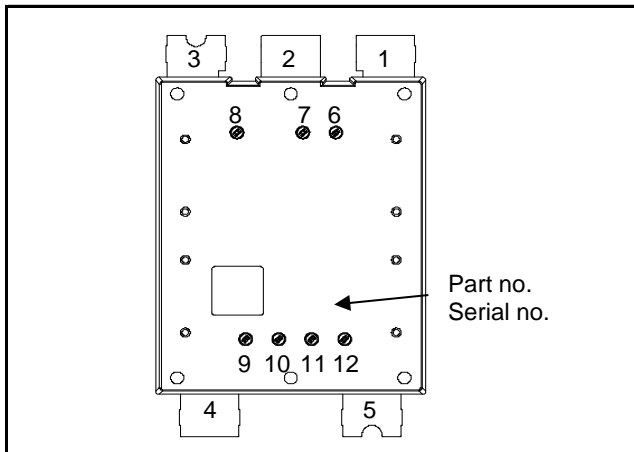
- Automotive application
- Inverter, Converter
- (Hybrid) electrical vehicles EV/HEV



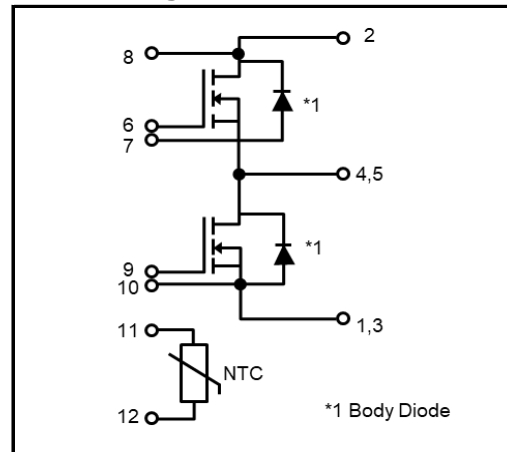
"TRCDRIVE pack™" is a trademark or a registered trademark of ROHM Co., Ltd.

"EcoSiC™" is a trademark or a registered trademark of ROHM Co., Ltd.

### Outline



### Circuit diagram



Pin No.	Pin Name	Function
1	N	Negative Power
2	P	Positive Power
3	N	Negative Power
4	OUT	Output (S1D2)
5	OUT	Output (S1D2)
6	G1	High side MOSFET Gate
7	Ss1	High side MOSFET Source Sense
8	Ds1	High side MOSFET Drain Sense
9	G2	Low side MOSFET Gate
10	Ss2	Low side MOSFET Source Sense
11	NTC1	NTC
12	NTC2	NTC

**Absolute maximum ratings (Tvj = 25°C unless otherwise specified)**

Parameter	Symbol	Conditions	Rating	Unit
Drain - source voltage	$V_{DSS}$	$V_{GS} = 0V$	1200	V
Gate - source voltage (DC)	$V_{GSS}$		-4 to +21	
Gate - source voltage ( $t_{surge} < 300ns$ )	$V_{GSSsurge}$		-6 to +23	
Continuous drain current (DC)	$I_D$	$Tvj = 175^\circ C, Tc = 60^\circ C, V_{GS} = 18V$	394	A
Pulsed drain current	$I_{D,pulse}$	Pulse 1ms, $Tvj = 175^\circ C, V_{GS} = 18V$ <sup>Note 2), 5)</sup>	788	
Continuous source current (DC)	$I_S$	$Tvj = 175^\circ C, V_{GS} = 18V$	394	
Pulsed source current	$I_{S,pulse}$	Pulse 1ms, $Tvj = 175^\circ C, V_{GS} = 18V$ <sup>Note 2)</sup>	788	
Body diode surge forward current	$I_{S,pulse}$	Pulse 1.5 $\mu s$ , $Tvj = 175^\circ C, V_{GS} = 0V$ <sup>Note 2), 4), 5)</sup>	788	
Total power dissipation <sup>Note 3), 5)</sup>	$P_{tot}$	$Tc = 25^\circ C$	1667	W
Virtual junction temperature	$Tvj$		-40 to +175	°C
Storage temperature	$T_{stg}$		-40 to +125	

Note 1) If the product is used beyond absolute maximum ratings defined in the specifications, as its internal structure may be damaged, please replace the product with a new one.

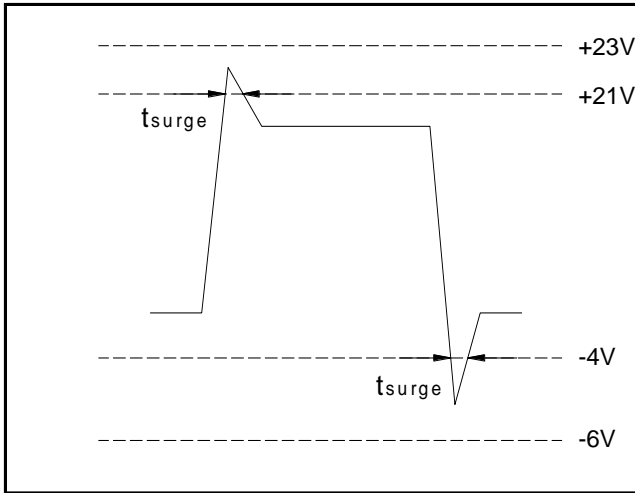
Note 2) Repetition rate should be kept within the range where temperature rise if die should not exceed  $Tvj_{max}$ .

Note 3) Case temperature ( $Tc$ ) is defined on the mounting side surface of copper plate.

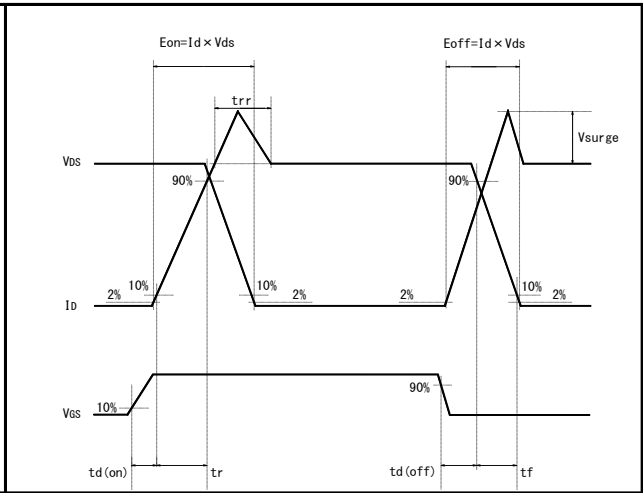
Note 4) Repetitive pulse,  $PW \leq 1.5\mu s$ , Duty cycle  $\leq 5\%$

Note 5)  $Tvj$  is less than  $175^\circ C$ .

<Example of acceptable  $V_{GS}$  waveform>



<Wavelength for switching test>



**Module ( $T_{vj} = 25^{\circ}\text{C}$  unless otherwise specified)**

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Isolation test voltage	$V_{isol}$	Terminals to baseplate DC 1s	4200	—	—	V
Stray inductance	$L_s$		—	5.7	—	nH
Comparative tracking index	CTI		400 ≤ CTI < 600			—
Creepage distance	—	Terminal to heat sink	8.0	—	—	mm
		Terminal to terminal	7.4	—	—	
Clearance	—	Terminal to heat sink	6.4	—	—	mm
		Terminal to terminal	5.1	—	—	
Thermal resistance, junction - case	$R_{th(j-c)}$	1/2 module <sup>Note 3)</sup>	—	—	90	$^{\circ}\text{C}/\text{kW}$
Module lead resistance, terminals - chip	$R_{DD'+SS'}$		—	0.29	—	mΩ
Weight	G		—	52	—	g

**MOSFET electrical characteristics (Tvj = 25°C unless otherwise specified)**

Parameter	Symbol	Conditions	Values			Unit	
			Min.	Typ.	Max.		
Drain - source on resistance	R <sub>DS(on)</sub>	I <sub>D</sub> = 350A, V <sub>GS</sub> = 18V	Tvj = 25°C	—	2.8	3.5	mΩ
			Tvj = 150°C	—	5.3	—	
			Tvj = 175°C	—	6.6	8.6	
Zero gate voltage drain current	I <sub>DSS,leak</sub>	V <sub>DS</sub> = 1200V, V <sub>GS</sub> = 0V	—	—	320	μA	
Gate - source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 150mA <sup>Note 6)</sup>	2.8	—	4.8	V	
Gate - source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = +21V, V <sub>DS</sub> = 0V	—	—	0.4	μA	
		V <sub>GS</sub> = -4V, V <sub>DS</sub> = 0V	-0.4	—	—		
Turn - on delay time	t <sub>d(on)</sub>	V <sub>GS(on)</sub> = 18V, V <sub>GS(off)</sub> = 0V V <sub>DS</sub> = 800V I <sub>D</sub> = 350A R <sub>G(on)</sub> = 6.8Ω, R <sub>G(off)</sub> = 3.3Ω Inductive load	—	119	—	ns	
Rise time	t <sub>r</sub>		—	102	—		
Turn - off delay time	t <sub>d(off)</sub>		—	198	—		
Fall time	t <sub>f</sub>		—	42	—		
Turn - on switching loss	E <sub>on</sub>		—	46	—		mJ
Turn - off switching loss	E <sub>off</sub>	—	18	—			
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 800V, V <sub>GS</sub> = 0V, 1MHz	—	31	—	nF	
Total gate charge	Q <sub>g</sub>	V <sub>GS(on)</sub> = 18V, V <sub>GS(off)</sub> = 0V V <sub>DS</sub> = 800V I <sub>D</sub> = 272A	—	1158	—	nC	
Gate - source charge	Q <sub>GS</sub>		—	334	—		
Gate - drain charge	Q <sub>gd</sub>		—	180	—		
Internal gate resistance	R <sub>Gint</sub>	Tvj = 25°C	—	0.25	—	Ω	

**Body diode electrical characteristics (Tvj = 25°C unless otherwise specified)**

Parameter	Symbol	Conditions	Values			Unit	
			Min.	Typ.	Max.		
Souce - drain voltage	V <sub>F,SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 350A	Tvj = 25°C	—	3.7	—	V
			Tvj = 150°C	—	3.9	—	
			Tvj = 175°C	—	3.9	—	
Reverse recovery time	t <sub>rr</sub>	V <sub>GS(on)</sub> = 18V, V <sub>GS(off)</sub> = 0V V <sub>DS</sub> = 800V I <sub>D</sub> = 350A R <sub>G(on)</sub> = 6.8Ω, R <sub>G(off)</sub> = 3.3Ω Inductive load	—	37	—	ns	
Recoverd charge	Q <sub>rr</sub>		—	2445	—	nC	
Peak reverse recovery current	I <sub>rr</sub>		—	110	—	A	
Reverse recovery energy	E <sub>rr</sub>		—	0.1	—	mJ	

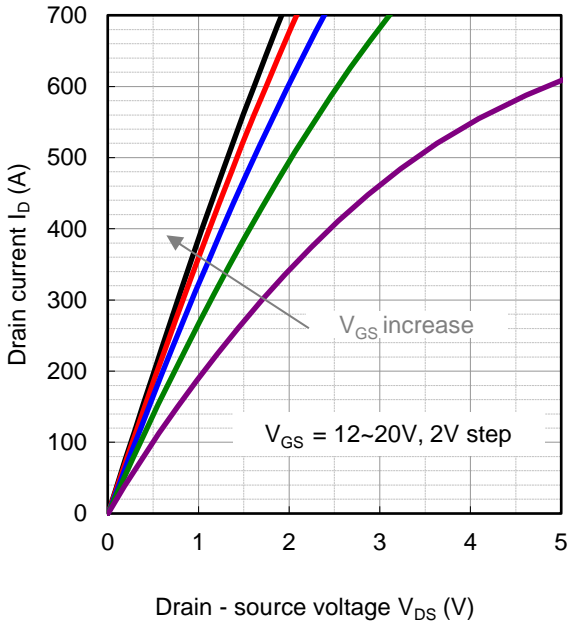
**NTC Thermistor electrical characteristics (Tvj = 25°C unless otherwise specified)**

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
NTC rated resistance	R <sub>25</sub>	Tc = 25°C	—	4.950	—	kΩ
	R <sub>98</sub>	Tc = 98°C	—	0.5185	—	
Deviation of R25	ΔR/R	Tc = 25°C	-5	—	5	%
Power dissipation	P <sub>25</sub>	Tc = 25°C	—	—	50	mW
NTC B Value	B <sub>25/98</sub>	R = R <sub>25</sub> exp [B <sub>25/98</sub> (1/T - 1/(298.15K))]	—	3420	—	K

- Note 6) Tested after applying  $V_{GS} = 21V$  for 100ms.
- Note 7) Not include the resistance from terminals to chip.
- Note 8) SiC devices have lower short circuit withstand capability due to high current density. Please be advised to pay careful attention to short circuit accident and try to adjust protection time to shutdown them as short as possible.

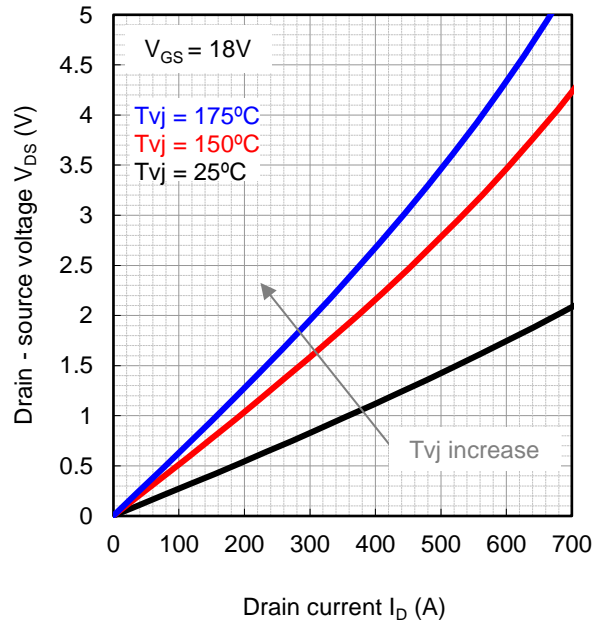
Electrical characteristic curves (Typical)

Figure 1. Output characteristic at 25°C (Typ.)



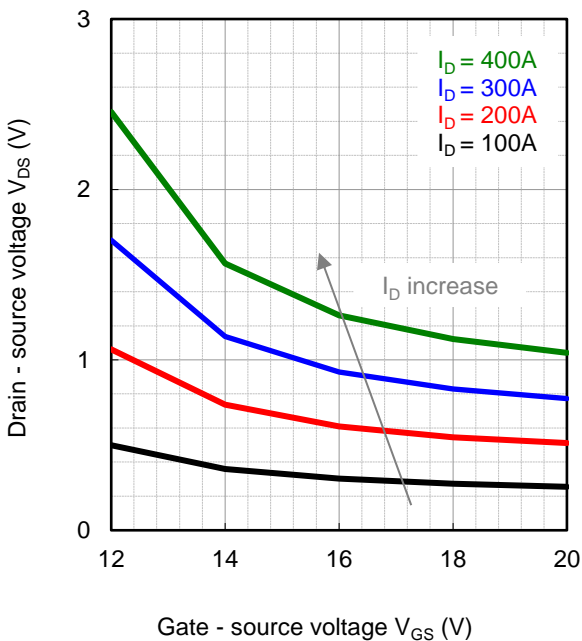
Note 7)

Figure 2. Drain - source voltage characteristic (Typ.)



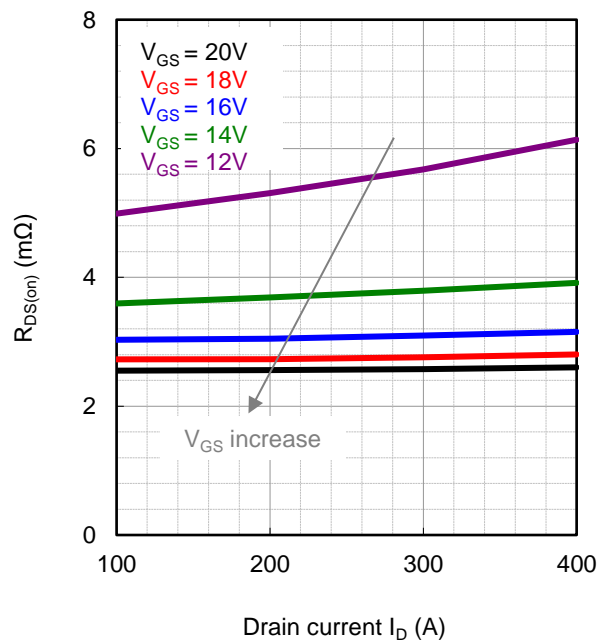
Note 7)

Figure 3. Drain - source voltage characteristic at 25°C (Typ.)



Note 7)

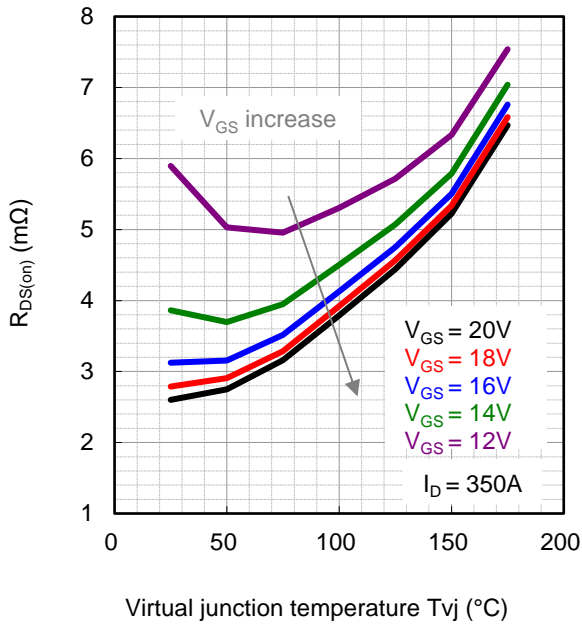
Figure 4. Drain current vs.  $R_{DS(on)}$  at 25°C (Typ.)



Note 7)

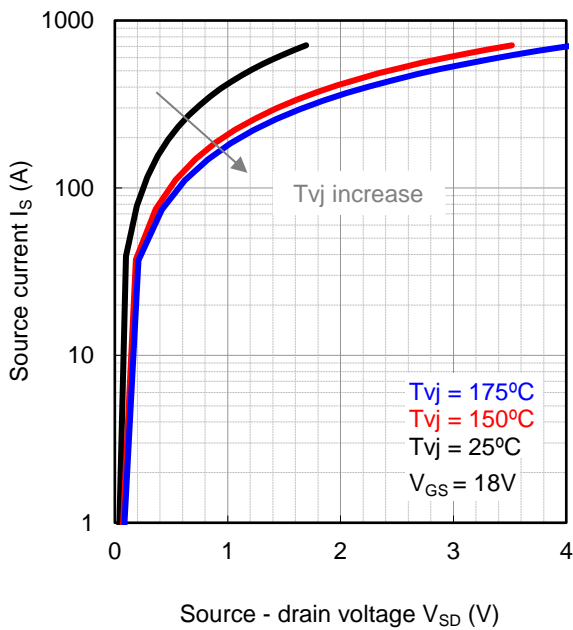
Electrical characteristic curves (Typical)

Figure 5.  $R_{DS(on)}$  vs.  $T_{vj}$  characteristic (Typ.)



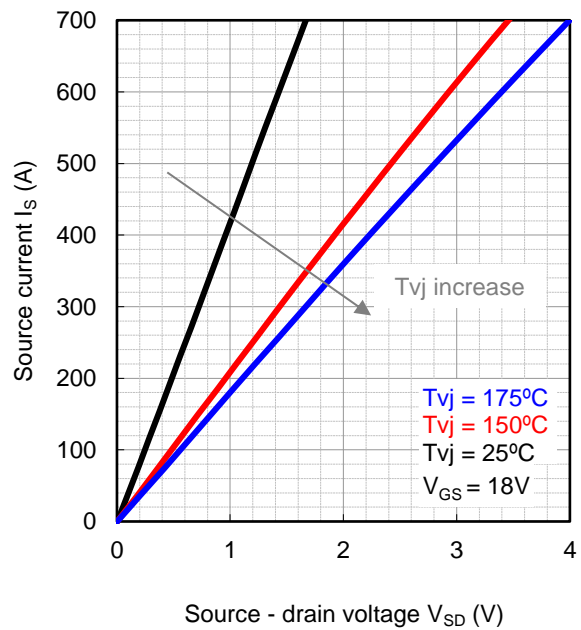
Note 7)

Figure 6. Forward characteristic of diode  $V_{GS} = 18V$  (Typ.)



Note 7)

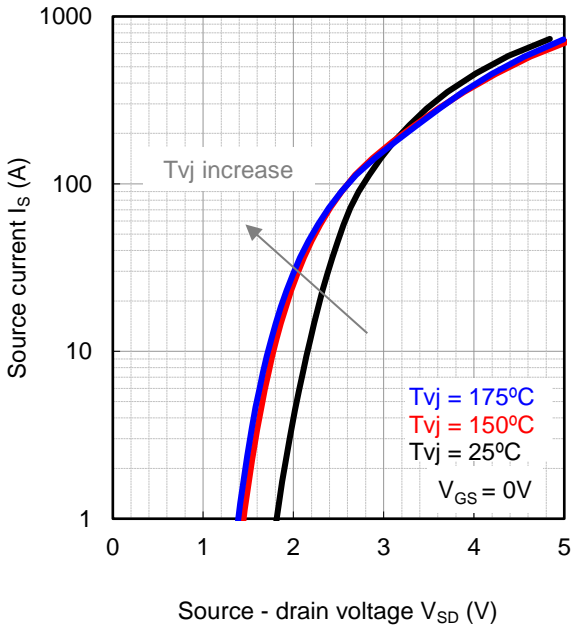
Figure 7. Forward characteristic of diode  $V_{GS} = 18V$  (Typ.)



Note 7)

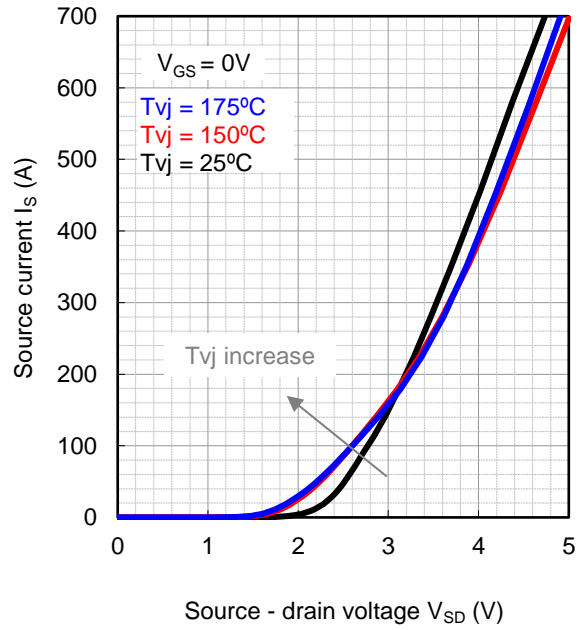
Electrical characteristic curves (Typical)

Figure 8. Forward characteristic of diode  
 $V_{GS} = 0V$  (Typ.)



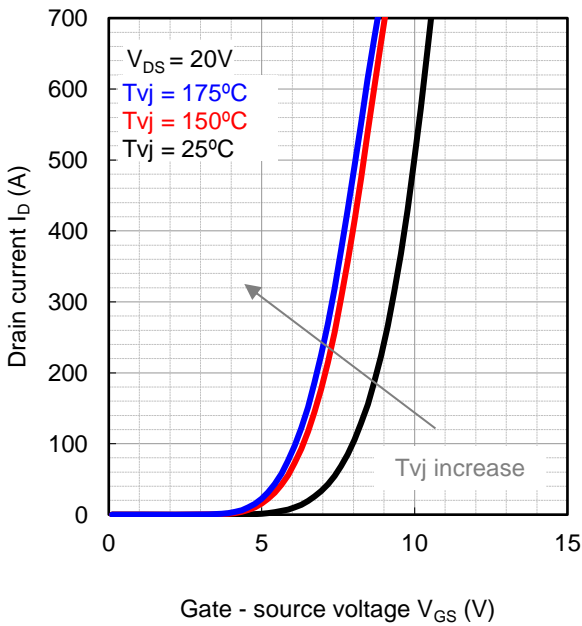
Note 7)

Figure 9. Forward characteristic of diode  
 $V_{GS} = 0V$  (Typ.)



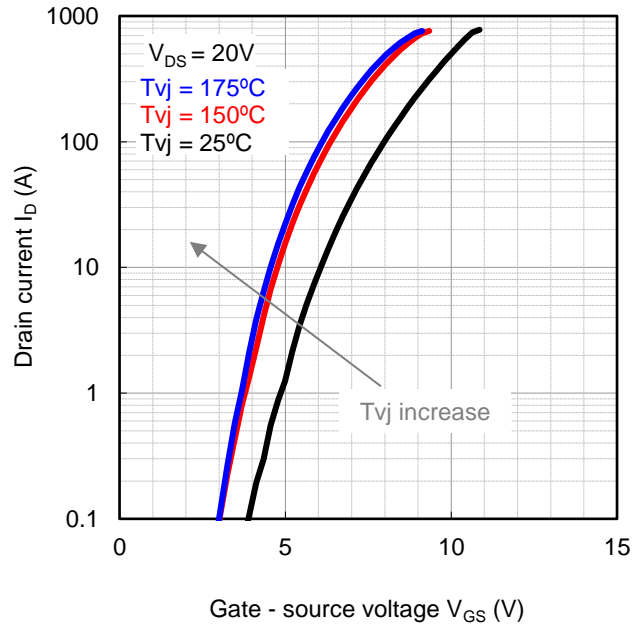
Note 7)

Figure 10. Drain current vs. Gate-source voltage (Typ.)



Note 7)

Figure 11. Drain current vs. Gate-source voltage (Typ.)

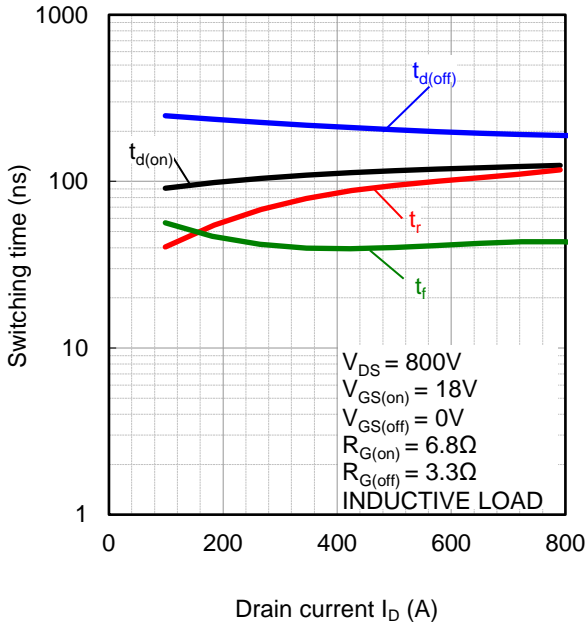


Note 7)



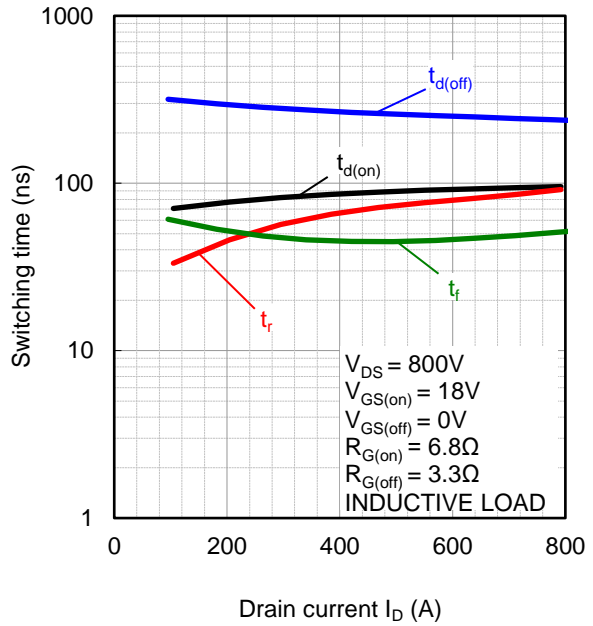
Electrical characteristic curves (Typical)

Figure 12. Switching time vs. Drain current at 25°C (Typ.)



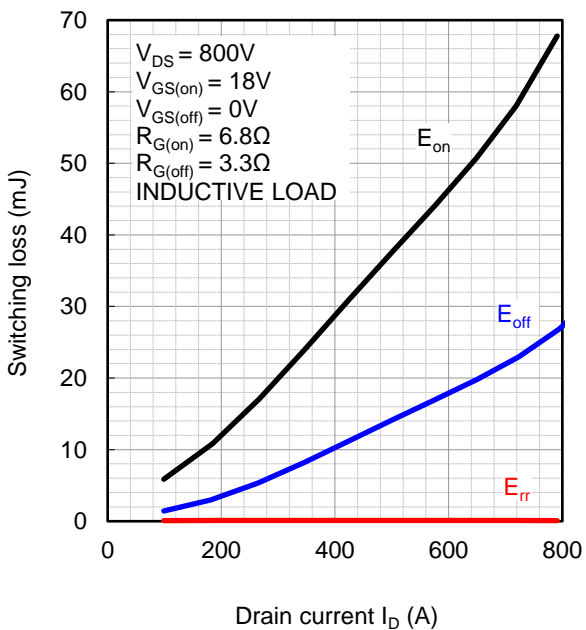
Note 7)

Figure 13. Switching time vs. Drain current at 175°C (Typ.)



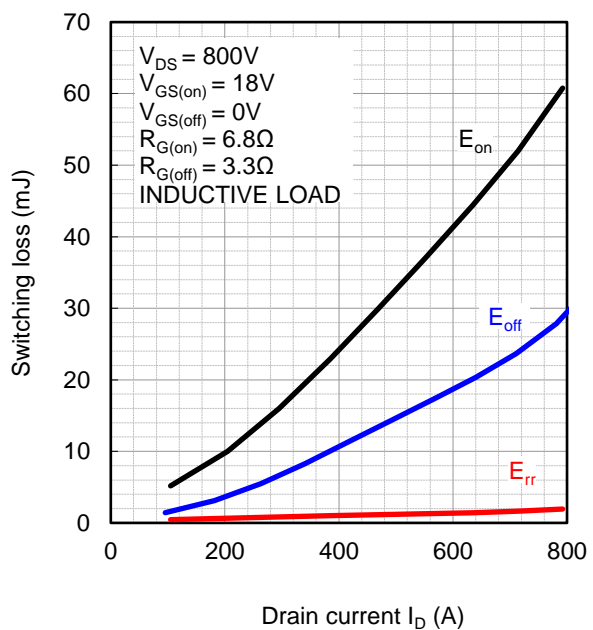
Note 7)

Figure 14. Switching loss vs. Drain current at 25°C (Typ.)



Note 7)

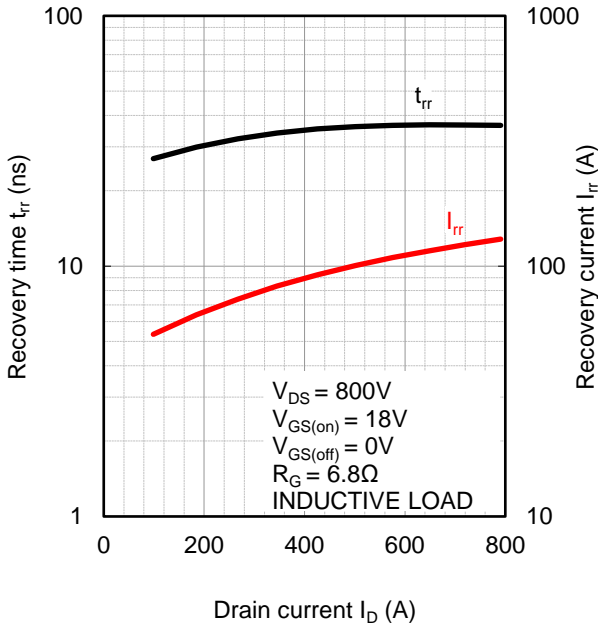
Figure 15. Switching loss vs. Drain current at 175°C (Typ.)



Note 7)

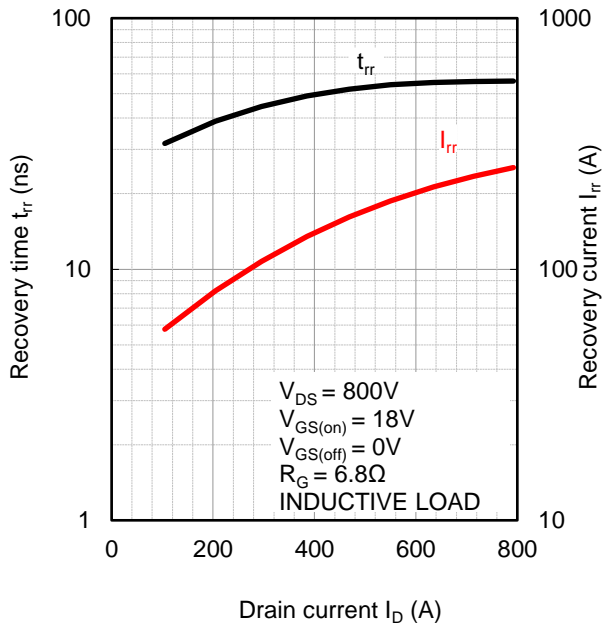
Electrical characteristic curves (Typical)

Figure 16. Recovery characteristic vs. Drain current at 25°C (Typ.)



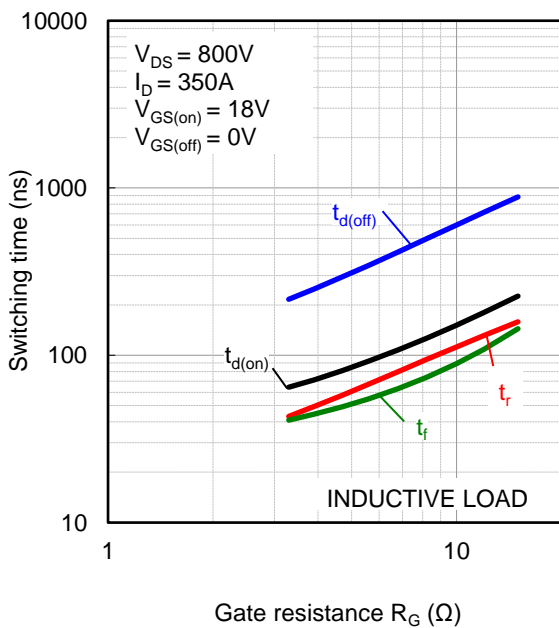
Note 7)

Figure 17. Recovery characteristic vs. Drain current at 175°C (Typ.)



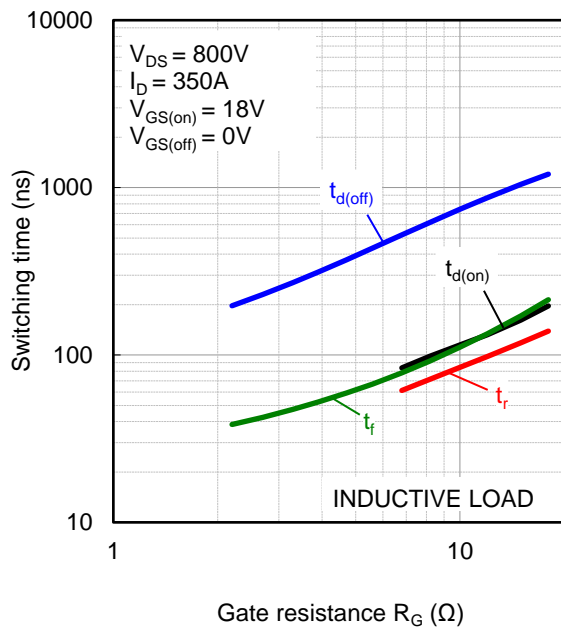
Note 7)

Figure 18. Switching time vs. Gate resistance at 25°C (Typ.)



Note 7)

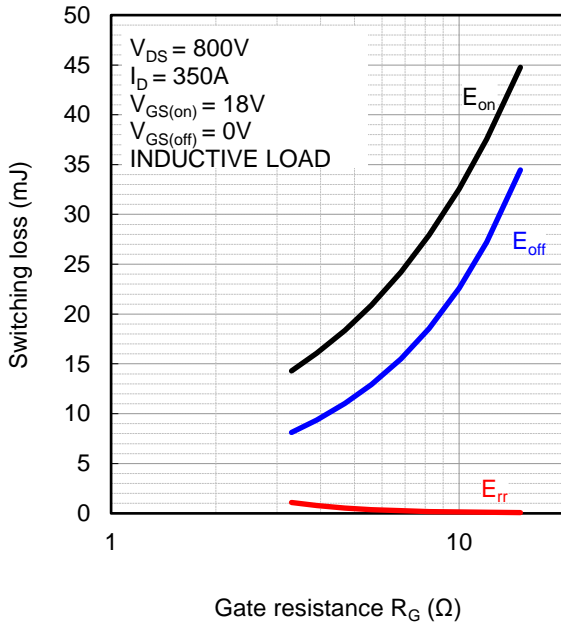
Figure 19. Switching time vs. Gate resistance at 175°C (Typ.)



Note 7)

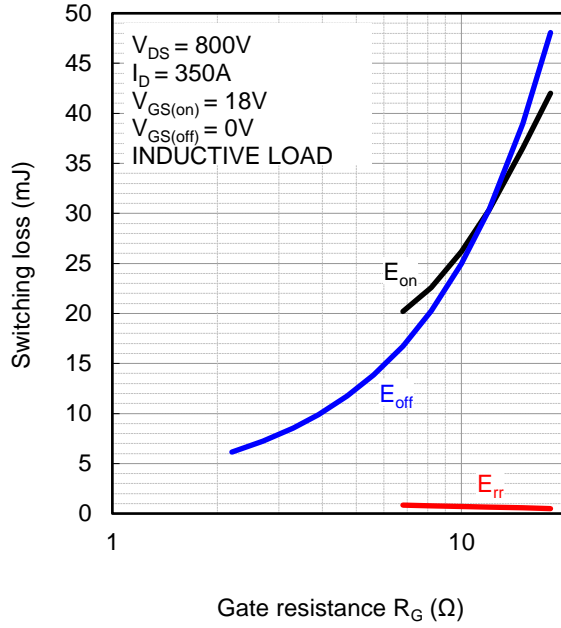
Electrical characteristic curves (Typical)

Figure 20. Switching loss vs. Gate resistance at 25°C (Typ.)



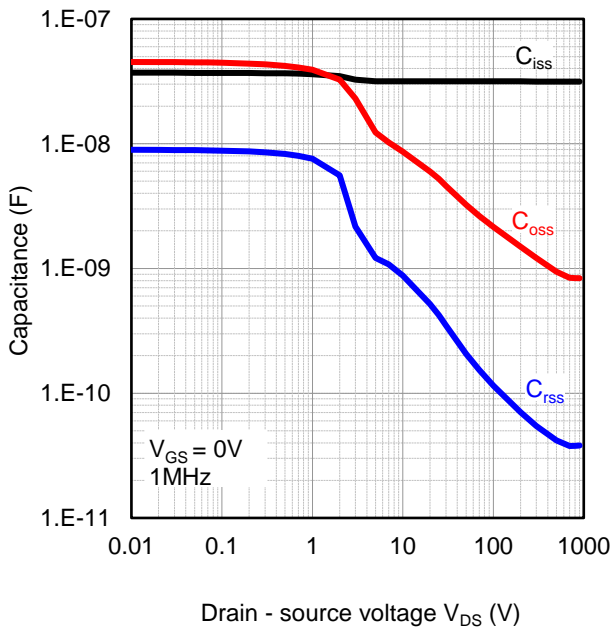
Note 7)

Figure 21. Switching loss vs. Gate resistance at 175°C (Typ.)



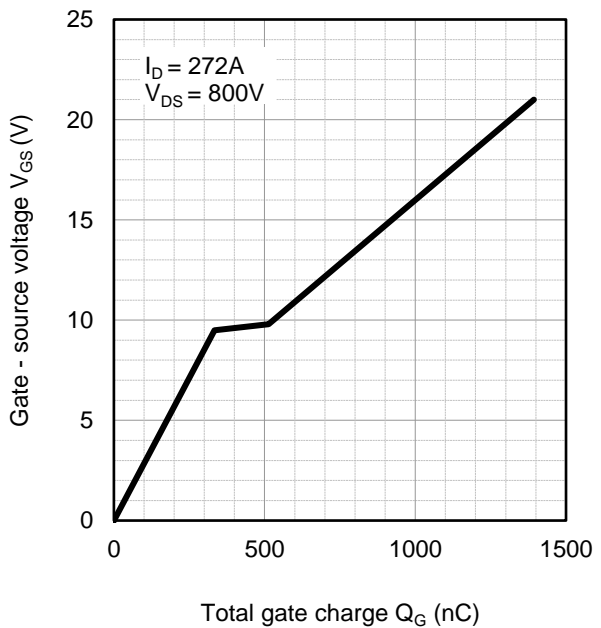
Note 7)

Figure 22. Capacitance vs. Drain - source voltage at 25°C (Typ.)



Note 7)

Figure 23. Gate charge characteristic at 25°C (Typ.)



Note 7)

Electrical characteristic curves (Typical)

Figure 24. Transient thermal impedance (Typ.)

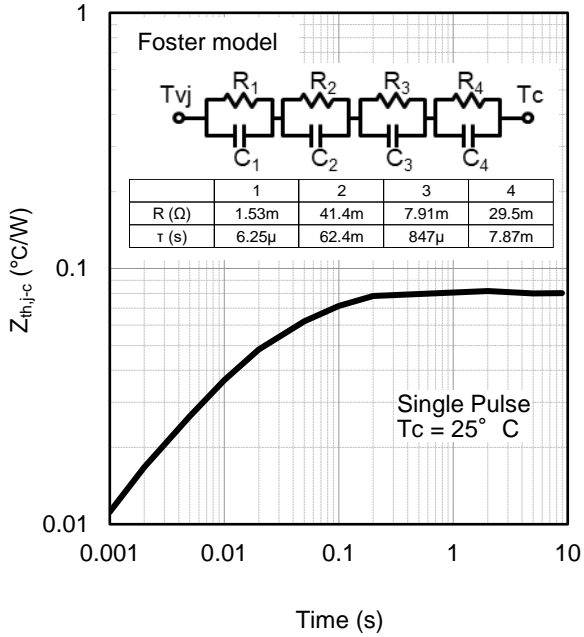


Figure 25. Maximum safe operating area

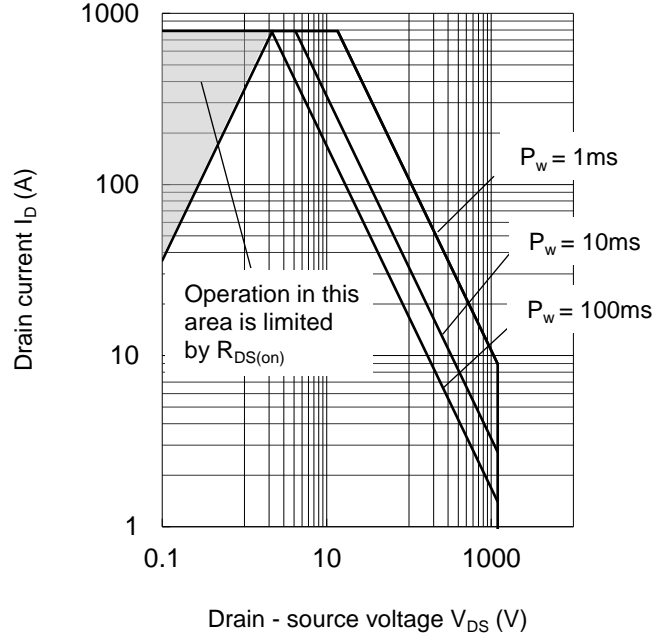


Figure 26. NTC Thermistor temperature characteristic (Typ.)

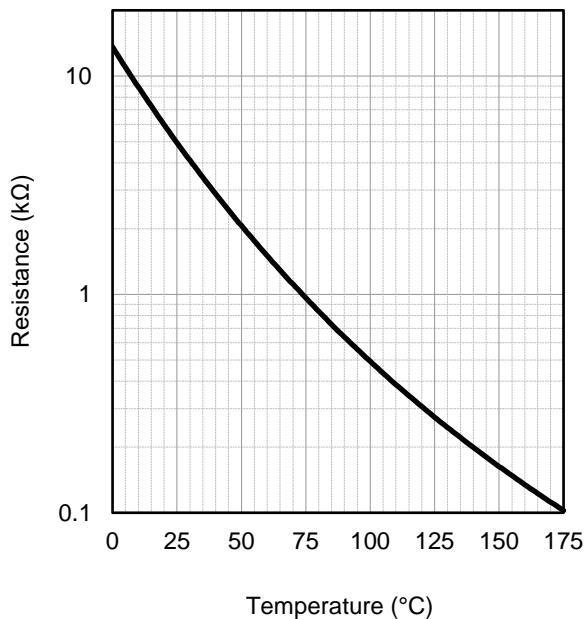
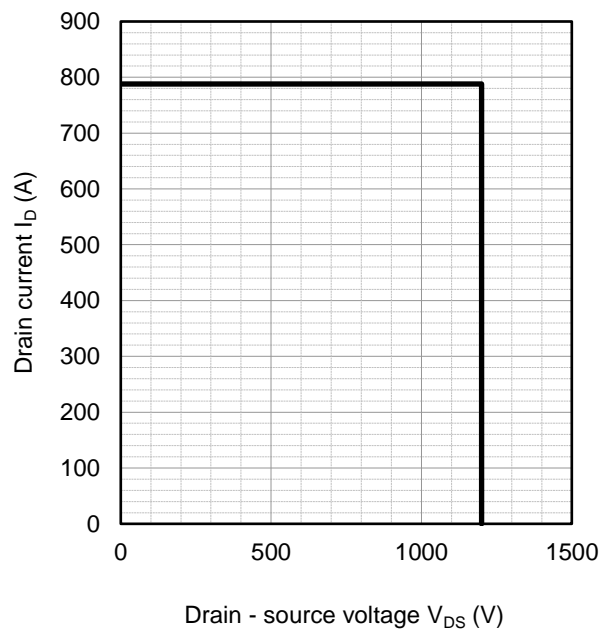
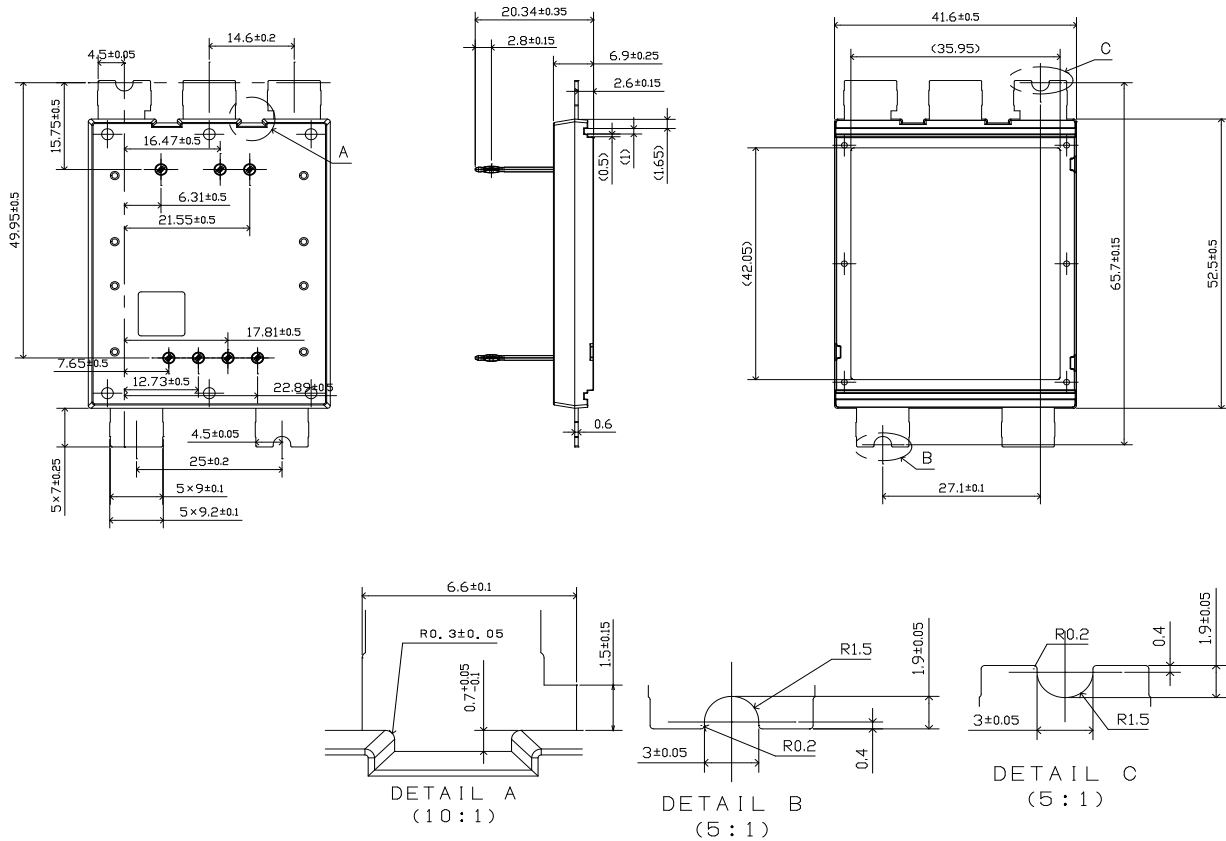


Figure 27. MOSFET RBSOA (Typ.)



Package outlines

Unit : mm



## Notes

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