Automotive Grade SiC Power Module

BST47B1P4K01-VC Datasheet

Features

- HSDIP20 package with the 4th Generation SiC-MOSFET
- $\cdot V_{DSS} = 750V$
- · Low R_{DS(on)}
- · High-speed switching possible
- · Low switching losses
- Tvjmax = 175°C
- · Compact design
- · With high thermal conductivity isolation
- · Integrated NTC temperature sensor
- · 4.2kV AC 1s insulation

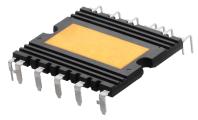
Construction

The power module is a full bridge module which implements SiC-MOSFETs.

Application

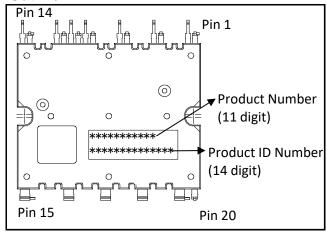
- · Automotive application
- · Inverter, Converter
- · (Hybrid) electrical vehicles EV/HEV



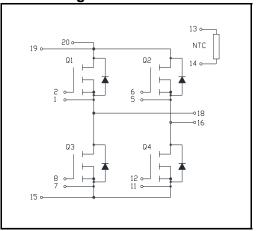


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Outline



Circuit diagram



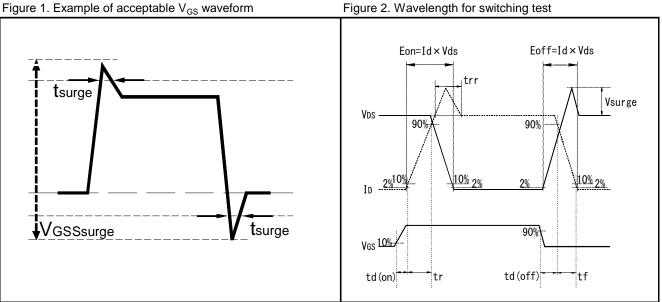
Pin No.	Pin Name	Function	
1	S1	MOSFET Source	
2	G1	MOSFET Gate	
3	N.C Non connect		
4	N.C	Non connect	
5	S2	MOSFET Source	
6	G2	MOSFET Gate	
7	S3	MOSFET Source	
8	G3	MOSFET Gate	
9	N.C	Non connect	
10	N.C	Non connect	

Pin No.	Pin Name	Function
11	S4	MOSFET Source
12	G4	MOSFET Gate
13	T1	Thermistor
14	T2	Thermistor
15	N	Negative power
16	W	Output
17	N.C	Non connect
18	U	Output
19	Р	Positive Power
20	Ps	Positive Power sense

Absolute maximum ratings (Tvj = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Rating	Unit
Drain - source voltage	V_{DSS}	V _{GS} = 0V	750	
Gate - source voltage (DC)	V_{GSS}		-4 to +21	V
Gate - source voltage (t _{surge} < 300ns)	$V_{GSSsurge}$		-4 to +23	V
Continuous dusin surrent (DC)		Tc = 25°C, V _{GS} = 18V	47	
Continuous drain current (DC)	I _D	Tc = 100°C, V _{GS} = 18V	33	
Pulsed drain current	1	Pulse 1ms, Tc = 25°C, V _{GS} = 18V Note 2), 5)	90	
Fulsed drain current	I _{D,pulse}	Pulse 1ms, Tc = 100°C, V _{GS} = 18V Note 2), 5)	64	Α
Continuous source current (DC)	I _S	Tc = 25°C, V _{GS} = 18V	47	
Pulsed source current	I _{S,pulse}	Pulse 1.5µs, Tc = 25°C, V _{GS} = 18V Note 2)	90	
Body diode surge forward current	I _{S,pulse}	Pulse 1.5 μ s, Tc = 100°C, V _{GS} = 0V ^{Note 2), 4), 5)}	56	
Total power dissipation Note 3), 5)	Ptot	Tc = 25°C	227	W
Virtual junction temperature	Tvj		-40 to +175	°C
Storage temperature	Tstg		-40 to +125	

- Note 1) If the product is used beyond absolute maximum ratings defined in the specifications, as its internal structure may be damaged, please replace the product with a new one.
- Repetition rate should be kept within the range where temperature rise if die should not exceed Tvjmax. Note 2)
- Note 3) Case temperature (Tc) is defined on the cooper surface just under the chips.
- Note 4) Repetitive pulse, PW≦1.5µs, Duty cycle≦5%
- Note 5) Tvj is less than 175°C.



Module (Tvj = 25°C unless otherwise specified)

Parameter	Cumbal	Symbol Conditions	Values			Unit
Parameter	Symbol	Conditions		Тур.	Max.	Offic
Isolation test voltage	V_{isol}	All terminals to baseplate AC 60Hz 1sec.	4200	_	_	V_{rms}
Stray inductance	L _s	Terminal P to Terminal N	_	46	_	nΗ
Croopaga diatanga	_	Terminal to heat sink	13.9	_	_	mm
Creepage distance	_	Terminal to terminal	8.2	_	_	
Clearance distance	-	Terminal to heat sink	10.2	_	_	mm
Clearance distance		Terminal to terminal	4.3	_	_	
Module flatness (Heatsink side)	_	Measurement point is shown in Figure 3.	0	_	100	μm
Mounting torque	_	Mounting to heatsink with M3 screw Note 6)	0.59	0.69	0.78	N∙m
Terminal pulling strength	_	Load:4.9N(Control terminal), 9.8N(Power terminal) Note 7)	10	_	_	S
Terminal bending strength	ı	Load:2.45N (Control terminal), 4.9N(Power terminal) Note7)	2	ı	1	time
Thermal resistance, junction - case	R _{th(j-c)}	1 arm heating Note 3)	_	0.48	0.66	°C/W

Note 6) 8 mm (outside diameter) plain washers (ISO 7089 to 7094) are recommended.

Note 7) EIAJ-ED-4701/400

Note 8) Rth(j-c) was measured after 1chip heating. Heatsink temperature was keep in 25°C.

The Rth(j-c) result was calculated from measured structure function, based on JESD51-14 guideline.

100µm thickness of thermally conductive grease was introduced between module and heatsink and fastened with two M4 screws with 0.85N·m torque.

Note 9) When installing a module to a heat sink, excessive uneven fastening force might apply stress to inside chips or ceramic of heat sink plate, which will break or crack or degrade a module.

An example of recommended fastening sequence is shown in Figure 5. The temporary fastening torque is set to 20 to 30% of the maximum torque rating.

Figure 3. Measurement point of module flatness

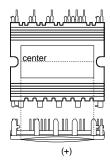


Figure 4. Flatness after installing to a heatsink (when using a heat radiation sheet)

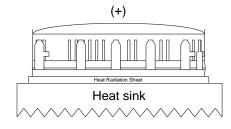
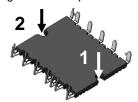


Figure 5. Example of recommended fastening sequence



Temporary fastening: 1→2 Permanent fastening: 1→2

MOSFET electrical characteristics (Tvj = 25°C unless otherwise specified)

Parameter	Symbol	ool Conditions -		Values			Unit
- Farameter	Symbol			Min.	Тур.	Max.	Offic
		$I_D = 29A, V_{GS} = 18V$	Tvj = 25°C	_	26	_	
Drain - source on resistance	$R_{DS(on)}$	I _D = 47A, V _{GS} = 18V	Tvj = 25°C	_	28	37	mΩ
rodictarios		$I_D = 47M$, $V_{GS} = 10V$	Tvj = 175°C	_	60	_	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 750V, V _{GS} = 0V		_	_	80	μΑ
Drain-Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 9.2 \text{mA}, T_{vj} = 25 ^{\circ}\text{C}$		750	_	_	V
Gate - source threshold voltage	$V_{GS(th)}$	$V_{DS} = 10V$, $I_D = 15.4$ mA $^{Note \ 12)}$		2.8	ı	4.8	V
Gate - source	I _{GSS}	$V_{GS} = +21V, V_{DS} = 0V$ $V_{GS} = -4V, V_{DS} = 0V$		ı	_	0.1	μА
leakage current	GSS			-0.1	_	_	
Turn - on delay time	$t_{d(on)}$	$V_{GS(on)} = 18V, V_{GS(off)} = 0V$ $V_{DS} = 400V$ $I_{D} = 47A$ $R_{G(on)} = 15\Omega, R_{G(off)} = 15\Omega$		ı	24	_	ns
Rise time	t _r			ı	23	_	
Turn - off delay time	$t_{d(off)}$			-	103	_	
Fall time	t _f			-	15	_	
Turn - on switching loss	E_{on}	Inductive load	-	_	0.3	_	
Turn - off switching loss	E_{off}			_	0.39	_	mJ
Input capacitance	C _{iss}	$V_{DS} = 500V, V_{GS} = 0V, 1MHz$		_	2.3	_	nF
Total gate charge	Q_g	$V_{GS(on)} = 18V, V_{GS(off)} = 0V$ $V_{DS} = 500V$ $I_{D} = 30A$		_	97	_	
Gate - source charge	Q_gs			-	25	_	nC
Gate - drain charge	Q_{gd}			_	18	_	
Internal gate resistance	R_{Gint}	Tvj = 25°C		-	1		Ω

- Note 10) Evenly apply thermally-conductive grease with 100µm to 200µm thickness over the contact surface between the module and the heat sink. Pay attention not to have any dirt left on the contact surface between the module and the heat sink.
 - It is recommended to install a module directly to a heat sink after applying grease.
- Note 11) When installing a module to a heat sink, inserting a heat radiation sheet between a module and a heat sink might apply stress depending on thickness and elastic modulus of the sheet to inside chips or ceramic of heat sink plate, which will break or crack or degrade a module.

 When using a heat radiation sheet, it is needed to prevent power module from heading into + side of
 - When using a heat radiation sheet, it is needed to prevent power module from bending into + side of Figure 4.
- Note 12) Tested after applying $V_{GS} = 21V$ for 100ms.
- Note 13) SiC devices have lower short circuit withstand capability due to high current density.

 Please be advised to pay careful attention to short circuit accident and try to adjust protection time to shutdown them as short as possible.



Body diode electrical characteristics (Tvj = 25°C unless otherwise specified)

Parameter	Symbol Conditions				Values		Unit
	Symbol	Conditions		Min.	Тур.	Max.	Offic
	V _{SD} -	$V_{GS} = 0V, I_{S} = 47A$	Tvj = 25°C	_	4.3	_	
Souce - drain			Tvj = 175°C	_	4.6	_	\ \/
voltage		V ₀₀ = 18V I ₀ = 4/A	Tvj = 25°C	1	1.2	-	v
			Tvj = 175°C	1	2.2	-	

NTC Thermistor electrical characteristics (Tvj = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Offic
NTC rated resistance	R ₂₅	Tc = 25°C	_	10	_	kΩ
NTC B Value	B _{50/25}		_	3380	1	K
Maximum operating current	1		ı	ı	0.1	mA

Figure 6. Output characteristic at 25°C (Typ.)

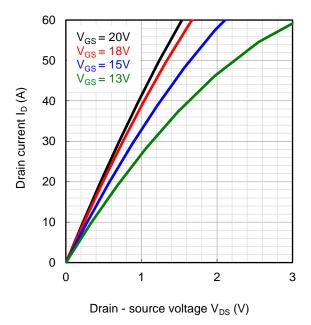


Figure 7. Drain - source voltage characteristic (Typ.)

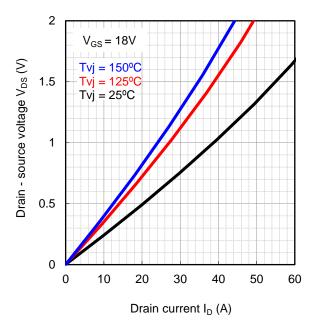


Figure 8. Drain - source voltage characteristic at 25°C (Typ.)

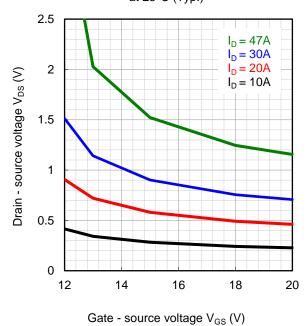
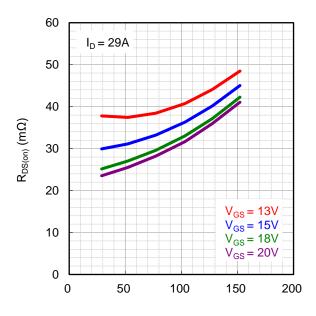


Figure 9. R_{DS(on)} vs. Tvj characteristic (Typ.)



Virtual junction temperature Tvj (°C)

Figure 10. Forward characteristic of diode $V_{GS} = 18V$ (Typ.)

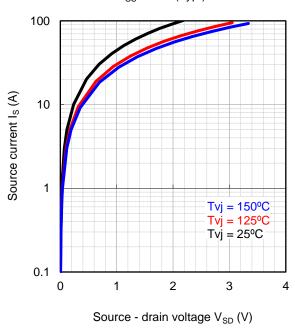
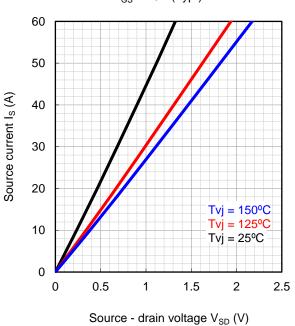
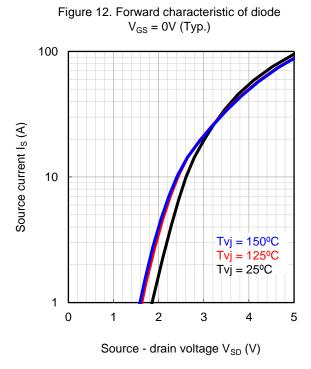


Figure 11. Forward characteristic of diode $V_{GS} = 18V$ (Typ.)





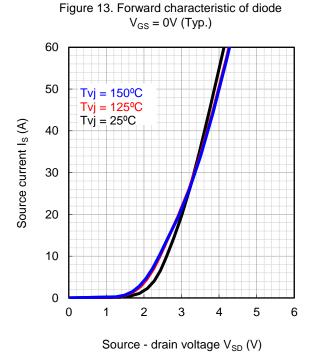
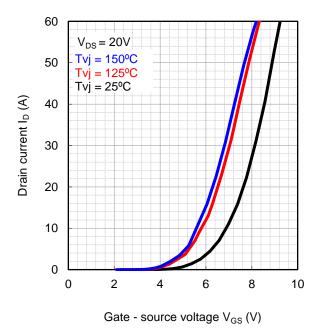


Figure 14. Drain current vs. Gate - source voltage (Typ.) Figure 15. Drain current vs. Gate - source voltage (Typ.)



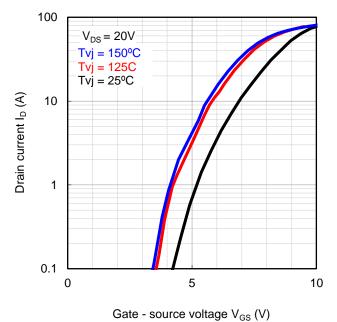


Figure 16. Switching time vs. Drain current at 25°C (Typ.) 1000 t_{d(off)} Switching time (ns) 100 10 $V_{DS} = 400V$ $\begin{aligned} R_{G(on)} &= 15\Omega \\ R_{G(off)} &= 15\Omega \\ \text{INDUCTIVE LOAD} \end{aligned}$ $V_{GS(on)} = 18V$ $V_{GS(off)} = 0V$ 0 20 40 60 80

at 150°C (Typ.) 1000 $t_{d(off)}$ Switching time (ns) 100 $t_{d(on)}$ 10 $V_{DS} = 400V$ $\begin{aligned} R_{G(on)} &= 15\Omega \\ R_{G(off)} &= 15\Omega \\ \text{INDUCTIVE LOAD} \end{aligned}$ $V_{GS(on)} = 18V$ $V_{GS(off)} = 0V$ 0 20 40 60 80

Figure 17. Switching time vs. Drain current

Figure 18. Switching loss vs. Drain current at 25°C (Typ.)

Drain current I_D (A)

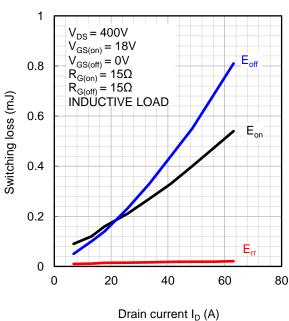


Figure 19. Switching loss vs. Drain current at 150°C (Typ.)

Drain current I_D (A)

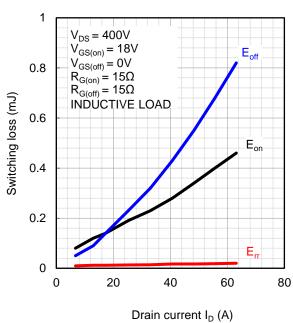


Figure 20. Recovery characteristic vs. Drain current at 25°C (Typ.)

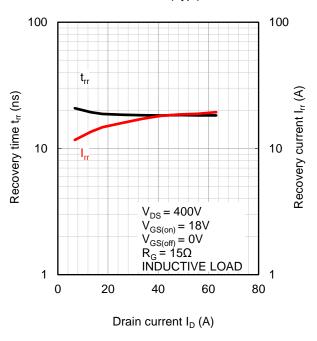


Figure 21. Recovery characteristic vs. Drain current at 150°C (Typ.)

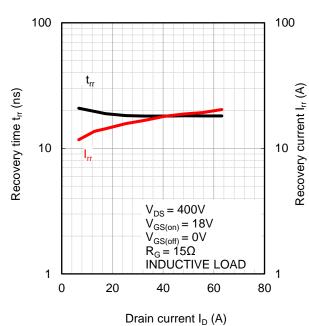


Figure 22. Switching time vs. Gate resistance at 25°C (Typ.)

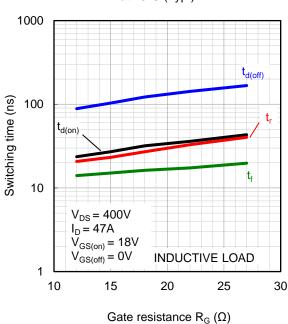


Figure 23. Switching time vs. Gate resistance at 150°C (Typ.)

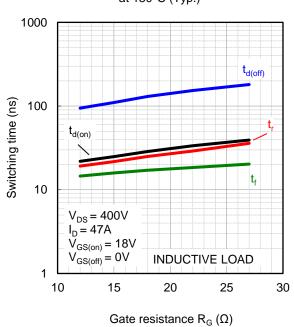


Figure 24. Switching loss vs. Gate resistance at 25°C (Typ.)

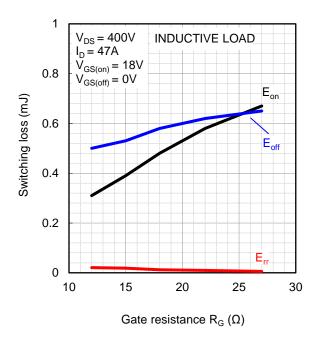


Figure 25. Switching loss vs. Gate resistance at 150°C (Typ.)

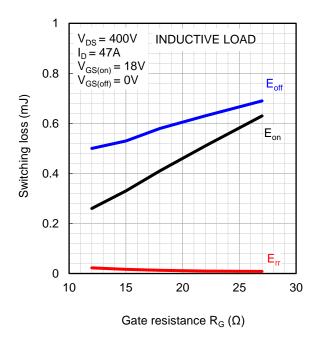


Figure 26. Capacitance vs. Drain - source voltage at 25°C (Typ.)

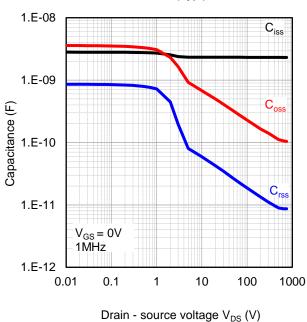


Figure 27. Gate charge characteristic at 25°C (Typ.)

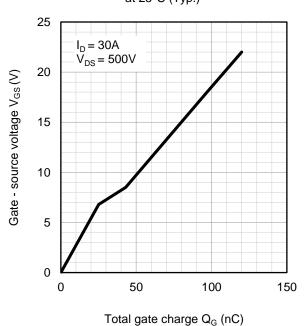
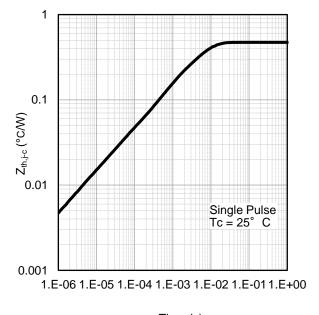
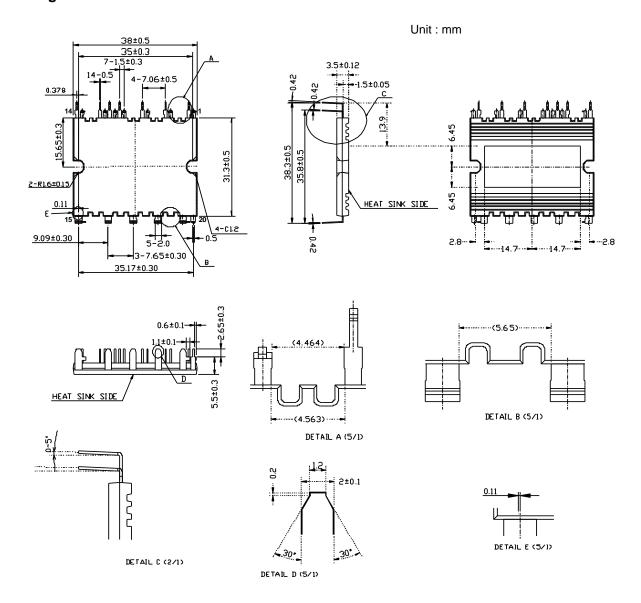


Figure 28. Transient thermal impedance (Typ.)



Time (s)

Package outlines



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