

PNP -100mA -50V Digital Transistors (Bias Resistor Built-in Transistors)

•Outline

(3)

DTA114WM

(VMT3)

Datasheet

(3)

DTA114WUA

(UMT3)

SOT-323

| Parameter | Value |
|----------------------|--------|
| V _{CC} | -50V |
| I _{C(MAX.)} | -100mA |
| R ₁ | 10kΩ |
| R ₂ | 4.7kΩ |

Features

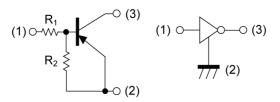
- 1) Built-In Biasing Resistors
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.

Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

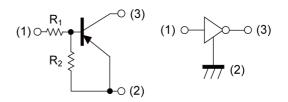
Inner circuit

DTA114WM



(1) IN (BASE)(2) GND (+) (EMITTER)(3) OUT (COLLECTOR)

DTA114WUA



(1) IN (BASE) (2) GND (+) (EMITTER) (3) OUT (COLLECTOR)

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|-----------|-------------------|-----------------|----------------|-------------------|--------------------|---------------------------------|---------|
| DTA114WM | SOT-723 (VMT3) | 1212 | T2L | 180 | 8 | 8000 | 74 |
| DTA114WUA | SOT-323 (UMT3) | 2021 | T106 | 180 | 8 | 3000 | 74 |

Packaging specifications

• Absolute maximum ratings ($T_a = 25^{\circ}C$)

| Parameter | | | Values | Unit |
|------------------------------|-----------|------------------|-------------|------|
| Supply voltage | | | -50 | V |
| Input voltage | | | -30 to 10 | V |
| Collector current | | | -100 | mA |
| Power dissipation | DTA114WM | D *2 | 150 | |
| | DTA114WUA | | 200 | — mW |
| Junction temperature | | Tj | 150 | °C |
| Range of storage temperature | | T _{stg} | -55 to +150 | °C |

•Electrical characteristics (T_a = 25°C)

| Deremeter | Cumb al | Conditions | Values | | | Unit | |
|----------------------|---------------------|---|--------|------|------|------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| Innutvoltogo | V _{I(off)} | V _{CC} = -5V, I _O = -100µA | - | - | -0.8 | - V | |
| Input voltage | V _{I(on)} | V _O = -0.3V, I _O = -2mA | -3.0 | - | - | | |
| Output voltage | V _{O(on)} | I _O = -10mA, I _I = -0.5mA | - | -100 | -300 | mV | |
| Input current | I | V _I = -5V | - | - | -880 | μA | |
| Output current | I _{O(off)} | V _{CC} = -50V, V _I = 0V | - | - | -500 | nA | |
| DC current gain | GI | V _O = -5V, I _O = -10mA | 24 | - | - | - | |
| Input resistance | R ₁ | - | 7 | 10 | 13 | kΩ | |
| Resistance ratio | R_2/R_1 | - | 0.37 | 0.47 | 0.57 | - | |
| Transition frequency | f _T *1 | V _{CE} = -10V, I _E = 5mA, f = 100MHz | - | 250 | - | MHz | |

*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference footprint



Fig.1 Input voltage vs. output current (ON characteristics)

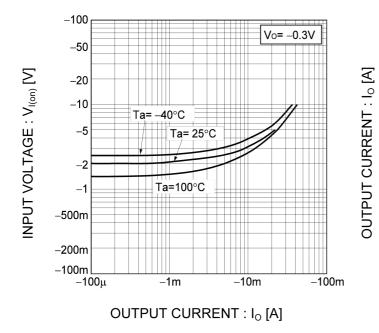


Fig.2 Output current vs. input voltage (OFF characteristics)

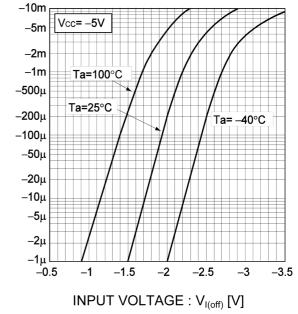


Fig.3 Output current vs. output voltage

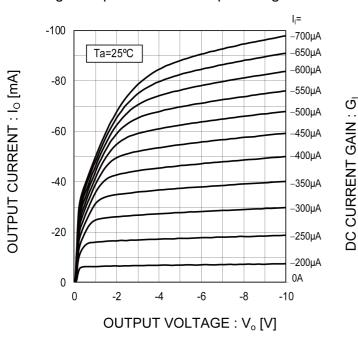
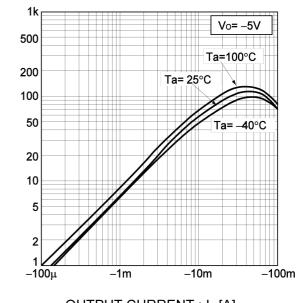


Fig.4 DC current gain vs. output current



OUTPUT CURRENT : I_o [A]



Datasheet

•Electrical characteristic curves (T_a =25°C)

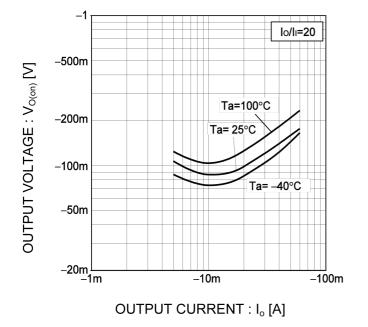


Fig.5 Output voltage vs. output current



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | | ETERS | INCHES | | |
|-----|------------|-------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| A | 0.45 | 0.55 | 0.018 | 0.022 | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| b | 0.17 | 0.27 | 0.007 | 0.011 | |
| b1 | 0.27 | 0.37 | 0.011 | 0.015 | |
| с | 0.08 | 0.18 | 0.003 | 0.007 | |
| D | 1.10 | 1.30 | 0.043 | 0.051 | |
| E | 0.70 | 0.90 | 0.028 | 0.035 | |
| е | 0.4 | 40 | 0.02 | | |
| HE | 1.10 | 1.30 | 0.043 | 0.051 | |
| L | 0.10 | 0.30 | 0.004 | 0.012 | |
| Lp | 0.20 | 0.40 | 0.008 | 0.016 | |
| x | - | 0.10 | - | 0.004 | |
| | | | | | |
| DIM | MILIMETERS | | INC | HES | |
| DIM | MIN | MAX | MIN | MAX | |
| b2 | - | 0.37 | - | 0.015 | |
| b3 | - | 0.47 | | 0.019 | |
| e1 | 0.80 | | 0.031 | | |
| 11 | | 0.50 | | 0.020 | |

Dimension in mm/inches



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | ETERS | INCHES | | |
|-----|------------|-------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| A | 0.80 | 1.00 | 0.031 | 0.039 | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| A3 | 0.3 | 25 | 0.0 | 10 | |
| b | 0.25 | 0.40 | 0.010 | 0.016 | |
| С | 0.10 | 0.20 | 0.004 | 0.008 | |
| D | 1.90 | 2.10 | 0.075 | 0.083 | |
| E | 1.15 | 1.35 | 0.045 | 0.053 | |
| е | 0. | 65 | 0.026 | | |
| HE | 2.00 | 2.20 | 0.079 | 0.087 | |
| L1 | 0.10 | 0.40 | 0.004 | 0.016 | |
| Lp | 0.25 | 0.55 | 0.010 | 0.022 | |
| Q | 0.10 | 0.30 | 0.004 | 0.012 | |
| x | - | 0.10 | - | 0.004 | |
| | | | | | |
| DIM | MILIMETERS | | INCHES | | |
| DIM | MIN | MAX | MIN | MAX | |
| b2 | - | 0.50 | - | 0.020 | |
| e1 | 1.55 | | 0.061 | | |
| 1 | - | 0.65 | - | 0.026 | |

Dimension in mm/inches



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| (Note1) Medical Equipment Classification of the Specific Applications |
|---|
|---|

| JÁPAN | USA | EU | CHINA | |
|--------|---------|------------|---------|--|
| CLASSⅢ | CLASSⅢ | CLASS II b | CLASSII | |
| CLASSⅣ | CLASSII | CLASSⅢ | CLASSI | |

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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