

500mA/50V Digital transistor (with built-in resistors)

Parameter	Value
V _{CC}	-50V
I _C	-500mA
R ₁	2.2kΩ
R ₂	10kΩ

SOT-346 SC-59

(SMT3)

Features

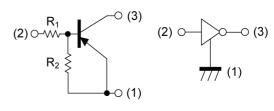
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.(see equivalent circuit)
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow positive

biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.

3)Only the on/off conditions need to be set for operation, making the device desigh easy.

•Inner circuit

Outline



- (1) GND (+) (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTB123YK	SOT-346 (SMT3)	2928	T146	180	8	3000	F52

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V _{CC}	-50	V
Input voltage	V _{IN}	-12 to 5	V
Collector current	I _C *1	-500	mA
Power dissipation	P _D *2	200	mW
Junction temperature	Tj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

Danamatan	C: resh al	Conditions	Values			11.26	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input valtage	$V_{I(off)}$	$V_{CC} = -5V, I_{O} = -100\mu A$	-	-	-0.3		
Input voltage	V _{I(on)}	$V_O = -0.3V$, $I_O = -20$ mA	V _O = -0.3V, I _O = -20mA -2.0 -		-	- V	
Output voltage	V _{O(on)}	I _O = -50mA, I _I = -2.5mA	-	-100	-300	mV	
Input current	I _I	V _I = -5V	-	-	-3.6	mA	
Output current	I _{O(off)}	V _{CC} = -50V, V _I = 0V	-	-	-500	nA	
DC current gain	G _I *3	$V_O = -5V$, $I_O = -50$ mA	56	-	-	-	
Input resistance	R ₁	-	1.54	2.2	2.86	kΩ	
Resistance ratio	R ₂ /R ₁	-	3.6	4.5	5.5	-	
Transition frequency	f _T *1	V _{CE} = -10V, I _E = 50mA, f = 100MHz	-	200	-	MHz	

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference land

^{*3} Pulsed

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

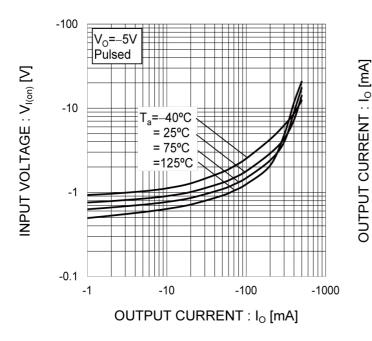


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

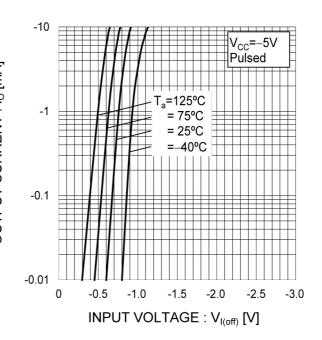


Fig.3 Output Current vs. Output Voltage

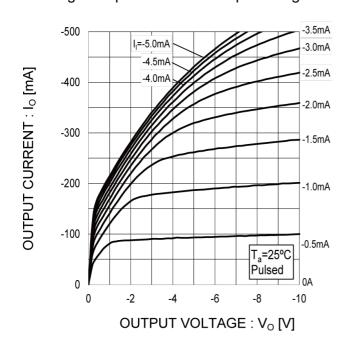
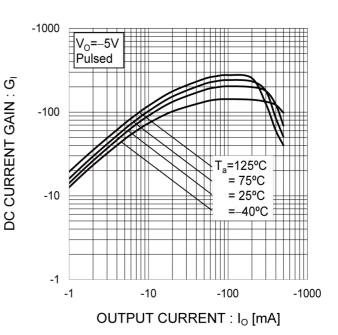
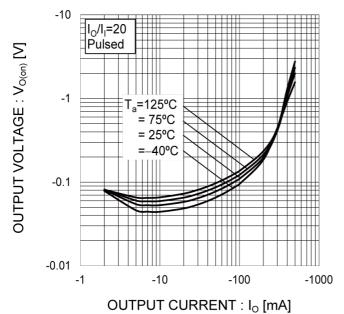


Fig.4 DC Current Gain vs. Output Current

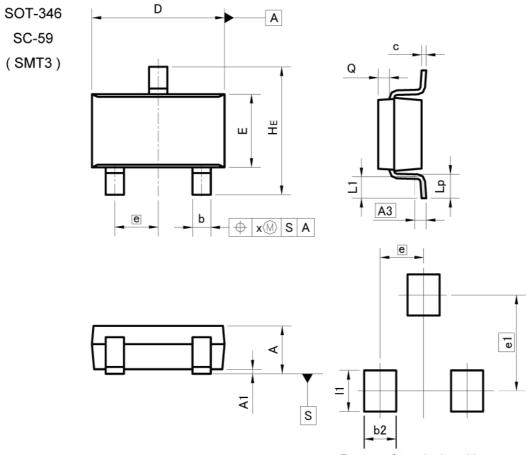


●Electrical characteristic curves (T_a =25°C)

Fig.5 Output Voltage vs. Output Current



Dimensions



Pattern of terminal position area	5
[Not a pattern of soldering pads	,]

DIM	MILIMETERS		INC	INCHES	
DIM	MIN	MAX	MIN	MAX	
Α	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A3	0.3	25	0.0	10	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.0	.037	
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
х	-	0.10	e=	0.004	
у	- 2	0.10	(-	0.004	

DIM MILIMETER MIN		ETERS	INCHES		
		MAX	MIN	MAX	
b2		0.60	_	0.024	
e1	2.10		0.0	83	
11	- 22	0.90	-	0.035	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CI ACCIII	CLASSIIb	CL A C C TT
CLASSIV	CLASSII	CLASSⅢ	CLASSIII

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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