DTC114T series

NPN 100mA 50V Digital Transistor (Bias Resistor Built-in Transistor)

Datasheet

Parameter	Value
$V_{\sf CEO}$	50V
I _C	100mA
R ₁	10kΩ

Features

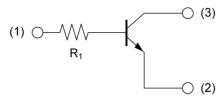
- 1) Built-In Biasing Resistor
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary PNP Types: DTA114T series

Application

INVERTER, INTERFACE, DRIVER

●Inner circuit

DTC114TM/ DTC114TEB/ DTC114TUB

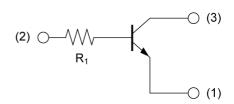


- (1) BASE
- (2) EMITTER
- (3) COLLECTOR

Outline

- Outilite	1
SOT-723	SOT-416FL (3)
(1)	(1)
DTC114TM (VMT3)	DTC114TEB (EMT3F)
SOT-416	SOT-323FL
(3)	(3)
(2)	
(1)	(1)
DTC114TE3	DTC114TUB
(EMT3)	(UMT3F)
SOT-323	SOT-346
(1)	(1)
DTC114TU3	DTC114TKA
(UMT3)	(SMT3)

DTC114TE3/ DTC114TU3/ DTC114TKA



- (1) EMITTER
- (2) BASE
- (3) COLLECTOR

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Quantity (pcs)	Marking
DTC114TM	SOT-723	1212	T2L	180	8	8000	04
DTC114TEB	SOT-416FL	1616	TL	180	8	3000	04
DTC114TE3	SOT-416	1616	TL	180	8	3000	04
DTC114TUB	SOT-323FL	2021	TL	180	8	3000	04
DTC114TU3	SOT-323	2021	T106	180	8	3000	04
DTC114TKA	SOT-346	2928	T146	180	8	3000	04

● Absolute maximum ratings (T_a = 25°C)

Parameter			Values	Unit
Collector-base voltage		V_{CBO}	50	V
Collector-emitter voltage		V _{CEO}	50	V
Emitter-base voltage		V_{EBO}	5	V
Collector current		I _C	100	mA
	DTC114TM		150	mW
	DTC114TEB		150	
Davie a discipation	DTC114TE3	P _D *1	150	
Power dissipation	DTC114TUB	P _D	200	
DTC114TU3			200	
DTC114TKA			200	
Junction temperature		T _j	150	°C
Range of storage temperate	ture	T _{stg}	-55 to +150	°C

• Electrical characteristics $(T_a = 25^{\circ}C)$

Davanastav	Currente ed	Conditions		Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV _{CBO}	I _C = 50μA	50	-	-	V
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	50	-	-	V
Emitter-base breakdown voltage	BV _{EBO}	I _E = 50μA	5	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	-	500	nA
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	-	-	500	nA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 10mA, I _B = 1mA	-	-	300	mV
DC current gain	h _{FE}	V _{CE} = 5V, I _C = 1mA	100	250	600	-
Input resistance	R ₁	-	7	10	13	kΩ
Transition frequency	f _T *2	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	250	-	MHz

^{*1} Each terminal mounted on a reference land.

^{*2} Characteristics of built-in transistor

● Electrical characteristic curves(Ta=25°C)

Fig.1 Grounded Emitter Propagation Characteristics

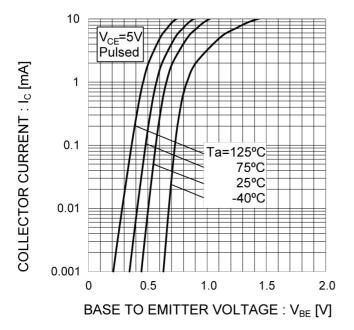
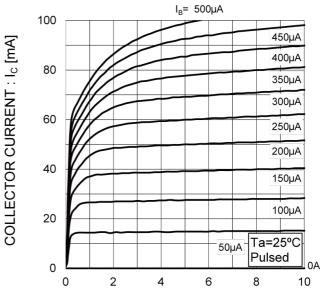


Fig.2 Typical Output Characteristics



COLLECTOR TO EMITTER VOLTAGE: V_{CE} [V]

Fig.3 DC Current Gain vs. Collector
Current

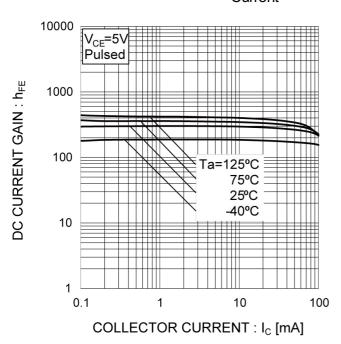
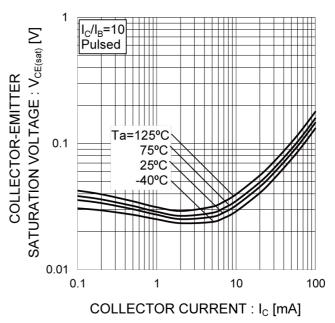
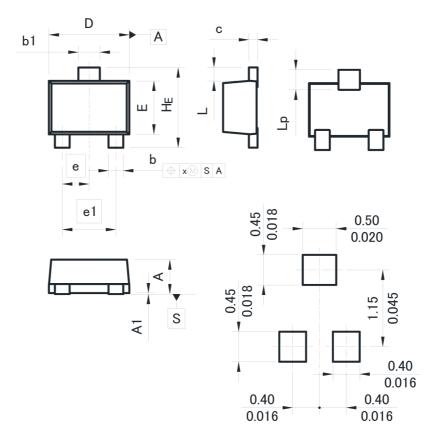


Fig.4 Collector-Emitter Saturation
Voltage vs. Collector Current



SOT-723 SC-105AA (VMT3)

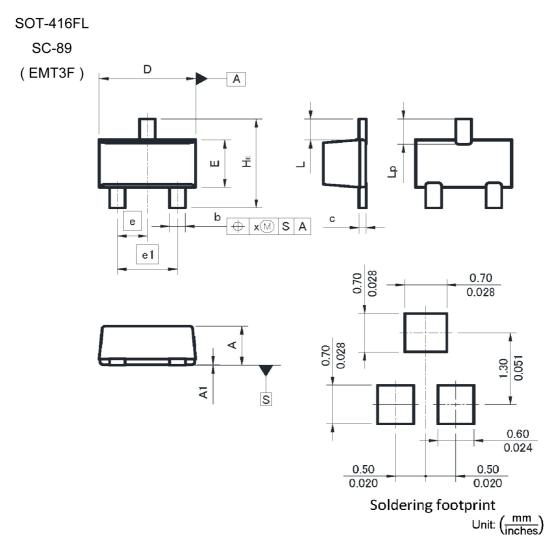


Soldering footprint

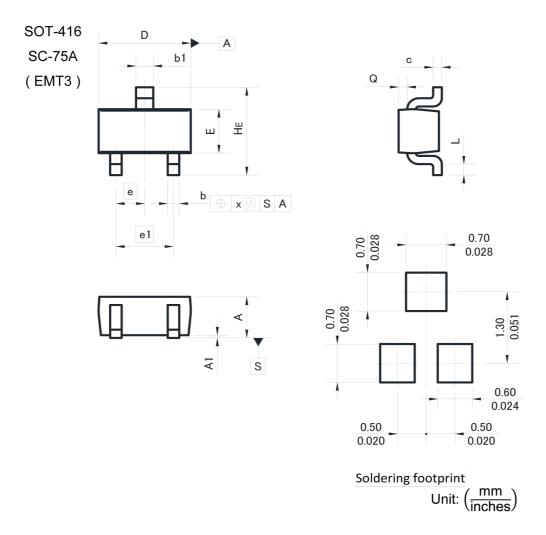
Unit: $\left(\frac{mm}{inches}\right)$

DIM	Millim	eters	Inc	nes	
DIIVI	Min.	Max.	Min.	Max.	
Α	0.45	0.55	0.018	0.022	
A1	0.00	0.10	0.000	0.004	
b	0.17	0.27	0.007	0.011	
b1	0.27	0.37	0.011	0.015	
С	0.08	0.18	0.003	0.007	
D	1.10	1.30	0.043	0.051	
E	0.70	0.90	0.028	0.035	
е	0.4	40	0.016		
e1	0.0	30	0.0	31	
HE	1.10	1.30	0.043	0.051	
L	0.10	0.30	0.004	0.012	
Lp	0.20	0.40	0.008	0.016	
Х	-	0.10	-	0.004	



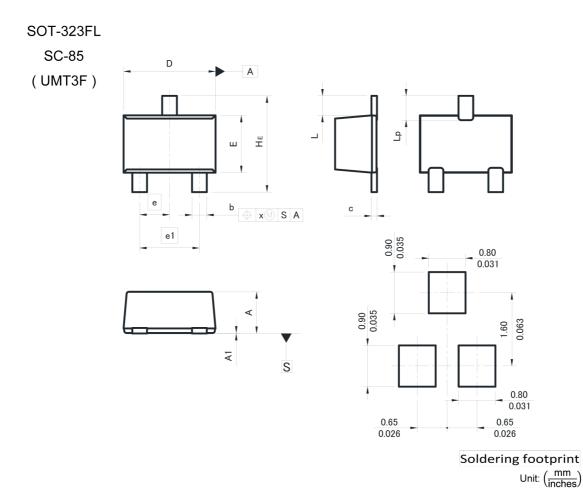


DIM	Millimeters		Inc	hes
DIIVI	Min.	Max.	Min.	Max.
Α	0.65	0.85	0.026	0.033
A1	0.00	0.10	0.000	0.004
b	0.21	0.36	0.008	0.014
С	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
Е	0.76	0.96	0.030	0.038
е	0.5	50	0.0	20
e1	1.0	00	0.039	
HE	1.50	1.70	0.059	0.067
L	0.0	37	0.0	15
Lp	0.35	0.55	0.014	0.022
Χ	-	0.10	-	0.004



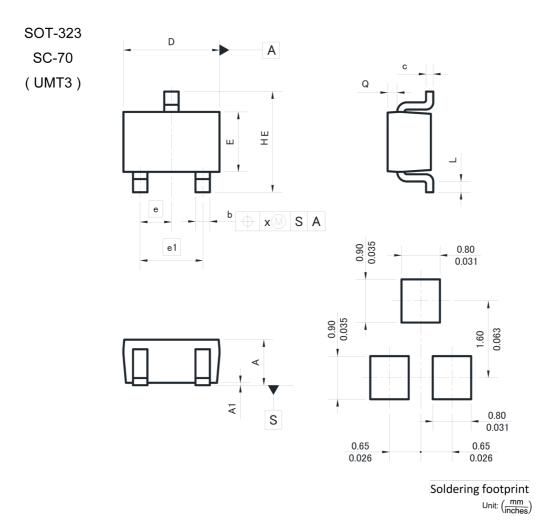
DIM	Millim	neters	Inc	hes	
DIIVI	Min.	Max.	Min.	Max.	
Α	0.60	0.90	0.024	0.035	
A1	0.00	0.10	0.000	0.004	
b	0.15	0.30	0.006	0.012	
b1	0.25	0.40	0.010	0.016	
С	0.10	0.20	0.004	0.008	
D	1.50	1.70	0.059	0.067	
E	0.70	0.90	0.028	0.035	
е	0.50		0.020		
e1	1.0	00	0.0	39	
HE	1.40	1.80	0.055	0.071	
L	0.10	_	0.004	-	
Q	0.05	0.25	0.002	0.010	
Х	-	0.10	-	0.004	





DIM	Millim	eters	Inc	hes	
DIIVI	Min.	Max.	Min.	Max.	
Α	0.85	1.05	0.033	0.041	
A1	0.00	0.10	0.000	0.004	
b	0.27	0.42	0.011	0.017	
С	0.08	0.18	0.003	0.007	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.6	65	0.026		
e1	1.3	30	0.0	51	
HE	2.00	2.20	0.079	0.087	
L	0.4	13	0.0	17	
Lp	0.43	0.63	0.017	0.025	
Х	-	0.10	_	0.004	

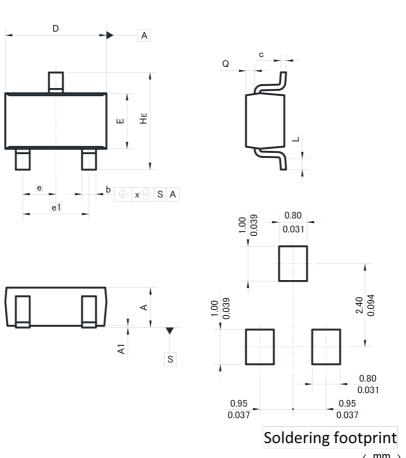




DIM	Millimeters		Incl	nes	
DIIVI	Min.	Max.	Min.	Max.	
Α	0.80	1.10	0.031	0.043	
A1	0.00	0.10	0.000	0.004	
b	0.25	0.40	0.010	0.016	
С	0.10	0.20	0.004	0.008	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.6	35	0.026		
e1	1.3	30	0.0	51	
HE	2.00	2.20	0.079	0.087	
L	0.10	-	0.004	-	
Q	0.10	0.30	0.004	0.012	
Х	-	0.10	-	0.004	



SOT-346 SC-59 (SMT3)



Unit: (mm/inches)

DIM	Millim	eters	Incl	nes	
DIIVI	Min.	Max.	Min.	Max.	
Α	1.00	1.40	0.039	0.055	
A1	0.00	0.10	0.000	0.004	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.037		
e1	1.9	90	0.0	75	
HE	2.60	3.00	0.102	0.118	
L	0.30	0.60	0.012	0.024	
Q	0.20	0.50	0.008	0.020	
Х	-	0.10	-	0.004	

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JÁPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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