Nch 600V 4A Power MOSFET

V _{DSS} (@Tj max.)*5	650V
$R_{DS(on)}(Max.)$	1.73Ω
I _{DP} *2	±12A
P _D	60W

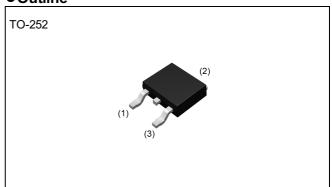
Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant

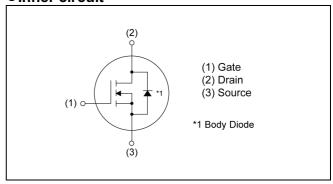
Application

Switching

Outline



•Inner circuit



Marking	R6004RND3

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	600	V
Continuous drain current (T _c = 25°C)	I _D *1	±4	А
Pulsed drain current	I _{DP} *2	±12	А
Gate - Source voltage	V _{GSS}	±30	V
Avalanche current, single pulse	I _{AS} *3	1.0	А
Avalanche energy, single pulse	E _{AS} *3	52	mJ
Power dissipation (T _c = 25°C)	P _D	60	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Davamatav	O	Values			1.1-24
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	2.11	°C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	50	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

● Electrical characteristics (T_a = 25°C)

Daramatar	Currelle e l	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	600	-	1	V
Zero gate voltage drain current	I _{DSS}	V _{DS} = 600V, V _{GS} = 0V	-	-	100	μΑ
Gate - Source leakage current	I _{GSS}	V _{GS} = ±30V, V _{DS} = 0V	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 450 \mu A$	5.0	6.0	7.0	V
Static drain - source on - state resistance	R _{DS(on)} *5	V _{GS} = 15V, I _D = 2.0A	-	1.33	1.73	Ω
Gate resistance	R_{G}	f = 1MHz, open drain	-	3.6	-	Ω

● Electrical characteristics (T_a = 25°C)

Davanastan	Cymaela al	Conditions	Values			Linit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 100V	-	230	-		
Output capacitance	C _{oss}	f = 1MHz	-	14	-		
Effective output capacitance energy related	C _{o(er)} *6	V _{GS} = 0V	-	13	-	pF	
Effective output capacitance time related	C _{o(tr)} *7	V _{DS} = 0V to 480V	-	47	1		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 300V$, $V_{GS} = 15V$	-	13	-		
Rise time	t _r *5	I _D = 2.0A	-	10	-		
Turn - off delay time	t _{d(off)} *5	R _L ≃ 150Ω	-	24	-	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	35	-		

● Gate charge characteristics (T_a = 25°C)

Darameter	Cymbol	Conditions	Values			Unit
Parameter	Symbol	Symbol Conditions -		Тур.	Max.	Offic
Total gate charge	Q_g^{*5}	V _{DD} ≃ 300V	-	10.5	-	
Gate - Source charge	Q _{gs} *5	I _D = 4A	-	3.0	-	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 15V	-	3.5	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 300V$, $I_D = 4A$	-	9.3	-	V

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Cumbal	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uill	
Source current	I _S *1	T 25°C	-	-	4	Α	
Pulsed source current	I _{SP} *2	T _C = 25°C	-	-	12	А	
Source-Drain voltage	V _{SD} *5	$V_{GS} = 0V$, $I_S = 4A$	-	-	1.7	V	
Reverse recovery time	t _{rr} *5	· V _{DD} ≃ 300V	-	40	-	ns	
Reverse recovery charge	Q _{rr} *5	I _S = 4A	-	80	-	nC	
Peak reverse recovery current	I _{rr} *5	di/dt = 100A/μs	-	4.0	-	А	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10µs, Duty cycle \leq 1%

^{*3} L \simeq 100mH, V_{DD} = 50V, R_G = 25 Ω , starting T_i = 25°C

^{*4} Tc=25°C

^{*5} Pulsed

^{*6} Co(er) is a fixed capacitance that gives the same stored energy as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

^{*7} Co(tr) is a fixed capacitance that gives the same charging time as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

• Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

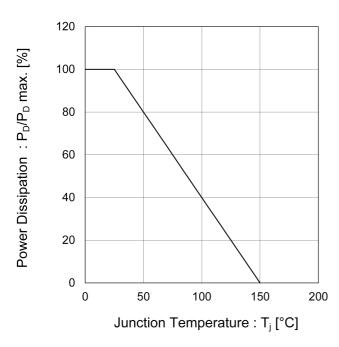


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

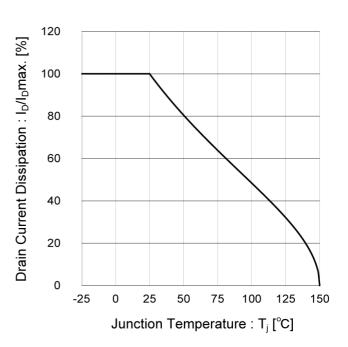


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

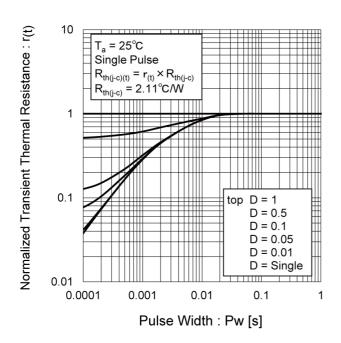
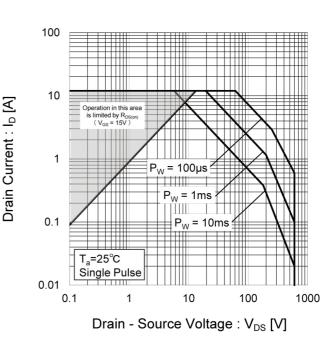


Fig.4 Maximum Safe Operating Area



Electrical characteristic curves

Fig.5 Avalanche Energy Derating Curve vs. Junction Temperature

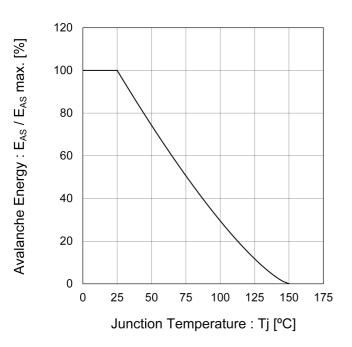


Fig.6 Normalized Breakdown Voltage vs. **Junction Temperature**

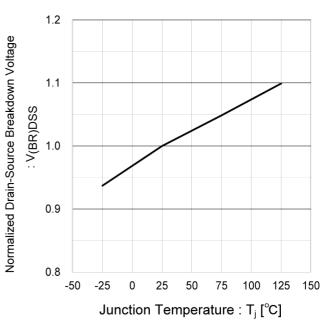
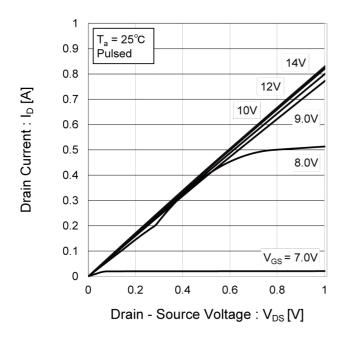
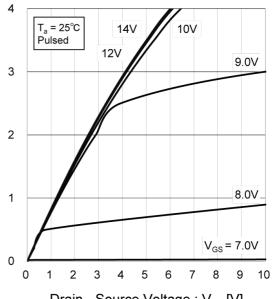


Fig.7 Typical Output Characteristics(I)



Drain Current : I_D [A]

Fig.8 Typical Output Characteristics(II)



Electrical characteristic curves

Fig.9 Typical Transfer Characteristics

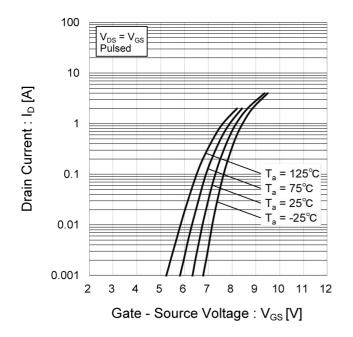


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

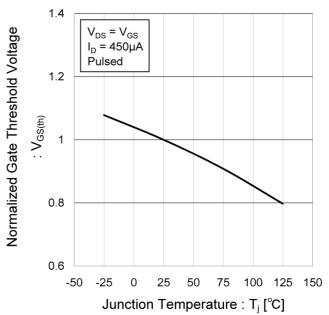


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

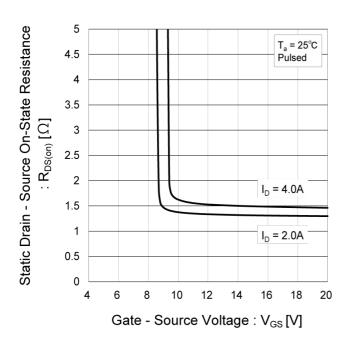
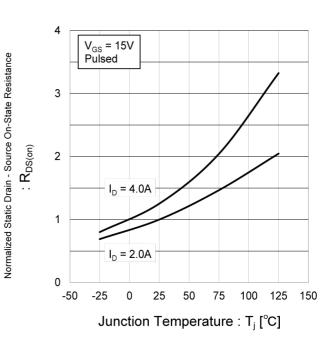


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature



• Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

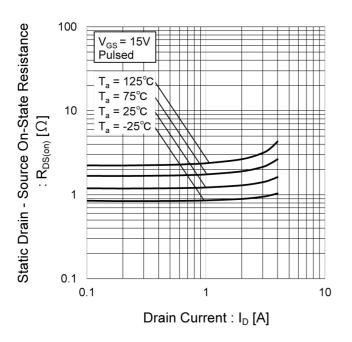


Fig.14 Typical Capacitance vs.
Drain - Source Voltage

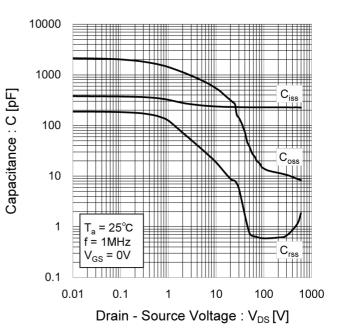


Fig.15 Typical Coss Stored Energy

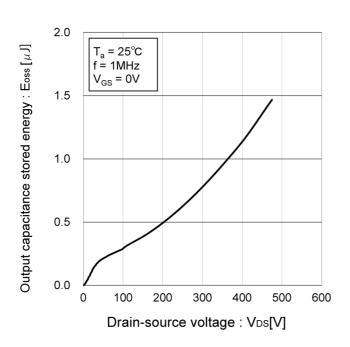
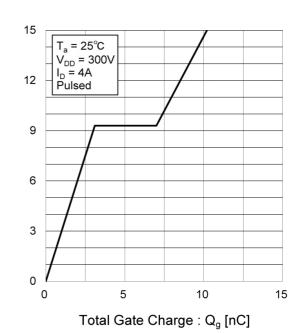


Fig.16 Typical Gate Charge



Gate - Source Voltage : V_{GS} [V]

• Electrical characteristic curves

Fig.17 Source Current vs. Source - Drain Voltage

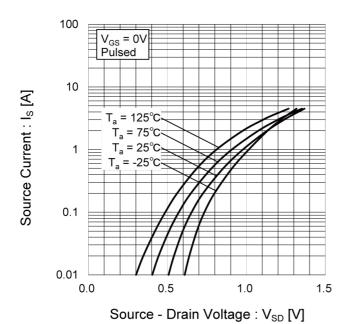
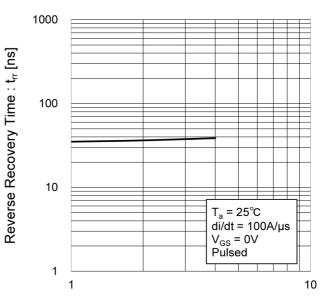


Fig.18 Reverse Recovery Time vs. Source Current



Source Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

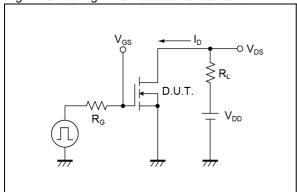


Fig.2-1 Gate Charge Measurement Circuit

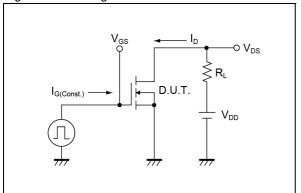


Fig.3-1 Avalanche Measurement Circuit

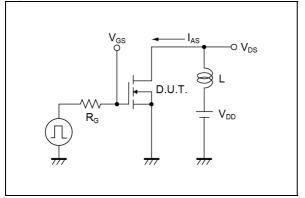


Fig.4-1 Diode Recovery Measurement Circuit

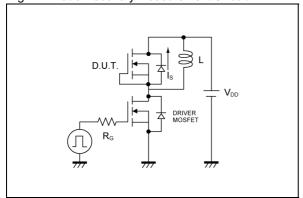


Fig.1-2 Switching Waveforms

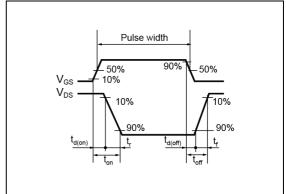


Fig.2-2 Gate Charge Waveform

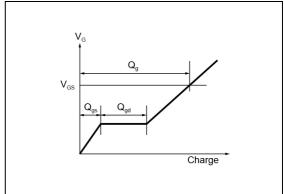


Fig.3-2 Avalanche Waveform

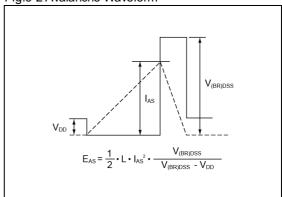
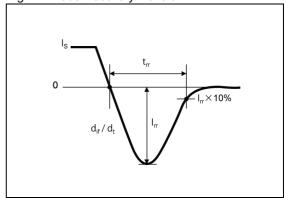
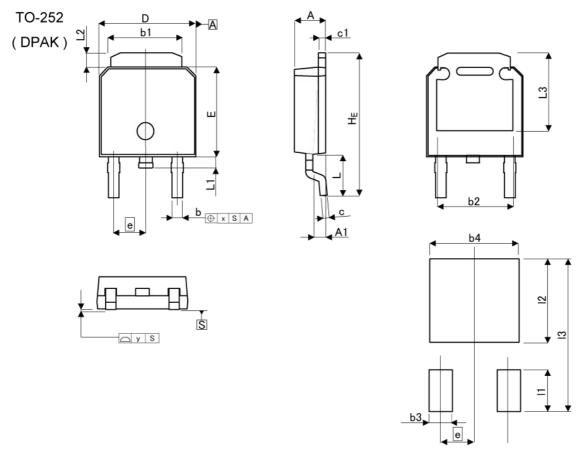


Fig.4-2 Diode Recovery Waveform



Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	MILIMETERS		MILIMETERS INCHES		HES
DIIVI	MIN	MAX	MIN	MAX		
Α	2.20	2.40	0.087	0.094		
A1	0.70	1.10	0.028	0.043		
b	0.60	0.90	0.024	0.035		
b1	5.20	5.50	0.205	0.217		
b2	4.	80	0.1	189		
С	0.40	0.60	0.016	0.024		
c1	0.40	0.60	0.016	0.024		
D	6.40	6.80	0.252	0.268		
е	2.	30	0.09			
E	6.00	6.40	0.236	0.252		
HE	9.40	10.40	0.370	0.409		
L	2.	90	0.1	114		
L1	0.60	1.00	0.024	0.039		
L2	0.70	1.30	0.028	0.051		
L3	5.	30	0.2	209		
Х	-	0.25	-	0.010		
у	-	0.10		0.004		
D.11.4	MILIME	MILIMETERS		HES		
DIM	MIN	MAX	MIN	MAX		
b3	-	1.15	-	0.045		
b4	-	5.55		0.219		
I1	-	2.77	(=)	0.109		
12	-	5.50	.=:	0.217		
13	-	10.40	548	0.409		

Dimension in mm/inches



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CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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