Nch 800V 2A Power MOSFET

| V _{DSS} | 800V |
|----------------------------|------|
| R _{DS(on)} (Max.) | 4.3Ω |
| I _D | ±2A |
| P _D | 62W |

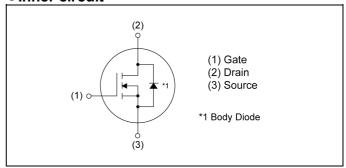
●Outline



Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Parallel use is easy.
- 4) Pb-free plating; RoHS compliant
- 5) AEC-Q101 qualified

●Inner circuit



Packaging specifications

| <u> </u> | Jing specifications | |
|----------|---------------------------|------------------|
| | Packing | Embossed Tape |
| | Reel size (mm) | 330 |
| Туре | Tape width (mm) | 24 |
| | Basic ordering unit (pcs) | 1000 |
| | Taping code | TL |
| | Marking | R8002ANJ |

Application

Switching Power Supply

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

| Parameter | Symbol | Value | Unit |
|--|--------------------|-------------|------|
| Drain - Source voltage | V _{DSS} | 800 | V |
| Continuous drain current (T _c = 25°C) | I _D *1 | ±2 | Α |
| Pulsed drain current | I _{DP} *2 | ±8 | Α |
| Gate - Source voltage | V _{GSS} | ±30 | V |
| Avalanche current, single pulse | I _{AS} *3 | 1 | Α |
| Avalanche energy, single pulse | E _{AS} *3 | 0.265 | mJ |
| Power dissipation (T _c = 25°C) | P _D | 62 | W |
| Junction temperature | T _j | 150 | °C |
| Operating junction and storage temperature range | T _{stg} | -55 to +150 | °C |

●Thermal resistance

| Davamatav | Cymah al | Values | | | l lm:4 |
|--|-------------------|--------|------|------|--------|
| Parameter | Symbol | Min. | Тур. | Max. | Unit |
| Thermal resistance, junction - case | R _{thJC} | - | - | 2.01 | °C/W |
| Thermal resistance, junction - ambient | R _{thJA} | - | - | 80 | °C/W |
| Soldering temperature, wavesoldering for 10s | T _{sold} | - | - | 265 | °C |

• Electrical characteristics $(T_a = 25^{\circ}C)$

| Darameter | Cumb al | Conditions | Values | | | Unit |
|---|------------------------|--|--------|------|------|-------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Offic |
| Drain - Source breakdown voltage | V _{(BR)DSS} | $V_{GS} = 0V$, $I_D = 1mA$ | 800 | - | - | V |
| | | $V_{DS} = 800V, V_{GS} = 0V$ | | | | |
| Zero gate voltage drain current | I _{DSS} | $T_j = 25^{\circ}C$ | - | - | 100 | μΑ |
| | | $T_j = 125^{\circ}C$ | - | - | - | |
| Gate - Source leakage current | I _{GSS} | $V_{GS} = \pm 30V$, $V_{DS} = 0V$ | 1 | 1 | ±100 | nA |
| Gate threshold voltage | $V_{GS(th)}$ | V_{DS} = 10V, I_D = 1mA | 3.0 | 1 | 5.0 | ٧ |
| | | V _{GS} = 10V, I _D = 1.0A | | | | |
| Static drain - source on - state resistance | R _{DS(on)} *4 | $T_j = 25^{\circ}C$ | - | 3.3 | 4.3 | Ω |
| | | $T_j = 125^{\circ}C$ | - | 7.2 | - | |
| Gate resistance | R_{G} | f = 1MHz, open drain | - | 6.6 | - | Ω |

● Electrical characteristics (T_a = 25°C)

| Darramatar | Cymah al | Conditions | Values | | | Unit |
|--------------------------------|------------------------|---|--------|------|------|------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
| Forward Transfer Admittance | Y _{fs} *4 | V _{DS} = 10V, I _D = 1.0A | 0.5 | - | - | S |
| Input capacitance | C _{iss} | V _{GS} = 0V | - | 250 | - | |
| Output capacitance | C _{oss} | V _{DS} = 25V | - | 130 | - | pF |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | - | 15 | - | |
| Turn - on delay time | t _{d(on)} *4 | V _{DD} ≈ 400V, V _{GS} = 10V | - | 20 | - | |
| Rise time | t _r *4 | I _D = 1A | - | 25 | - | 20 |
| Turn - off delay time | t _{d(off)} *4 | $R_L \simeq 402\Omega$ | - | 35 | - | ns |
| Fall time | t _f *4 | $R_G = 10\Omega$ | - | 70 | - | |

● Gate charge characteristics (T_a = 25°C)

| Darameter | Cumphal | Conditions | Values | | | l limit |
|----------------------|------------------------|---|--------|------|------|---------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
| Total gate charge | Qg*4 | V _{DD} ≈ 400V | - | 13 | - | |
| Gate - Source charge | Q _{gs} *4 | I _D = 2A | - | 3 | - | nC |
| Gate - Drain charge | Q _{gd} *4 | V _{GS} = 10V | - | 7 | - | |
| Gate plateau voltage | V _(plateau) | V _{DD} ≈ 400V, I _D = 2A | - | 7.0 | - | V |

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10µs, Duty cycle \leq 1%

^{*3} L $^{\sim}$ 500 μ H, V_{DD}=50V, R_G=25 Ω , starting T_j=25 $^{\circ}$ C

^{*4} Pulsed

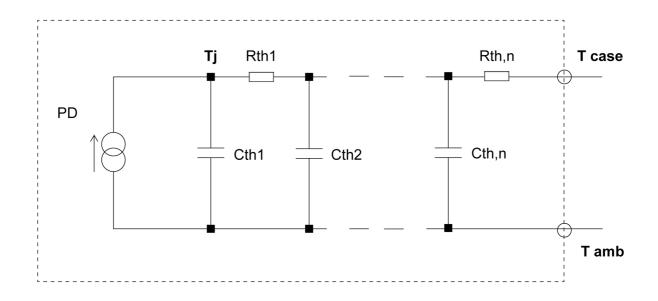
●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

| Parameter | Cumb ol | Conditions | Values | | | Unit |
|-------------------------------|---------------------|--|--------|------|------|-------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Uniit |
| Continuous forward current | I _S *1 | | - | - | 2 | А |
| Pulse forward current | I _{SP} *2 | T _C = 25°C | - | - | 8 | Α |
| Forward voltage | V _{SD} *4 | $V_{GS} = 0V$, $I_S = 2A$ | - | - | 1.5 | V |
| Reverse recovery time | t _{rr} *4 | | - | 220 | - | ns |
| Reverse recovery charge | Q _{rr} *4 | I _S = 2A, V _{GS} = 0V di/dt = 100A/μs | - | 1.16 | - | μC |
| Peak reverse recovery current | I _{rrm} *4 | - α, ατ 100/ γμο | - | 10.5 | - | Α |

● Typical transient thermal characteristics

| Symbol | Value | Unit |
|------------------|--------|------|
| R _{th1} | 0.8193 | |
| R _{th2} | 4.436 | K/W |
| R _{th3} | 65.84 | |

| Symbol | Value | Unit |
|------------------|----------|------|
| C _{th1} | 0.003182 | |
| C _{th2} | 0.1798 | Ws/K |
| C _{th3} | 0.9345 | |



• Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

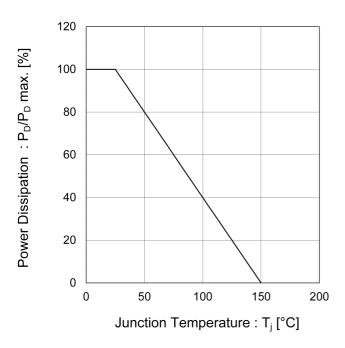


Fig.2 Maximum Safe Operating Area

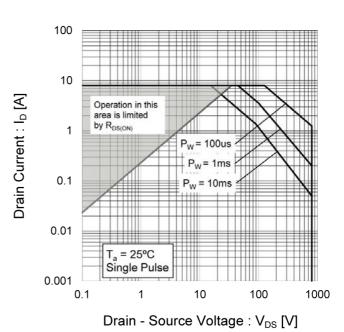


Fig.3 Normalized Transient Thermal
Resistance vs. Pulse Width

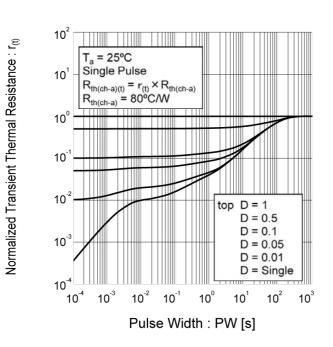
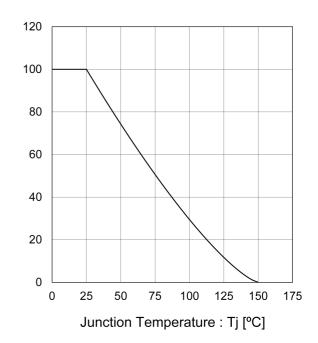


Fig.4 Avalanche Energy Derating Curve vs. Junction Temperature



Avalanche Energy : E $_{\mathsf{AS}}$ / E $_{\mathsf{AS}}$ max. [%]

• Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

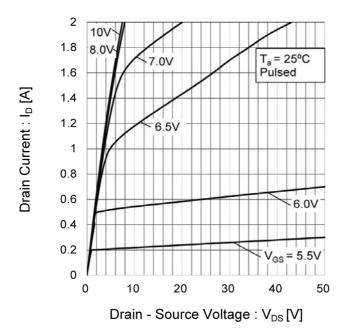
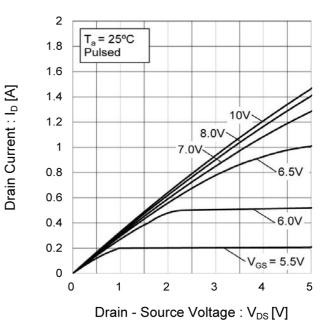


Fig.6 Typical Output Characteristics(II)



• Electrical characteristic curves

Fig.7 Normalized Breakdown Voltage vs.

Junction Temperature

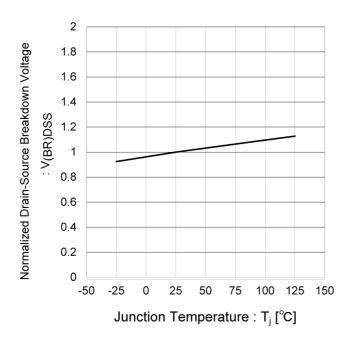


Fig.8 Typical Transfer Characteristics

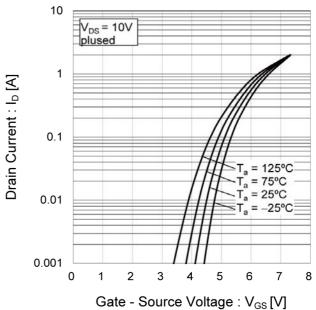


Fig.9 Normalized Gate Threshold Voltage vs. Junction Temperature

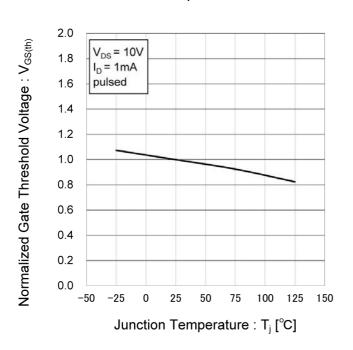
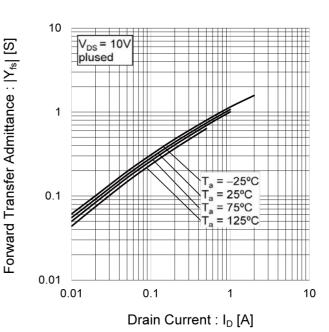


Fig.10 Forward Transfer Admittance vs. Drain Current



Electrical characteristic curves

Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

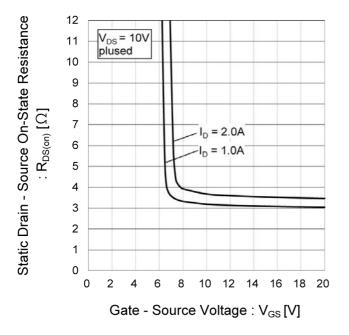


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

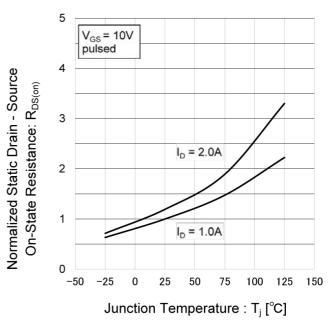


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

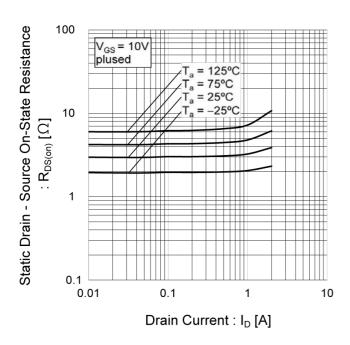
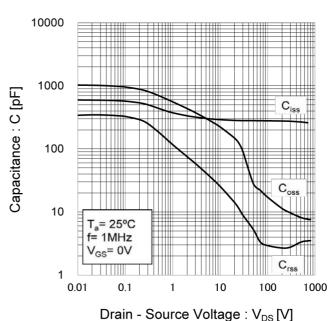


Fig.14 Typical Capacitance vs. Drain - Source Voltage



Electrical characteristic curves

Fig.15 Switching Characteristics

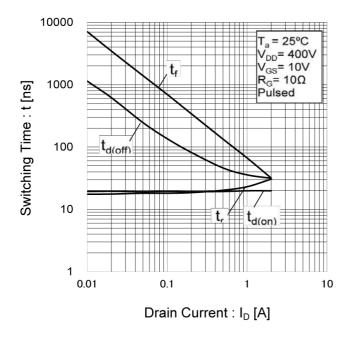
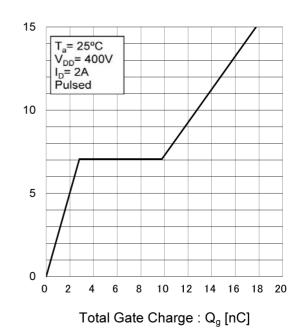


Fig.16 Dynamic Input Characteristics



Gate - Source Voltage : V_{GS} [V]

Fig.17 Inverse Diode Forward Current vs. Source - Drain Voltage

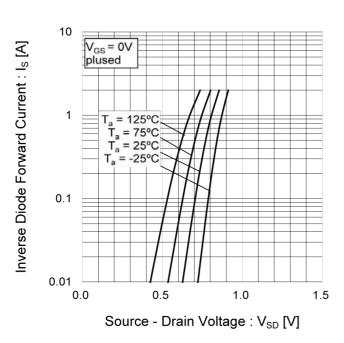
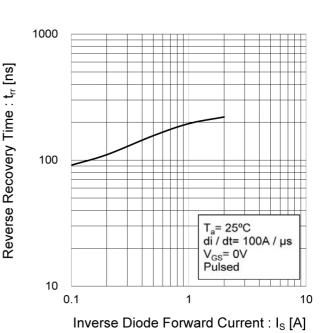


Fig.18 Reverse Recovery Time vs. Inverse Diode Forward Current



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

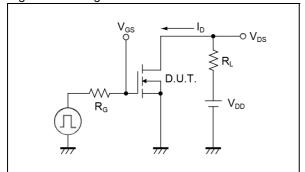


Fig.2-1 Gate Charge Measurement Circuit

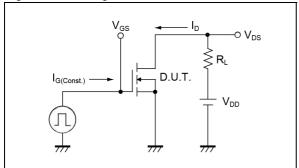


Fig.3-1 Avalanche Measurement Circuit

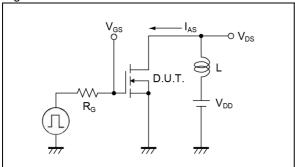
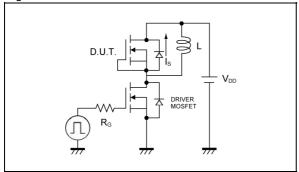


Fig.4-1 trr Measurement Circuit



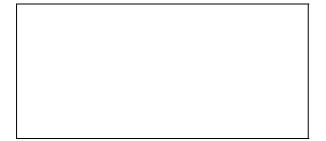


Fig.1-2 Switching Waveforms

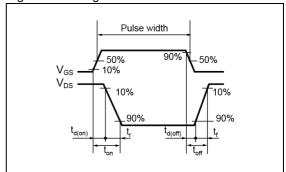


Fig.2-2 Gate Charge Waveform

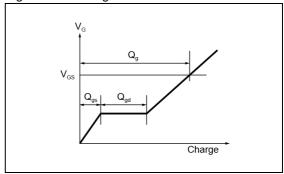


Fig.3-2 Avalanche Waveform

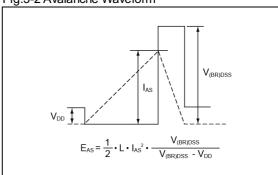
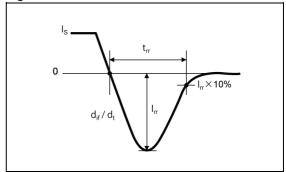
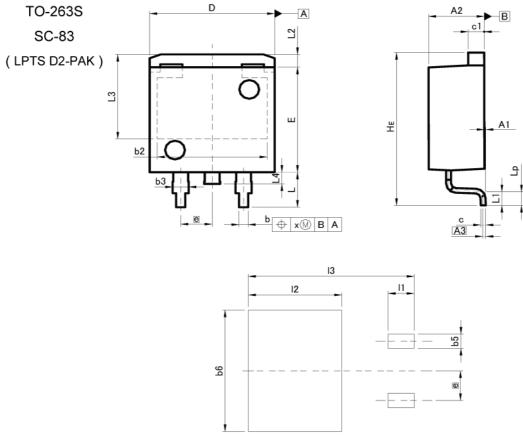


Fig.4-2 trr Waveform





Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | ETERS | INC | HES |
|-----|----------|-------|-------|-------|
| DIM | MIN | MAX | MIN | MAX |
| A1 | 0.00 | 0.30 | 0.000 | 0.012 |
| A2 | 4.30 | 4.70 | 0.169 | 0.185 |
| A3 | 0. | | 0.0 | |
| b | 0.68 | 0.98 | 0.027 | 0.039 |
| b2 | | 90 | 0.3 | |
| b3 | 1.14 | 1.44 | 0.045 | 0.057 |
| С | 0.30 | 0.60 | 0.012 | 0.024 |
| c1 | 1.10 | 1.50 | 0.043 | 0.059 |
| D | 9.80 | 10.40 | 0.386 | 0.409 |
| E | 8.80 | 9.20 | 0.346 | 0.362 |
| е | 2. | | 0.100 | |
| HE | 12.80 | 13.40 | 0.504 | 0.528 |
| L | 2.70 | 3.30 | 0.106 | 0.130 |
| L1 | 1. | 20 | 0.047 | |
| L2 | 1. | 10 | 0.043 | |
| L3 | 7.25 | | 0.285 | |
| L4 | 1.00 | | 0.0 | 39 |
| Lp | 0.90 | 1.50 | 0.035 | 0.059 |
| Х | =, | 0.25 | = | 0.010 |
| | NATI TNA | 1922 | TNO | 52 TE |

| DIM | MILIM | ETERS | INCHES | | |
|------|-------------|-------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| b5 | | 1.23 | - | 0.049 | |
| b6 | =7 | 10.40 | - | 0.409 | |
| . 11 | <u>==</u> 8 | 2.10 | , 72 | 0.083 | |
| 12 | | 7.55 | 1.70 | 0.297 | |
| 13 | | 13.40 | ·- | 0.528 | |

Dimension in mm/inches



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|---|---------|---------|------------|----------|
| Ī | CLASSⅢ | CLASSII | CLASS II b | CLASSIII |
| | CLASSIV | | CLASSⅢ | |

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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