

## Nch 800V 3A Power MOSFET

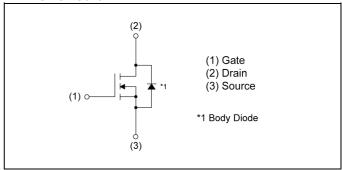
| V <sub>DSS</sub>           | 800V |
|----------------------------|------|
| R <sub>DS(on)</sub> (Max.) | 1.8Ω |
| I <sub>D</sub>             | ±3A  |
| P <sub>D</sub>             | 36W  |

# ● Package TO-220FM (1)(2)(3)

## Features

- 1) Low on-resistance
- 2) Fast switching
- 3) Parallel use is easy
- 4) Pb-free plating ; RoHS compliant

## •Inner circuit



# Application

Switching applications

## Marking specification

| Marking | R8003KNX |
|---------|----------|
|---------|----------|

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

| Parameter                                 | Symbol             | Value              | Unit        |    |
|---|--------------------|--------------------|-------------|----|
| Drain - Source voltage                    |                    | $V_{DSS}$          | 800         | V  |
| Continuous drain current                  |                    | I <sub>D</sub> *1  | ±3          | Α  |
| Pulsed drain current                      | I <sub>DP</sub> *2 | ±9                 | Α           |    |
| Cata Carrage vallage                      | static             | V                  | ±20         | V  |
| Gate - Source voltage                     | AC(f>1Hz)          | $V_{GSS}$          | ±30         | V  |
| Avalanche current, single pulse           |                    | I <sub>AS</sub>    | 0.6         | А  |
| Avalanche energy, single pulse            |                    | E <sub>AS</sub> *3 | 19          | mJ |
| Power dissipation (T <sub>c</sub> = 25°C) | P <sub>D</sub>     | 36                 | W           |    |
| Junction temperature                      | T <sub>j</sub>     | 150                | °C          |    |
| Operating junction and storage temper     | erature range      | T <sub>stg</sub>   | -55 to +150 | °C |

## Thermal characteristics

| Downwortow                                   | Cymah al                | Values |      |      | 1.1:4 |
|--|-------------------------|--------|------|------|-------|
| Parameter                                    | Symbol                  | Min.   | Тур. | Max. | Unit  |
| Thermal resistance, junction - case          | R <sub>th(j-c)</sub> *4 | -      | -    | 3.4  | °C/W  |
| Thermal resistance, junction - ambient       | R <sub>th(j-a)</sub>    | -      | -    | 75   | °C/W  |
| Soldering temperature, wavesoldering for 10s | T <sub>sold</sub>       | -      | -    | 265  | °C    |

# ● Static characteristics (T<sub>a</sub> = 25°C)

| Darameter                                      | Cumphal                | Canditions                                   | Values |      |      | Unit  |
|--|------------------------|--|--------|------|------|-------|
| Parameter                                      | Symbol                 | Conditions                                   | Min.   | Тур. | Max. | Offic |
| Drain - Source breakdown<br>voltage            | V <sub>(BR)DSS</sub>   | V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA   | 800    | -    | -    | V     |
| Zero gate voltage<br>drain current             | I <sub>DSS</sub>       | V <sub>DS</sub> = 800V, V <sub>GS</sub> = 0V | 1      | 1    | 100  | μA    |
| Gate - Source leakage current                  | I <sub>GSS</sub>       | $V_{GS} = \pm 20V, V_{DS} = 0V$              | -      | -    | ±100 | nA    |
| Gate threshold voltage                         | V <sub>GS(th)</sub>    | $V_{DS} = V_{GS}$ , $I_D = 2mA$              | 2.5    | 3.5  | 4.5  | V     |
| Static drain - source<br>on - state resistance | R <sub>DS(on)</sub> *5 | V <sub>GS</sub> = 10V, I <sub>D</sub> = 1.5A | -      | 1.5  | 1.8  | Ω     |

# ● Dynamic characteristics (T<sub>a</sub> = 25°C)

| Darramatar                                  | Cymah al               | Conditions                                    | Values |      |      | Unit |  |
|---|------------------------|---|--------|------|------|------|--|
| Parameter                                   | Symbol                 | Symbol Conditions Min. Typ.                   |        | Тур. | Max. | Oill |  |
| Gate resistance                             | $R_{G}$                | f = 1MHz, open drain                          | -      | 5    | -    | Ω    |  |
| Input capacitance                           | C <sub>iss</sub>       | V <sub>GS</sub> = 0V, VDS = 100V              | -      | 300  | -    |      |  |
| Output capacitance                          | C <sub>oss</sub>       | f = 1MHz                                      | -      | 25   | -    |      |  |
| Effective output capacitance energy related | C <sub>o(er)</sub> *6  | V <sub>GS</sub> = 0V                          | -      | 5    | -    | pF   |  |
| Effective output capacitance time related   | C <sub>o(tr)</sub> *7  | V <sub>DS</sub> = 0V to 400V                  | -      | 20   | -    |      |  |
| Turn - on delay time                        | t <sub>d(on)</sub> *5  | V <sub>DD</sub> ≈ 400V, V <sub>GS</sub> = 10V | -      | 15   | -    |      |  |
| Rise time                                   | t <sub>r</sub> *5      | I <sub>D</sub> = 1.5A                         | -      | 15   | -    | 20   |  |
| Turn - off delay time                       | t <sub>d(off)</sub> *5 | R <sub>L</sub> ≃ 267Ω                         | -      | 45   | -    | ns   |  |
| Fall time                                   | t <sub>f</sub> *5      | $R_G = 10\Omega$                              | -      | 65   | -    |      |  |

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

| Darameter            | Cumb al                | Conditions                                  | Values |      |      | Unit  |
|----------------------|------------------------|---|--------|------|------|-------|
| Parameter            | Symbol                 | ymbol Conditions                            |        | Тур. | Max. | Offic |
| Total gate charge    | $Q_g^{*5}$             | V <sub>DD</sub> ≈ 400V                      | -      | 11.5 | -    |       |
| Gate - Source charge | Q <sub>gs</sub> *5     | I <sub>D</sub> = 3A                         | -      | 2.5  | -    | nC    |
| Gate - Drain charge  | Q <sub>gd</sub> *5     | V <sub>GS</sub> = 10V                       | -      | 5    | -    |       |
| Gate plateau voltage | V <sub>(plateau)</sub> | V <sub>DD</sub> ≈ 400V, I <sub>D</sub> = 3A | -      | 5.6  | -    | V     |

# ●Body diode characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

| Daramatar                     | Cymab al           | Conditions                             | Values |      |      | Unit  |  |
|-------------------------------|--------------------|--|--------|------|------|-------|--|
| Parameter                     | Symbol             | Conditions                             | Min.   | Тур. | Max. | UTIIL |  |
| Source current                | I <sub>S</sub> *1  | · T <sub>C</sub> = 25°C                | -      | -    | 3    | Α     |  |
| Pulsed source current         | l <sub>SP</sub> *2 | 1C - 25 C                              | -      | -    | 9    | Α     |  |
| Source-Drain voltage          | V <sub>SD</sub> *5 | $V_{GS} = 0V, I_{S} = 3A$              | -      | -    | 1.5  | V     |  |
| Reverse recovery time         | t <sub>rr</sub> *5 |  | -      | 230  | -    | ns    |  |
| Reverse recovery charge       | Q <sub>rr</sub> *5 | I <sub>S</sub> = 3A<br>di/dt = 100A/µs | -      | 1600 | -    | μC    |  |
| Peak reverse recovery current | I <sub>rr</sub> *5 |  | -      | 14   | -    | Α     |  |

<sup>\*1</sup> Limited only by maximum channel temperature allowed

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L  $\stackrel{.}{=}$  100mH, V<sub>DD</sub>=50V, R<sub>G</sub>=25 $\Omega$ , STARTING T<sub>j</sub>=25 $^{\circ}$ C

<sup>\*4</sup> T<sub>C</sub>=25°C

<sup>\*5</sup> Pulsed

<sup>\*6</sup> Co(er) is a fixed capacitance that gives the same stored energy as Coss while  $V_{DS}$  is rising from 0 to 50%  $V_{DSS}$ 

<sup>\*7</sup> Co(er) is a fixed capacitance that gives the same charging time as Coss while  $V_{DS}$  is rising from 0 to 50%  $V_{DSS}$ 

Fig.1 Power Dissipation Derating Curve

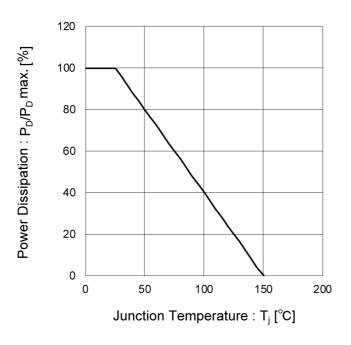


Fig.2 Drain Current Derating Curve

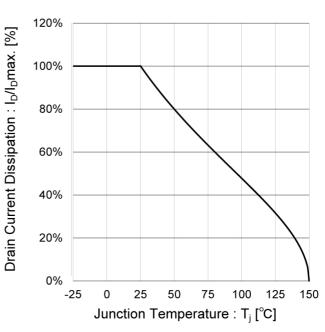


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

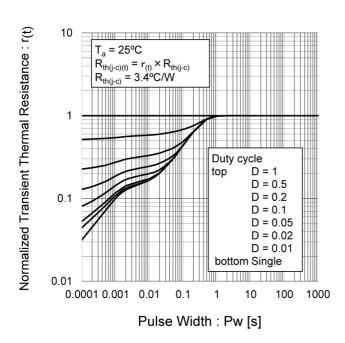
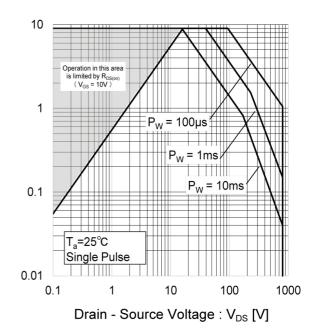


Fig.4 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Fig.5 Avalanche Energy Derating Curve

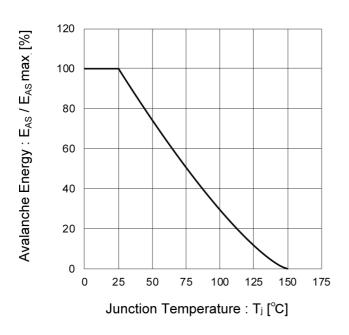


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

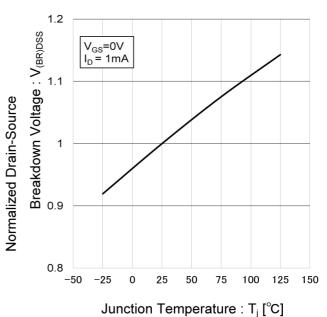


Fig.7 Output Characteristics(I))

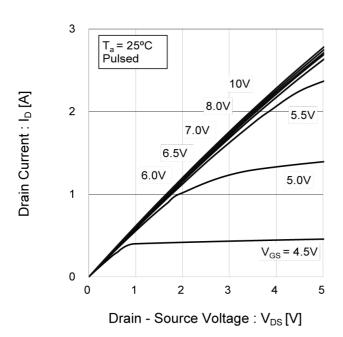
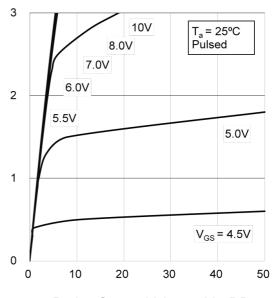


Fig.8 Output Characteristics(II)



Drain - Source Voltage : V<sub>DS</sub> [V]

Drain Current : I<sub>D</sub> [A]

Fig.9 Gate Threshold Voltage vs. Drain Current

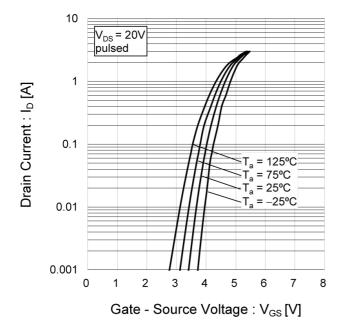


Fig.10 Normalized Gate Threshold Voltage vs. Junction Temperature

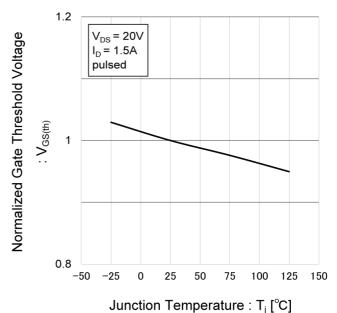


Fig.11 Static Drain - Source On - State Resistance vs. Drain Current

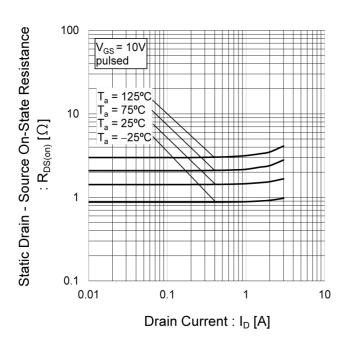


Fig.12 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

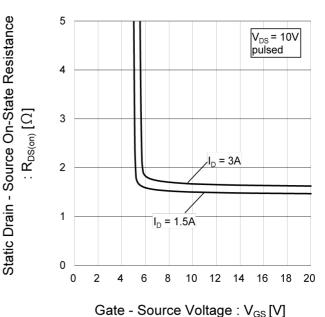


Fig.13 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

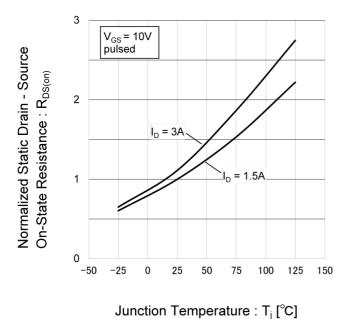


Fig.14 Capacitances

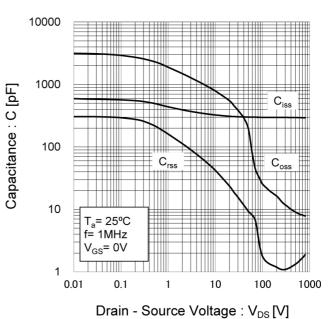


Fig.15 Switching Times

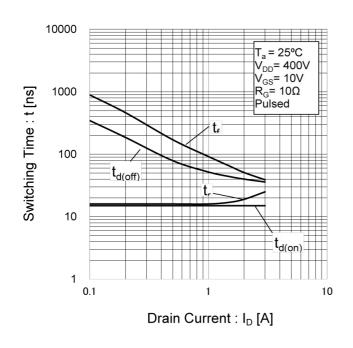
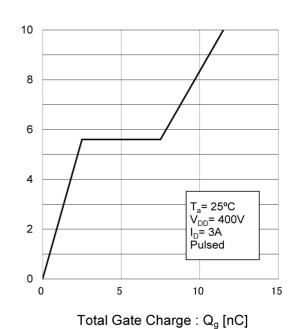


Fig.16 Gate Charge



Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.17 Source Current vs. Source - Drain Voltage

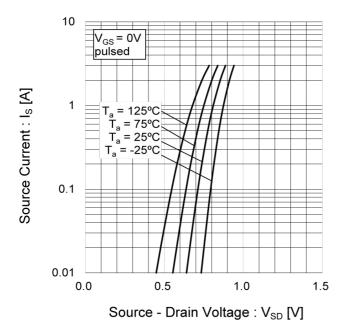
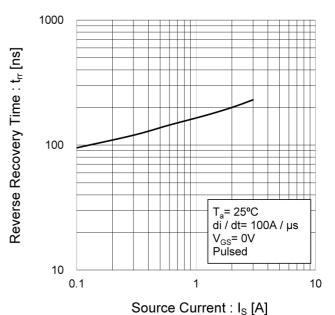


Fig.18 Reverse Recovery Time vs. Source Current



**ROHM** 

## Measurement circuits

Fig.1-1 Switching time measurement circuit

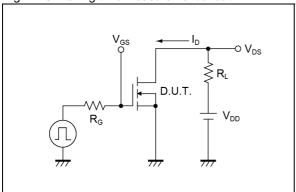


Fig.2-1 Gate charge measurement circuit

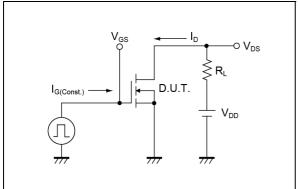


Fig.3-1 Avalanche measurement circuit

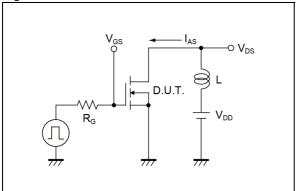


Fig.4-1 trr measurement circuit

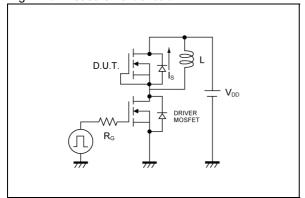


Fig.1-2 Switching waveforms

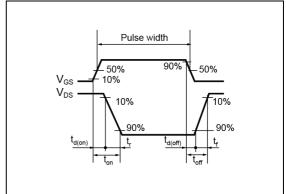


Fig.2-2 Gate charge waveform

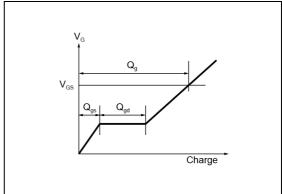


Fig.3-2 Avalanche waveform

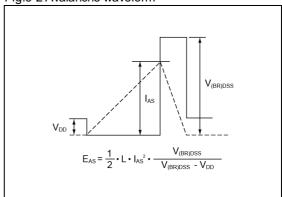
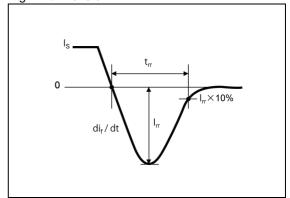
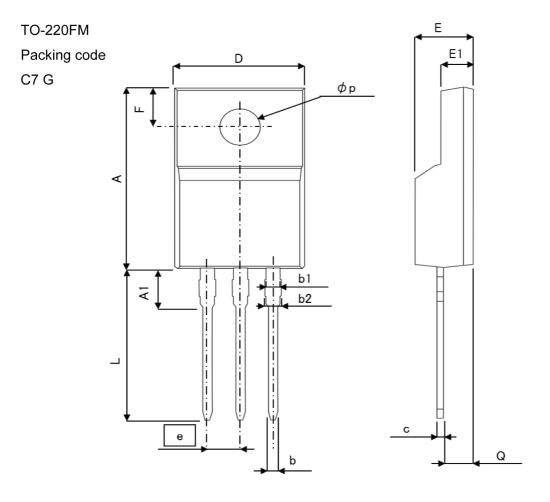


Fig.4-2 trr waveform



## Dimensions



| DIM | MILIM | ETERS | INC   | HES   |
|-----|-------|-------|-------|-------|
| DIM | MIN   | MAX   | MIN   | MAX   |
| Α   | 15.67 | 16.27 | 0.617 | 0.641 |
| A1  | 3.03  | 3.43  | 0.119 | 0.135 |
| b   | 0.70  | 0.95  | 0.028 | 0.037 |
| b1  | 1.00  | 1.40  | 0.039 | 0.055 |
| b2  | 1.10  | 1.50  | 0.043 | 0.059 |
| С   | 0.45  | 0.65  | 0.018 | 0.026 |
| D   | 9.90  | 10.30 | 0.390 | 0.406 |
| Е   | 4.60  | 5.00  | 0.181 | 0.197 |
| E1  | 2.44  | 2.74  | 0.096 | 0.108 |
| е   | 2.    | 54    | 0.1   | 00    |
| F   | 3.10  | 3.50  | 0.122 | 0.138 |
| L   | 12.6  | 13.6  | 0.946 | 0.535 |
| р   | 2.98  | 3.38  | 0.117 | 0.133 |
| Q   | 2.25  | 3.25  | 0.089 | 0.128 |

11/11

Dimension in mm/inches



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| JÁPAN   | USA      | EU         | CHINA     |
|---------|----------|------------|-----------|
| CLASSⅢ  | CL ACCTI | CLASS II b | CL ACCIII |
| CLASSIV | CLASSⅢ   | CLASSⅢ     | CLASSⅢ    |

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  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

## **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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