Nch 100V 10A Power MOSFET

V _{DSS}	100V
R _{DS(on)} (Max.)	133mΩ
I _D	±10A
P _D	20W

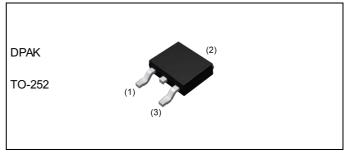
Features

- 1) Low on resistance
- 2) Fast switching speed
- 3) Drive circuits can be simple
- 4) Parallel use is easy
- 5) Pb-free lead plating; RoHS compliant

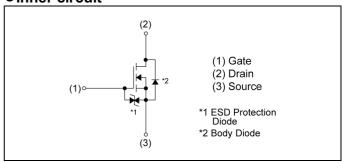
Application

Switching

Outline



Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
_	Tape width (mm)	16
Туре	Basic ordering unit (pcs)	2500
	Taping code	TL
	Taping code	TL1
	Marking	RD3P100SN

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

	· · · · · · · · · · · · · · · · · · ·		
Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	100	V
Continuous drain current	I _D *1	±10	Α
Pulsed drain current	I _{DP} *2	±20	А
Gate - Source voltage	V _{GSS}	±20	V
Power dissipation	P _D *3	20	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Llmit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *3	-	-	6.25	°C/W

● Electrical characteristics (T_a = 25°C)

Davamatav	Cymah ol	Conditions	Values			Lloit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	100	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	116.9	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA	
Gate - Source leakage current	I _{GSS}	V_{GS} = ±20V, V_{DS} = 0V	-	-	±10	μA	
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V , I _D = 1mA	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I _D = 1mA referenced to 25°C	-	-3.6	1	mV/°C	
		V _{GS} = 10V, I _D = 5A	-	95	133		
Static drain - source on - state resistance	R _{DS(on)} *4	V _{GS} = 4.5V, I _D = 5A	-	100	140	mΩ	
		$V_{GS} = 4.0V, I_D = 5A$	-	105	147		
Gate resistance	R_G	f = 1MHz, open drain	-	6.3	1	Ω	
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 10V, I _D = 5A	4.5	-	-	S	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*3} T_C=25°C

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

Daramatar	Cumahad	Conditions	Values			Lloit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	700	-		
Output capacitance C _{oss}		V _{DS} = 25V	-	65	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	40	-		
Turn - on delay time	t _{d(on)} *4	V _{DD} ≈ 50V,V _{GS} = 10V	-	10	-		
Rise time	t _r *4	I _D = 5A	-	17	-	no	
Turn - off delay time	f delay time ${\mathsf t_{\mathsf d(off)}}^{*4}$		-	50	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	20	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

	\ a	,				
Parameter	Cumbal	Conditions	Values			l lm:t
raianietei	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg*4	V _{DD} ≃ 50V.	-	18	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \approx 50V$, $I_D = 10A$,	-	2	-	nC
Gate - Drain charge	Q _{gd} *4	V _{GS} = 10V	-	4.5	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Doromotor	Symbol Conditions -		Values			l leit
Parameter			Min.	Тур.	Max.	Unit
Continuous forward current	I _S *1	T = 25°C	-	-	10	Α
Pulse forward current	I _{SP} *2	⊤ _a = 25℃	-	-	20	Α
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 10A	-	-	1.5	V

Fig.1 Power Dissipation Derating Curve

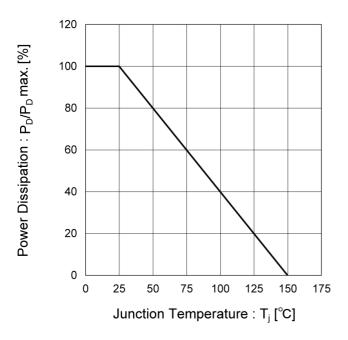


Fig.2 Maximum Safe Operating Area

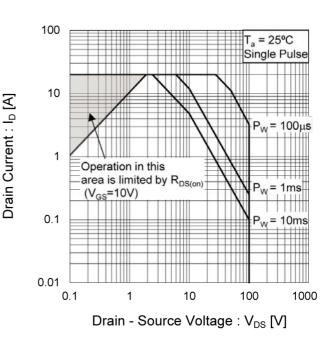


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

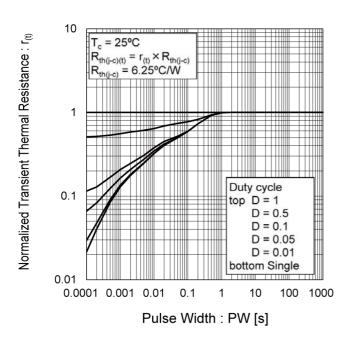
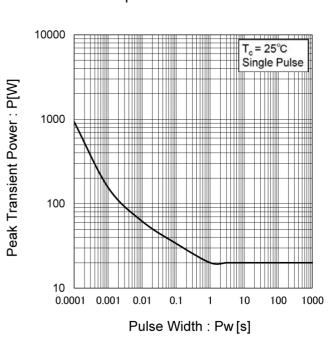


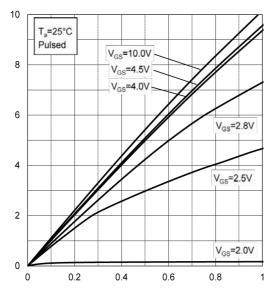
Fig.4 Single Pulse Maximum Power dissipation



Drain Current : I_D [A]

• Electrical characteristic curves

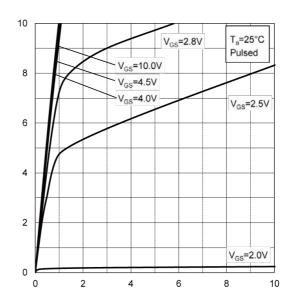
Fig.5 Typical Output Characteristics(I)



Drain Current : I_D [A]

Drain - Source Voltage: V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

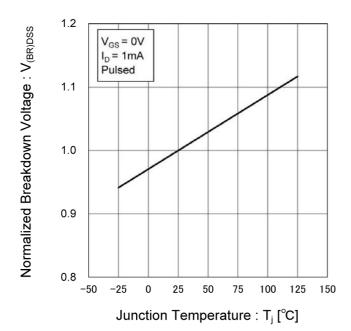


Fig.8 Typical Transfer Characteristics

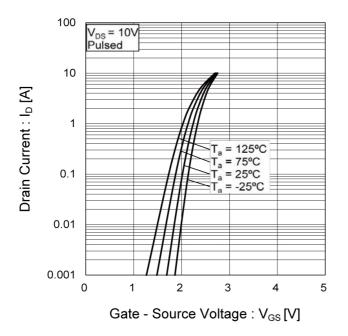


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

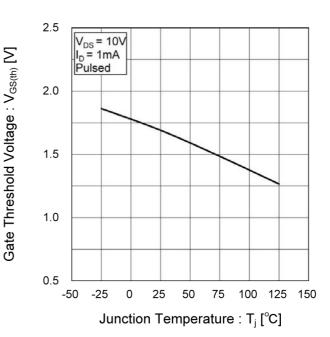


Fig.10 Forward Transfer Admittance vs.
Drain Current

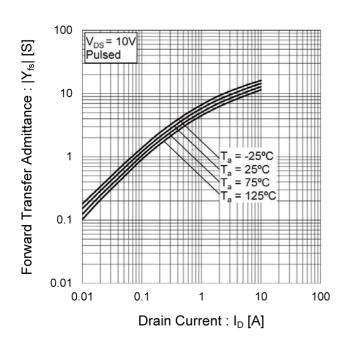


Fig.11 Drain Current Derating Curve

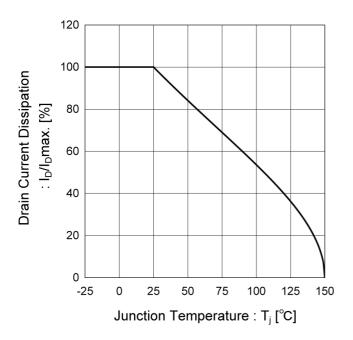


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

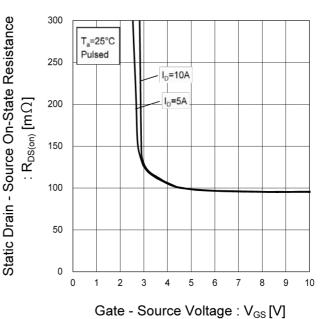


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

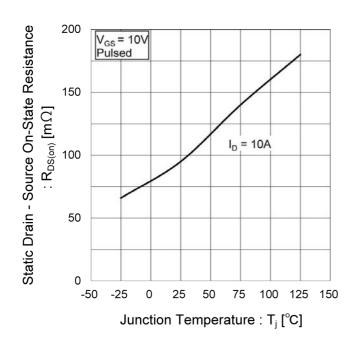


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

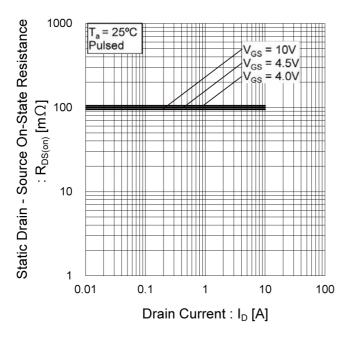


Fig.15 Static Drain - Source On - State
Resistance vs. Drain Current(II)

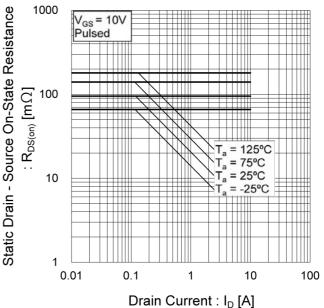


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

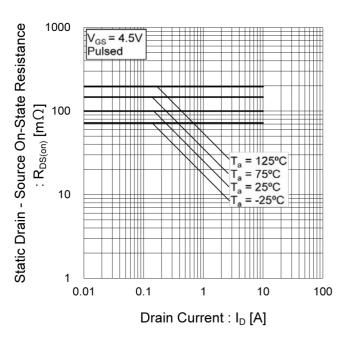


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

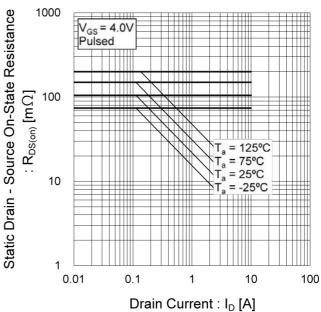
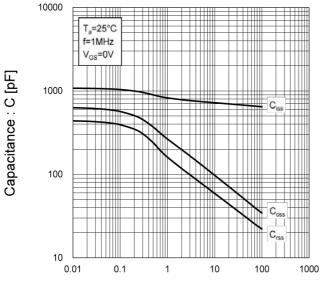


Fig.18 Typical Capacitance vs.

Drain - Source Voltage



Drain - Source Voltage : V_{DS} [V]

Fig.19 Switching Characteristics

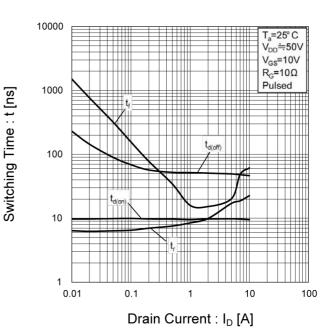


Fig.20 Dynamic Input Characteristics

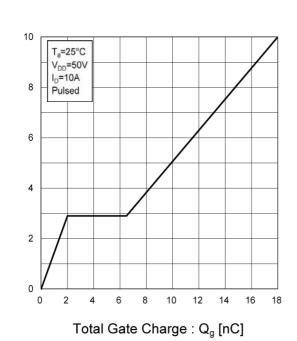
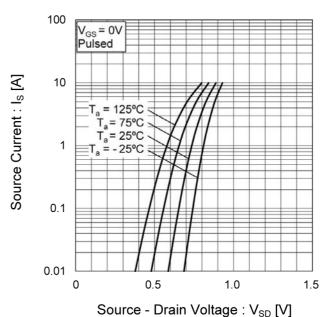


Fig.21 Source Current vs.
Source Drain Voltage



Gate - Source Voltage : V_{GS} [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

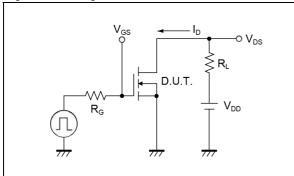


Fig.2-1 Gate Charge Measurement Circuit

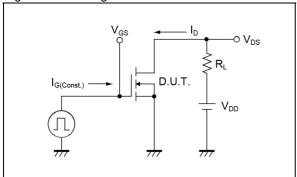


Fig.1-2 Switching Waveforms

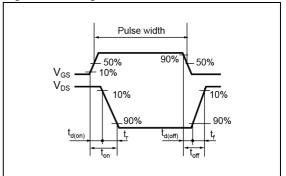
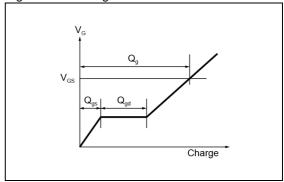
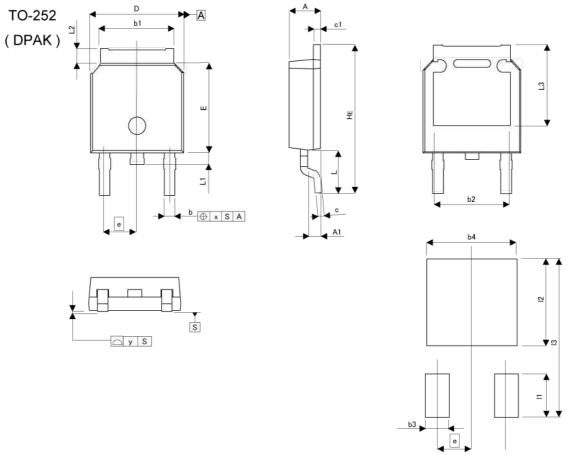


Fig.2-2 Gate Charge Waveform



● Dimensions (TL)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

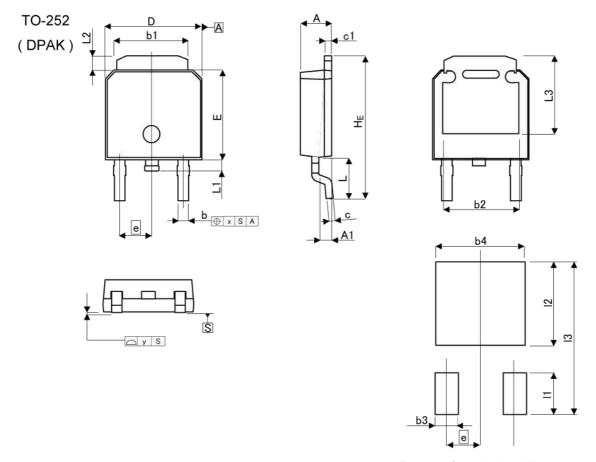
DIM -	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	2.10	2.30	0.083	0.091
A1	0.70	1.10	0.028	0.043
b	0.65	0.85	0.026	0.033
b1	5.10	5.40	0.201	0.213
b2	5.	10	0.2	201
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.	30	0.091	
E	6.00	6.40	0.236	0.252
HE	9.50	10.50	0.374	0.413
L	2.	90	0.114	
L1	0.70	0.90	0.028	0.035
L2	0.70	1.30	0.028	0.051
L3	5.	30	0.2	209
х	-	0.10	161	0.004
у	-	0.10	-	0.004

DIM	MILIM	ETERS	INC	HES
DIN	MIN	MAX	MIN	MAX
b3	₽	1.10	622	0.043
b4	*	5.40	5 .0 5	0.213
11	<u> </u>	2.90	72	0.114
12	*	5.50	5.00	0.217
13	2	10.50	021	0.413

Dimension in mm/inches



● Dimensions (TL1)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIME	ETERS	INCI	HES
DIIVI	MIN	MAX	MIN	MAX
Α	2.20	2.40	0.087	0.094
A1	0.70	1.10	0.028	0.043
b	0.60	0.90	0.024	0.035
b1	5.20	5.50	0.205	0.217
b2	4.	80	0.1	89
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.30		0.0	91
E	6.00	6.40	0.236	0.252
HE	9.40	10.40	0.370	0.409
L	2.	90	0.114	
L1	0.60	1.00	0.024	0.039
L2	0.70	1.30	0.028	0.051
L3	5.	30	0.209	
х	÷	0.25	X (#)	0.010
у	8	0.10	(5)	0.004
- T	MILIME	TERS	INCI	HES
DIM	MIN	MAX	MIN	MAX
b3	¥]	1.15	9 ± 3	0.045
b4	-	5.55	0.75)	0.219
11	-	2.77	S (#3)	0.109
12	8	5.50	(8)	0.217
13	#	10.40	7E0	0.409

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSII

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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