

Nch 60V 7.0A Power MOSFET

V_{DSS}	60V
R _{DS(on)} (Max.)	27mΩ
I _D	±7.0A
P_D	2.0W

P_D 2

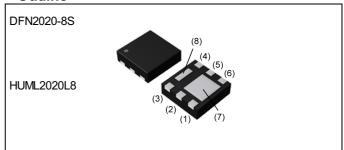
Features

- 1) Low on resistance
- 2) High Power small mold Package (HUML2020L8)
- 3) Pb-free plating; RoHS compliant
- 4) Halogen Free

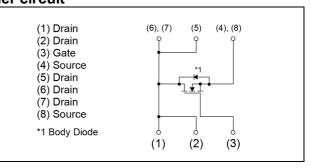
Application

Switching

Outline



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TCR
	Marking	KK

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
	Symbol	value	Offit
Drain - Source voltage	V_{DSS}	60	V
Continuous drain current	I _D	±7.0	А
Pulsed drain current	I _{DP} *1	±28	Α
Gate - Source voltage	V_{GSS}	±20	V
Avalanche current, single pulse	I _{AS} *2	7.0	А
Avalanche energy, single pulse	E _{AS} *2	4.0	mJ
Power dissipation	P _D *3	2.0	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R _{thJA} *3	-	ı	62.5	°C/W

● Electrical characteristics (T_a = 25°C)

Davamatav	Cymah al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	60	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j} I_D = 1 \text{mA}$ referenced to 25°C		38.9	-	mV/°C
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 60V, V_{GS} = 0V$	1	-	1	μA
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	ı	1	±100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 1mA$	1.0	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I _D = 1mA referenced to 25°C	-	-4.7	-	mV/°C
Static drain - source	D *4	V _{GS} = 10V, I _D = 7.0A	-	21	27	0
on - state resistance	R _{DS(on)} *4	V _{GS} = 4.5V, I _D = 7.0A	-	29	40	mΩ
Gate resistance	R_{G}	-	-	2.2	-	Ω
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 5V, I _D = 7.0A	2.5	-	-	S

^{*1} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*2} L \simeq 0.1mH, V_{DD} = 30V, R_G = 25 Ω , Starting T_i = 25 $^{\circ}$ C Fig.3-1,3-2

^{*3} Mounted on a Cu board (40×40×0.8mm)

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C)

Daramatar	Cumbal	Conditions	Values			Unit	
Parameter	Symbol	Symbol Conditions —		Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	460	-		
Output capacitance	C _{oss}	V _{DS} = 30V	-	180	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	17	-		
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq 30V, V_{GS} = 10V$	-	8.7	-		
Rise time	t _r *4	I _D = 3.5A	-	5.7	-		
Turn - off delay time	t _{d(off)} *4	$R_L \simeq 8.5\Omega$	-	21.0	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	4.4	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymahal	Conditions		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total gate above	O *4		V _{GS} = 10V	-	7.6	-	
Total gate charge	Q_{g} .	Q_g^{*4} $V_{DD} \simeq 30V$ $I_D = 7.0A$		-	3.9	-	5 C
Gate - Source charge	Q _{gs} *4		V _{GS} = 4.5V	-	1.7	-	nC
Gate - Drain charge	Q _{gd} *4			-	1.2	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Sumb al	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I _S	T _a = 25°C	-	-	1.67	Α
Pulse forward current	I _{SP} *1	1 _a - 25 C	-	-	28	Α
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 1.67A	-	-	1.2	V
Reverse recovery time	t _{rr} *4	I _S = 7.0A, V _{GS} =0V	-	25	-	ns
Reverse recovery charge	Q _{rr} *4	di/dt = 100A/μs	-	22	-	nC

Fig.1 Power Dissipation Derating Curve

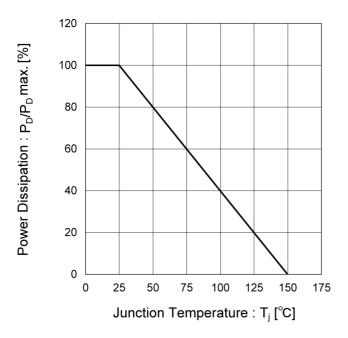
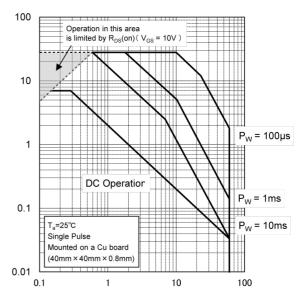


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage: V_{DS}[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

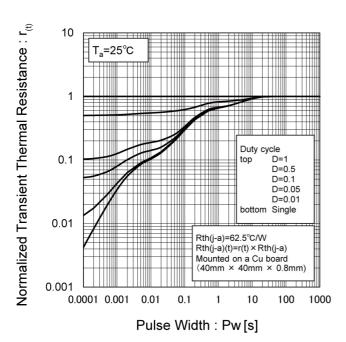


Fig.4 Single Pulse Maximum Power Dissipation

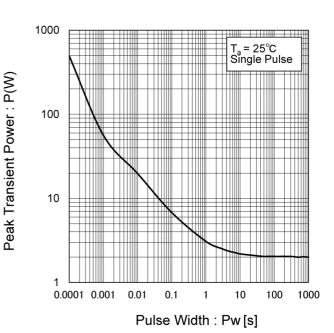


Fig.5 Typical Output Characteristics(I)

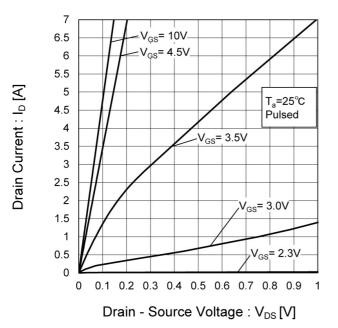
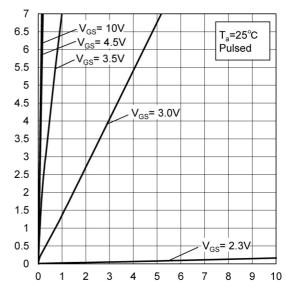


Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.

Junction Temperature

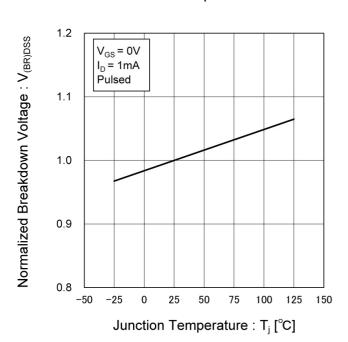


Fig.8 Typical Transfer Characteristics

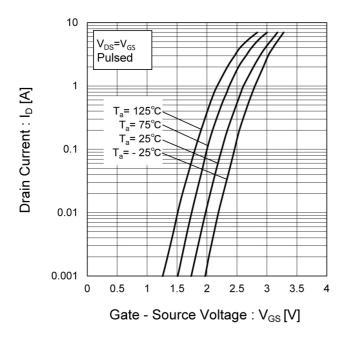


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

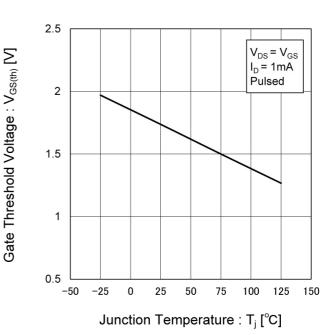


Fig.10 Forward Transfer Admittance vs.
Drain Current

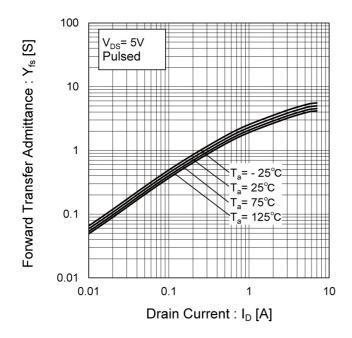


Fig.11 Drain Current Derating Curve

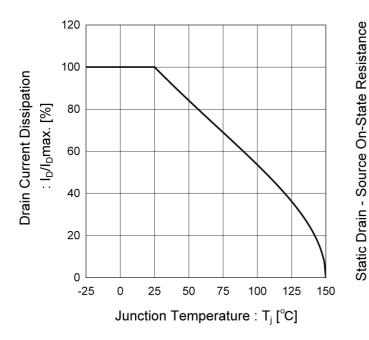


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

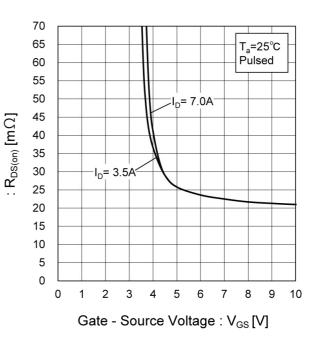
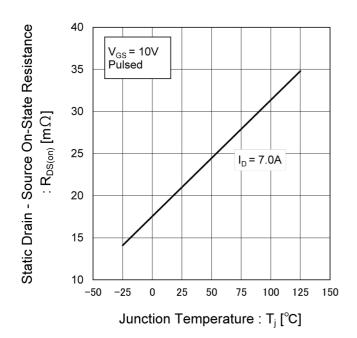


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



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Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

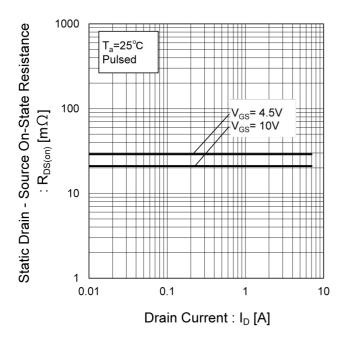


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

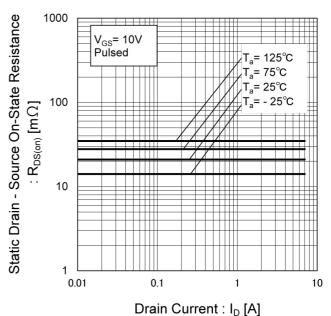


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

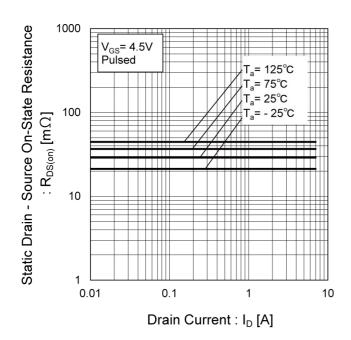


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

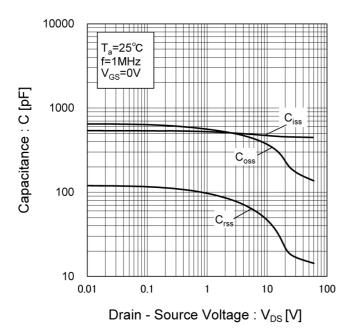


Fig.18 Switching Characteristics

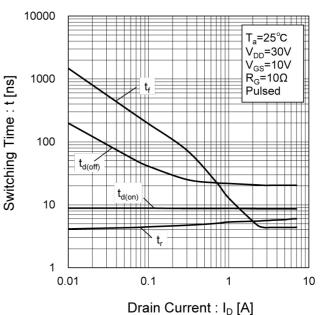


Fig.19 Typical Gate Charge

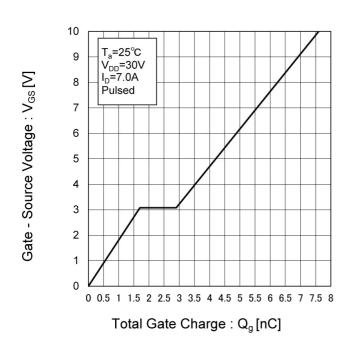
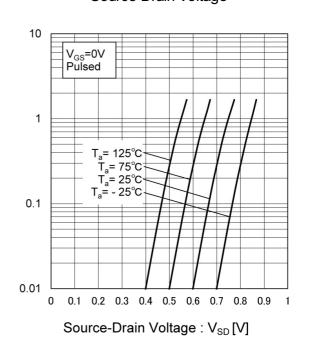


Fig.20 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

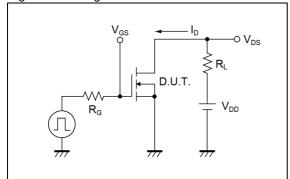


Fig.1-2 Switching Waveforms

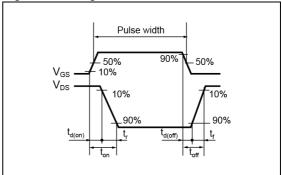


Fig.2-1 Gate Charge Measurement Circuit

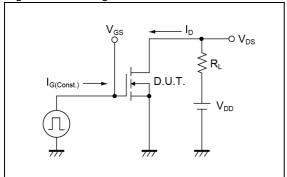


Fig.2-2 Gate Charge Waveform

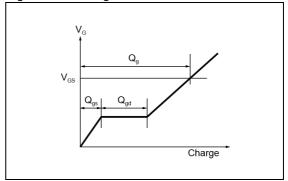


Fig.3-1 Avalanche Measurement Circuit

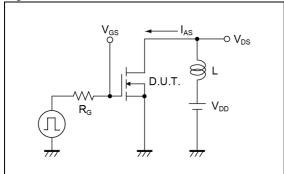
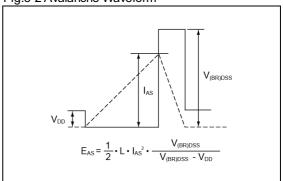


Fig.3-2 Avalanche Waveform

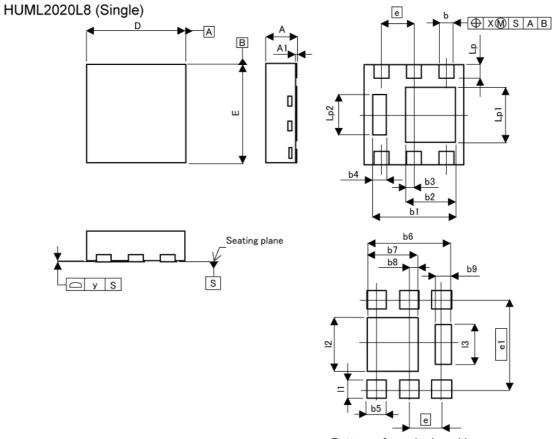


Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions

DFN2020-8S



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMETERS		INCI	HES
DIIVI	MIN	MAX	MIN	MAX
Α	0.55	0.65	0.022	0.026
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	1.55	1.75	0.061	0.069
b2	0.95	1.05	0.037	0.041
b3	0.175		0.007	
b4	0.	25	0.0	10
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
е	0.60	0.70	0.024	0.028
Lp	0.225	0.325	0.009	0.013
Lp1	1.00	1.20	0.039	0.047
Lp2	0.	80	0.0	31
х	-	0.10	-	0.004
у	(=))	0.10	0.00	0.004

DIM	MILIME	TERS	HES	
DIIVI	MIN	MAX	MIN	MAX
b5	-	0.45	(I -)	0.018
b6		1.75		0.069
b7	-	1.05	-	0.041
b8	0.175		0.0	07
b9	-	0.30	-	0.012
e1	1.725		0.068	
l1	-	0.425	(- (- (- (- (- (- (- (- (- (-	0.017
12	-	1.15	(I=)	0.045
13		0.85	-	0.033

Dimension in mm/inches



Notice

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CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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