

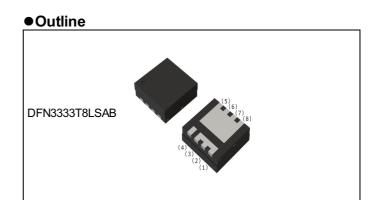
Nch 40V 40A Power MOSFET

## Datasheet

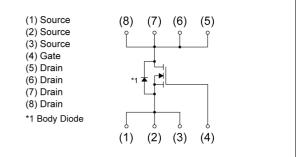
V <sub>DSS</sub>	40V
R <sub>DS(on)</sub> (Max.)	5.8mΩ
Ι <sub>D</sub>	±40A
P <sub>D</sub>	62W

## Features

- 1) Wettable Flanks Product
- 2) AEC-Q101 Qualified
- 3) 100% Avalanche tested



### Inner circuit



## Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	3000
	Taping code	ТСВ
	Marking	G04CBL

# • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

<b>U</b> ( a		• •		
Parameter	Symbol	Value	Unit	
Drain - Source voltage		V <sub>DSS</sub>	40	V
Continuous drain current V <sub>GS</sub> = 10V		۱ <sub>D</sub> *1	±40	А
Pulsed drain current	$I_{DP}^{*2}$	±80	А	
Gate - Source voltage	V <sub>GSS</sub>	±20	V	
Avalanche current, single pulse	I <sub>AS</sub> *3	17	А	
Avalanche energy, single pulse	E <sub>AS</sub> *3	23	mJ	
Power dissipation		P <sub>D</sub> <sup>*1</sup>	62	W
Junction temperature	Tj	175	°C	
Operating junction and storage temper	T <sub>stg</sub>	-55 to +175	C°	

# Application

ADAS/Info./Lighting/Body

#### •Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}^{*1}$	-	-	2.4	°C/W

## •Electrical characteristics (T<sub>a</sub> = 25°C)

Deverseter	Currente e l	Conditions	Values			1.1:4	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		40	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{i}} I_{D} = 1 \text{mA}$ referenced to 25°C		-	21	-	mV/°C	
Zero gate voltage drain current	$I_{DSS}$ $V_{DS}$ = 40V, $V_{GS}$ = 0V		-	-	1	μA	
Gate - Source leakage current	$I_{GSS}$ $V_{GS} = \pm 20V, V_{DS} = 0V$		-	-	±500	nA	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 600µA	2.0	-	4.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	$I_D = 600 \mu A$ referenced to 25°C	-	-6.8	-	mV/°C	
Static drain - source	D *4	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	4.4	5.8		
on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 6.0V, I <sub>D</sub> = 10A	-	6.2 8.7		- mΩ	
Gate resistance	R <sub>G</sub> f = 1MHz, open drain		-	2.8	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>   <sup>*4</sup>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 10A	11.0	-	-	S	

\*1 T<sub>c</sub>=25°C, Limited only by maximum temperature allowed.

\*2 Pw≤10µs , Duty cycle≤1%

\*3 L  $\simeq$  0.1mH, V\_{DD} = 20V, R\_G = 25 $\Omega$ , Starting T\_i = 25°C Fig.3-1,3-2

\*4 Pulsed



# •Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Cumphol	Conditions		l loit			
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1140	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 20V	-	520	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	65	-		
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 20V, V_{GS}$ = 10V	-	20.0	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 10A	-	9.0	-		
Turn - off delay time	$t_{d(off)}^{*4}$	$R_L \simeq 2\Omega$	-	36.0	-	ns	
Fall time	t <sub>f</sub> *4	R <sub>G</sub> = 1Ω	-	9.0	-		

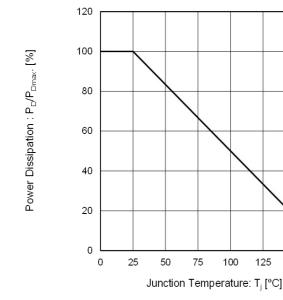
# • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Currence of	Conditions		Values			Lincit		
Parameter	Symbol			Min.	Тур.	Max.	Unit		
Total gata abarga	Qg*4 Qgs*4		V <sub>GS</sub> = 10V	-	17.7	-			
Total gate charge		Q <sub>g</sub> ·	Qg	$V_{DD} \simeq 20V$		-	8.9	-	nC
Gate - Source charge		I <sub>D</sub> = 10A	V <sub>GS</sub> = 6.0V	-	2.9	-	nc		
Gate - Drain charge	Q <sub>gd</sub> *4			-	3.3	-			

## •Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

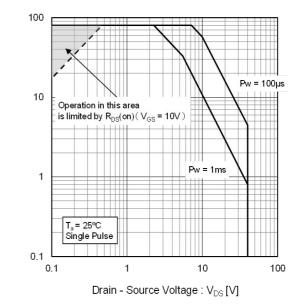
Deremeter	Sumbol	Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ <sub>S</sub> *1	T <sub>a</sub> = 25℃	-	-	40	А
Pulse forward current	$I_{SP}^{*2}$	$T_a = 25 C$	-	-	80	А
Forward voltage	$V_{SD}^{*4}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A	-	-	1.2	V
Reverse recovery time	t <sub>rr</sub> *4	I <sub>S</sub> = 10A, V <sub>GS</sub> =0V	-	35	-	ns
Reverse recovery charge	Q <sub>rr</sub> *4	di/dt = 100A/µs	-	26	-	nC





#### Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

175

150

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

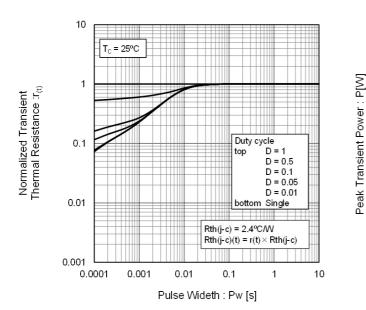
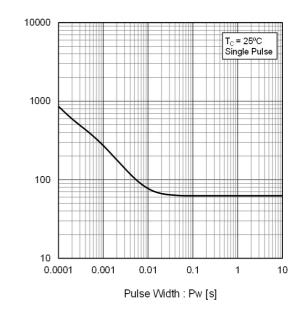
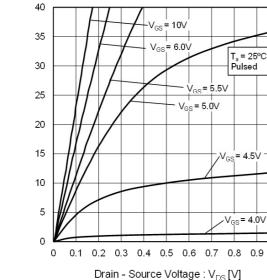


Fig.4 Single Pulse Maximum Power dissipation







Drain Current : I<sub>D</sub> [A]

### Fig.5 Typical Output Characteristics(I)

V<sub>GS</sub> – 10V

V<sub>GS</sub> = 6.0V

V<sub>GS</sub>= 5.5V

V<sub>GS</sub> = 5.0V

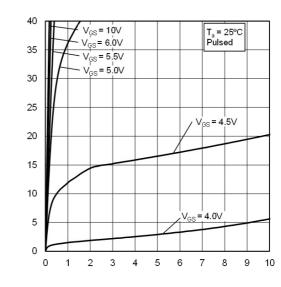
Drain Current : I<sub>D</sub> [A]

T<sub>a</sub> = 25°C Pulsed

V<sub>GS</sub> = 4.5V

V<sub>GS</sub>= 4.0V

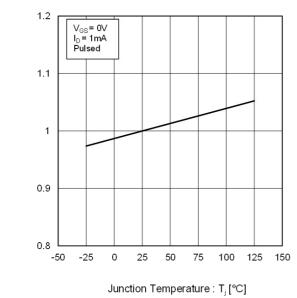
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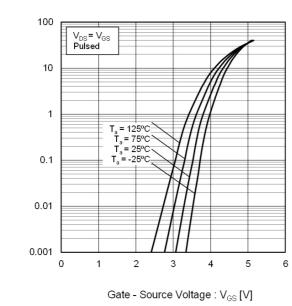
## Fig.6 Typical Output Characteristics(II)

Drain - Source Voltage :  $V_{DS}$  [V]

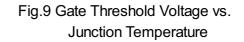
Fig.7 Breakdown Voltage vs. **Junction Temperature** 

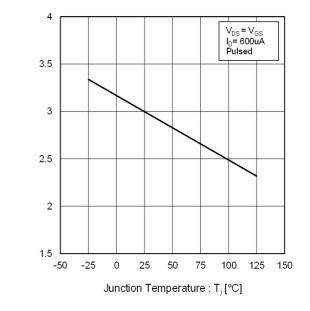






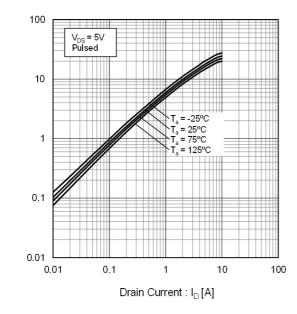
## Fig.8 Typical Transfer Characteristics





Gate Threshold Voltage :  $V_{GS(th)}$  [V]

## Fig.10 Forward Transfer Admittance vs. Drain Current



Drain Current : I<sub>D</sub> [A]



Drain Current Dissipation : I<sub>D</sub>/I<sub>D</sub>max. [%]

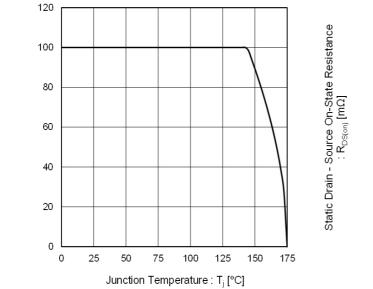


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

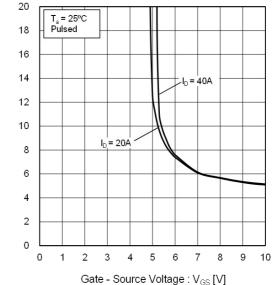
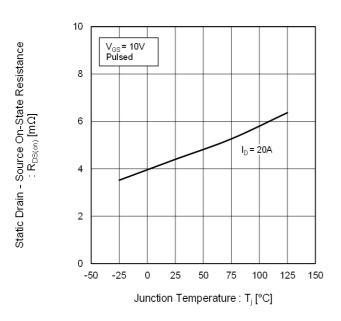


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature





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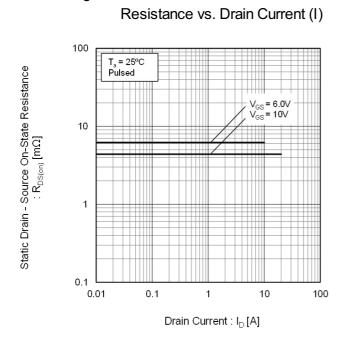
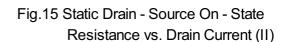


Fig.14 Static Drain - Source On - State



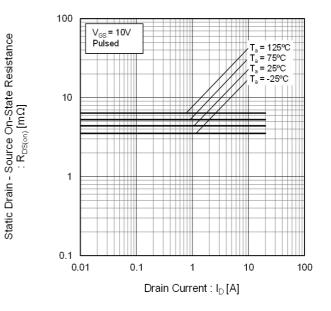
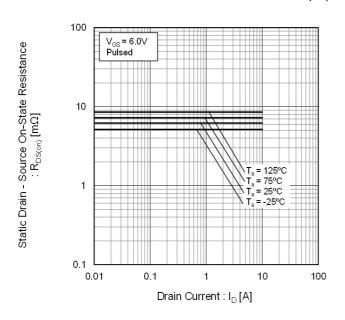
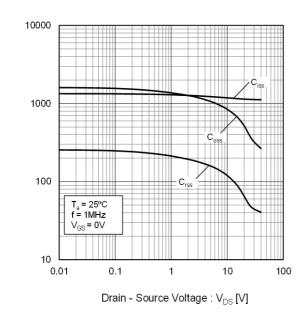


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

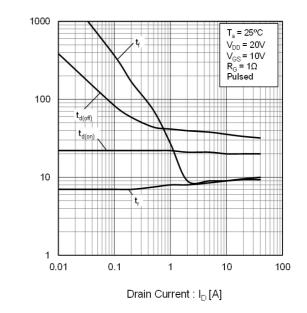








## Fig.17 Typical Capacitance vs. Drain - Source Voltage



Switching Time : t [ns]

## Fig.18 Switching Characteristics

Fig.19 Dynamic Input Characteristics

Gate - Source Voltage : V<sub>GS</sub> [V]

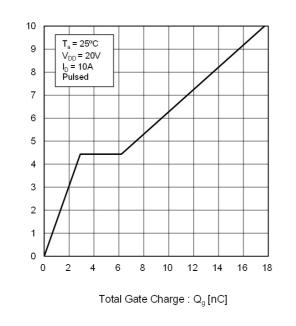
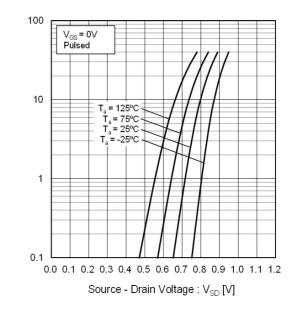


Fig.20 Source Current vs. Source Drain Voltage



Capacitance : C [pF]

Source Current : I<sub>S</sub> [A]



## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

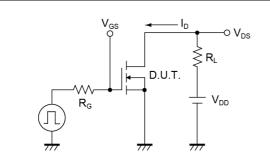


Fig.2-1 Gate Charge Measurement Circuit

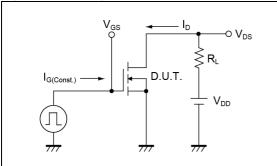


Fig.3-1 Avalanche Measurement Circuit

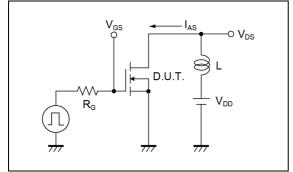


Fig.1-2 Switching Waveforms

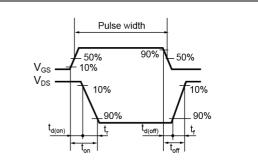
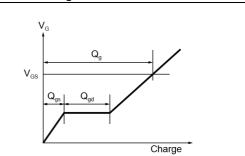
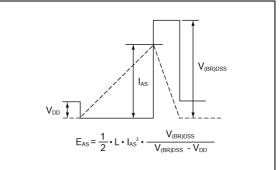


Fig.2-2 Gate Charge Waveform



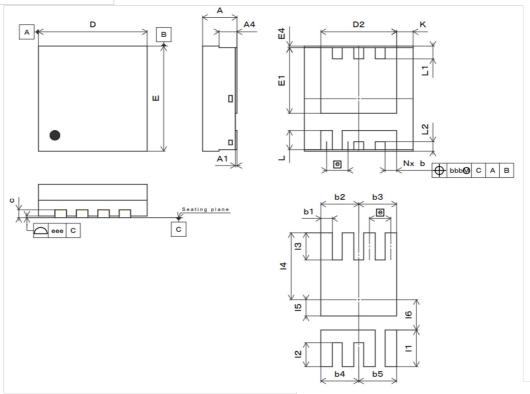
#### Fig.3-2 Avalanche Waveform





#### Dimensions

#### DFN3333T8LSAB



#### [reference pattern of soldering pads]

	DIM			HES	
DIV	MIN	MAX	MIN	MAX	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.050	0.000	0.002	
A4	0.300	-	0.012	-	
b	0.250	0.450	0.010	0.018	
С	0.100	0.300	0.004	0.012	
D	3.200	3.400	0.126	0.134	
D2	2.200	2.400	0.087	0.094	
E	3.200	3.400	0.126	0.134	
E1	1.960	2.160	0.077	0.085	
E4	0.005	-	0.000	-	
е	0.6	350		)26	
K	0.400	0.600	0.016	0.024	
L	0.500	0.700	0.020	0.028	
L1	0.300	0.500	0.012	0.020	
L2	0.200	0.400	0.008	0.016	
N			8		
DIM	MILIME	TERS	INC	HES	
Diivi	MIN	MAX	MIN	MAX	
b1	0.3	350	0.014		
b2		50		)45	
b3		150		)45	
b4		50	0.045		
b5	1.1	1.150		)45	
1	1.150		0.045		
12	0.750		0.030		
13	0.850		0.0	)33	
14	2.1	00	0.083		
15	0.5	510	0.020		
16	0.9	950	0.0	)37	

Dimension in mm/inches



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CLASSⅣ	CLASSI	CLASSII	CLASSⅢ

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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