

RJ1L04BBG

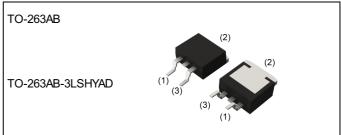
Nch 60V 100A Power MOSFET

| V _{DSS} | 60V |
|----------------------------|-------|
| R _{DS(on)} (Max.) | 4.6mΩ |
| I _D | ±100A |
| P _D | 89W |

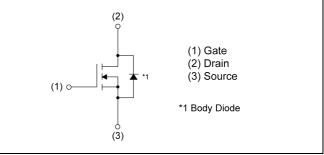
Features

- 1) Low on resistance
- 2) High power package(TO263AB)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen free
- 5) 100% Rg and UIS tested

Outline



Inner circuit



Packaging specifications

| | | Packing | Embossed Tape |
|-----------------|------|-----------------|------------------|
| | | Reel size (mm) | 330 |
| ● Application | Туре | Tape width (mm) | 24 |
| Switching | | Quantity (pcs) | 800 |
| Motor drives | | Taping code | TL1 |
| DC/DC converter | | Marking | RJ1L04BBG |
| | | | |

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

| Para | meter | Symbol | Value | Unit |
|--------------------------------------|------------------------------------|--------------------|-------------|------|
| Drain - Source voltage | | V _{DSS} | 60 | V |
| Silicon limit (V _{GS} =10V) | | I _D *1 | ±100 | А |
| Continuous drain current | $T_c = 25^{\circ}C (V_{GS} = 10V)$ | ۱ _D *2 | ±40 | А |
| Pulsed drain current | | ا _{DP} *3 | ±400 | А |
| Gate - Source voltage | V _{GSS} | ±20 | V | |
| Avalanche current, single pulse | | I_{AS}^{*4} | 33 | А |
| Avalanche energy, single p | E _{AS} *4 | 85 | mJ | |
| Power dissipation | | P _D *2 | 89 | W |
| Junction temperature | | Tj | 150 | °C |
| Operating junction and stor | age temperature range | T _{stg} | -55 to +150 | C° |

•Thermal resistance

| Parameter | Symbol | Values | | | Linit |
|-------------------------------------|-----------------|--------|------|------|-------|
| Falameter | | Min. | Тур. | Max. | Unit |
| Thermal resistance, junction - case | R_{thJC}^{*2} | - | - | 1.4 | °C/W |

• Electrical characteristics (T_a = 25°C)

| Deremeter | Symbol Conditions - | | Values | | | Linit |
|--|--|--|--------|------|------|-------|
| Parameter | | | Min. | Тур. | Max. | Unit |
| Drain - Source breakdown voltage | $V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$ | | 60 | - | - | V |
| Breakdown voltage temperature coefficient | $\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$ | $\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1 \text{mA}$ referenced to 25°C | | 38.9 | - | mV/°C |
| Zero gate voltage drain current | I _{DSS} | V _{DS} = 60V, V _{GS} = 0V | - | - | 2 | μA |
| Gate - Source leakage current | I_{GSS} V_{GS} = ±20V, V_{DS} = 0V | | - | - | ±200 | nA |
| Gate threshold voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 1mA$ | 1.0 | - | 2.5 | V |
| Gate threshold voltage temperature coefficient | $\frac{\Delta V_{GS(th)}}{\Delta T_j}$ | I _D = 1mA referenced to 25°C | - | -4.7 | - | mV/°C |
| Static drain - source | D *5 | V _{GS} = 10V, I _D = 40A | - | 3.5 | 4.6 | |
| on - state resistance | R _{DS(on)} *5 | V _{GS} = 4.5V, I _D = 20A | - | 4.6 | 6.5 | mΩ |
| Gate resistance | R _G | - | - | 0.9 | - | Ω |
| Forward Transfer Admittance | Y _{fs} * ⁵ | V _{DS} = 5V, I _D = 20A | 22 | - | - | S |

*1 Limited by silicon chip capability.

*2 T_c=25°C, Limited only by maximum temperature allowed.

*3 Pw \leq 10µs, Duty cycle \leq 1%

*4 L \simeq 0.1mH, V_{DD} = 30V, R_G = 25 Ω , Starting T_j = 25°C Fig.3-1,3-2

*5 Pulsed



•Electrical characteristics (T_a = 25°C)

| Deremeter | Cumph of | Conditions | Values | | | Lincit |
|------------------------------|------------------------|-----------------------------------|--------|------|------|--------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
| Input capacitance | C _{iss} | V _{GS} = 0V | - | 2950 | - | |
| Output capacitance | C _{oss} | V _{DS} = 30V | - | 750 | - | pF |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | - | 55 | - | |
| Turn - on delay time | t _{d(on)} *5 | $V_{DD} \simeq 30V, V_{GS}$ = 10V | - | 21 | - | |
| Rise time | t _r *5 | I _D = 20A | - | 12 | - | |
| Turn - off delay time | t _{d(off)} *5 | $R_L \simeq 1.5\Omega$ | - | 72 | - | ns |
| Fall time | t _f *5 | R _G = 10Ω | - | 27 | - | |

• Gate charge characteristics ($T_a = 25^{\circ}C$)

| Deremeter | Sumbol | Conditions | | Values | | | l lait |
|----------------------|--------------------|----------------------|------------------------|--------|------|------|--------|
| Parameter | Symbol Conditions | | Min. | Тур. | Max. | Unit | |
| Total acto charge | O *5 | | V _{GS} = 10V | - | 47.0 | - | |
| Total gate charge | Q_g^{*5} | $V_{DD} \simeq 30V$ | | - | 23.0 | - | nC |
| Gate - Source charge | Q_{gs}^{*5} | I _D = 40A | V _{GS} = 4.5V | - | 8.8 | - | nc |
| Gate - Drain charge | Q _{gd} *5 | | | - | 7.9 | - | |

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

| Deremeter | Symbol Conditions | | Values | | | Unit |
|----------------------------|------------------------|--|--------|------|------|------|
| Parameter | | | Min. | Тур. | Max. | Unit |
| Continuous forward current | ${\sf I}_{\sf S}^{*2}$ | | - | - | 40 | А |
| Pulse forward current | I_{SP}^{*3} | - | - | - | 400 | А |
| Forward voltage | V_{SD}^{*5} | V _{GS} = 0V, I _S = 40A | - | - | 1.2 | V |
| Reverse recovery time | t _{rr} *5 | I _S = 40A, V _{GS} =0V | - | 49 | - | ns |
| Reverse recovery charge | Q _{rr} *5 | di/dt = 100A/µs | - | 64 | - | nC |





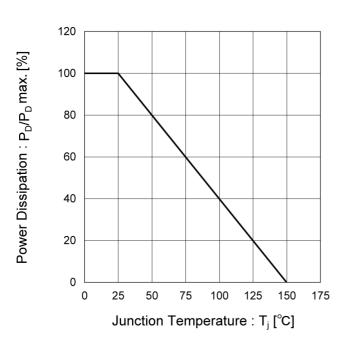


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

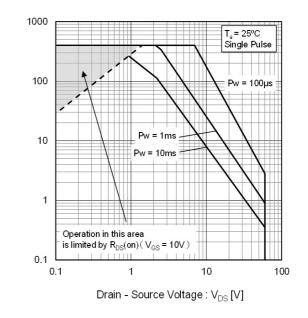
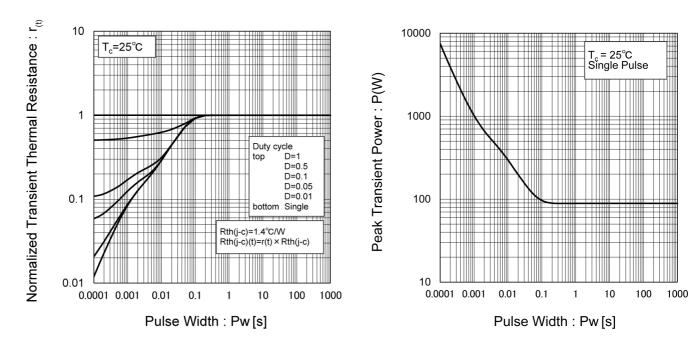


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Fig.4 Single Pulse Maximum Power Dissipation



Drain Current : I_D [A]



• Electrical characteristic curves

Drain Current : I_D [A]

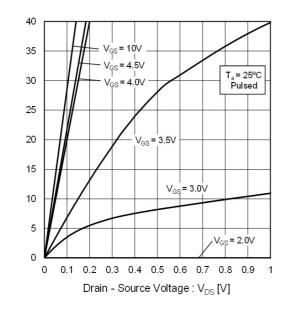
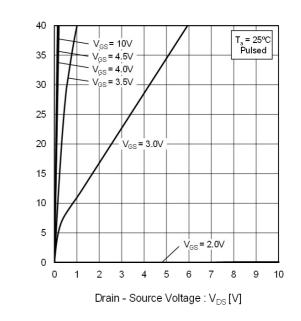


Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.7 Normalized Breakdown Voltage vs. Junction Temperature

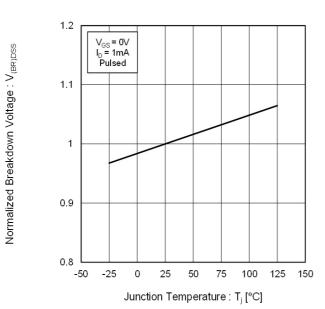
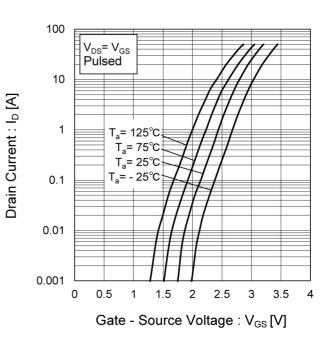


Fig.8 Typical Transfer Characteristics





• Electrical characteristic curves

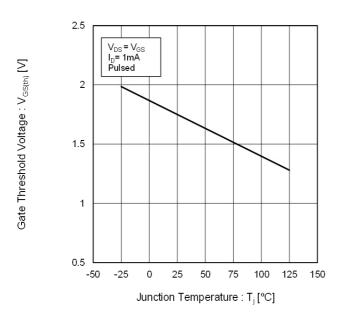


Fig.9 Gate Threshold Voltage vs. Junction Temperature

Fig.10 Forward Transfer Admittance vs. Drain Current

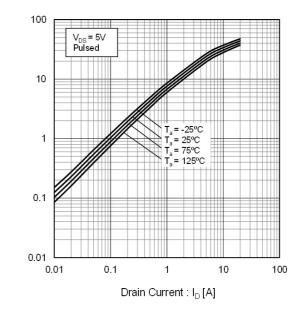


Fig.11 Drain Current Derating Curve

Drain Current Dissipation : I_D/I_Dmax. [%]

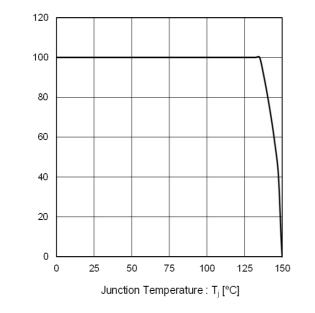
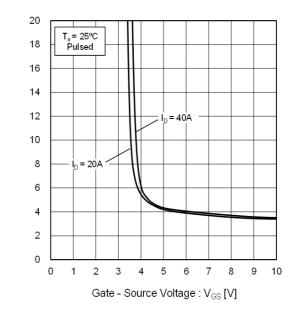


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Static Drain - Source On-State Resistance : $R_{\text{DS}(\text{on})}$ [mΩ]

Forward Transfer Admittance : Y_{fs} [S]



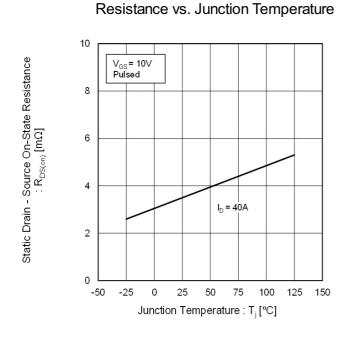


Fig.13 Static Drain - Source On - State

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

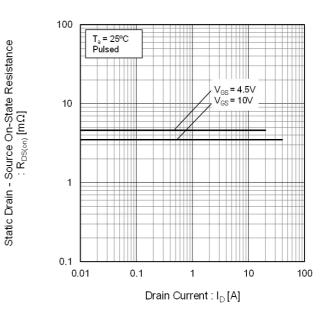
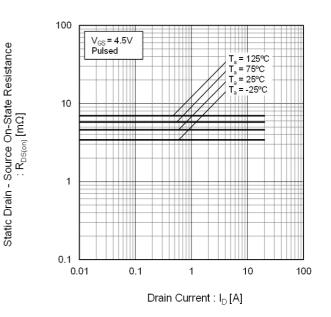


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

100 V_{GS} = 10V Static Drain - Source On-State Resistance Pulsed T_a = 125°C = 75°C = 25°C = -25°C 10 $: R_{DS(on)} [m\Omega]$ 1 0.1 0.01 0.1 1 10 100 Drain Current : I_D [A]

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



7/10



• Electrical characteristic curves

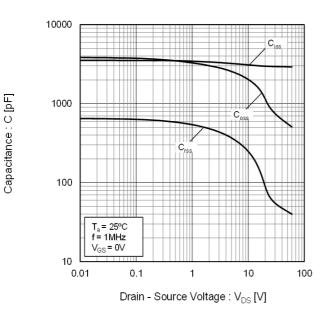


Fig.17 Typical Capacitances vs. Drain - Source Voltage

Fig.18 Switching Characteristics

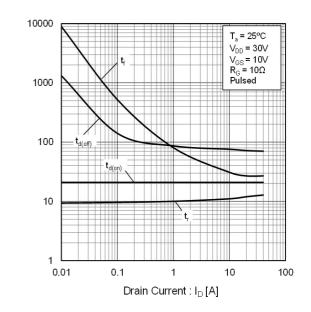


Fig.19 Typical Gate Charge

Gate - Source Voltage : V_{GS} [V]

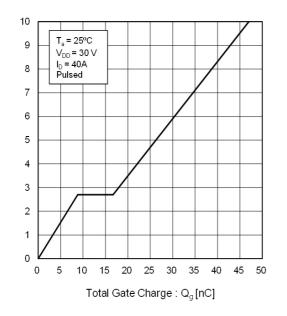
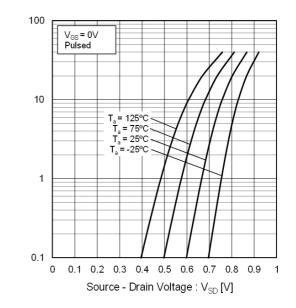


Fig.20 Source Current vs. Source Drain Voltage



Source Current : I_s [A]

Switching Time : t [ns]



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

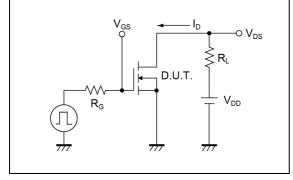


Fig.2-1 Gate Charge Measurement Circuit

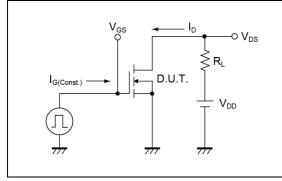


Fig.3-1 Avalanche Measurement Circuit

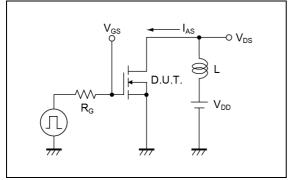


Fig.1-2 Switching Waveforms

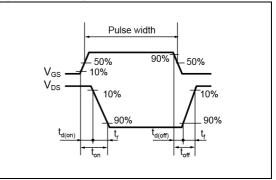


Fig.2-2 Gate Charge Waveform

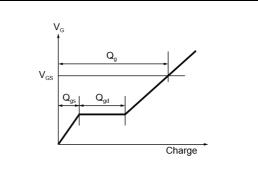
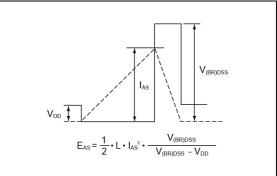
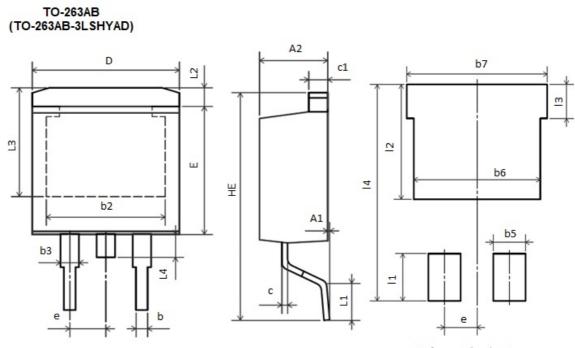


Fig.3-2 Avalanche Waveform



Dimensions



Reference land pattern

| DIM | MILIM | ETERS | INC | HES |
|-----|-------|-------|-------|-------|
| DIN | MIN | MAX | MIN | MAX |
| A1 | 0.00 | 0.25 | 0.000 | 0.010 |
| A2 | 4.37 | 4.77 | 0.168 | 0.188 |
| b | 0.70 | 0.96 | 0.028 | 0.038 |
| b2 | 7.50 | _ | 0.295 | |
| b3 | 1.17 | 1.47 | 0.046 | 0.058 |
| с | 0.30 | 0.53 | 0.012 | 0.021 |
| c1 | 1.22 | 1.42 | 0.048 | 0.056 |
| D | 9.86 | 10.36 | 0.388 | 0.408 |
| E | 8.50 | 8.90 | 0.335 | 0.350 |
| e | 2.5 | 54 | 0.1 | 00 |
| HE | 14.70 | 15.50 | 0.579 | 0.610 |
| L1 | 2.00 | 2.60 | 0.079 | 0.102 |
| L2 | 1.07 | 1.47 | 0.042 | 0.058 |
| L3 | 6.60 | 3.75 | 0.260 | 1.7 |
| L4 | 1.40 | 1.70 | 0.055 | 0.067 |

| DIM | MILIMETERS | INCHES |
|-----|------------|--------|
| DIM | NOM | NOM |
| 1 | 3.5 | 0.14 |
| 12 | 8.5 | 0.33 |
| 13 | 2.5 | 0.10 |
| 4 | 16.0 | 0.63 |
| b5 | 2.5 | 0.10 |
| b6 | 10.0 | 0.39 |
| b7 | 11.0 | 0.43 |

Dimension in mm / inches



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|---|
|---|

| JAPAN | USA | EU | CHINA |
|--------|---------|------------|---------|
| CLASSⅢ | CLASSI | CLASS II b | CLASSII |
| CLASSⅣ | CLASSII | CLASSⅢ | CLASSI |

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 - [f] Sealing or coating our Products with resin or other coating materials
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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 - [d] the Products are exposed to high Electrostatic
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