## Nch 60V 240A Power MOSFET

V <sub>DSS</sub>	60V
R <sub>DS(on)</sub> (Max.)	1.85mΩ
I <sub>D</sub>	±240A
P <sub>D</sub>	192W

# ● Features

- 1) Low on resistance
- 2) High power package(TO263AB)
- 3) Pb-free plating; RoHS compliant
- 4) Halogen free
- 5) 100% Rg and UIS tested

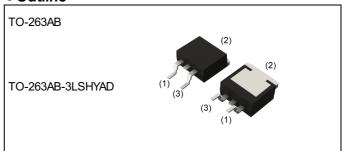
## Application

Switching

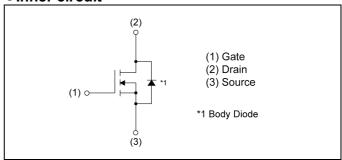
Motor drives

DC/DC converter

### Outline



## •Inner circuit



Packaging specifications

	ing opcomoducino	
	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	24
	Quantity (pcs)	800
	Taping code	TL1
	Marking	RJ1L10BBG

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Para	meter	Symbol	Value	Unit
Drain - Source voltage		$V_{DSS}$	60	V
Cantinuous drain aurent	Silicon limit (V <sub>GS</sub> =10V)	I <sub>D</sub> *1	±240	Α
Continuous drain current	$T_c = 25^{\circ}C (V_{GS} = 10V)$	I <sub>D</sub> *2	±105	Α
Pulsed drain current		I <sub>DP</sub> *3	±900	Α
Gate - Source voltage		$V_{GSS}$	±20	V
Avalanche current, single pulse		l <sub>AS</sub> *4	70	Α
Avalanche energy, single pulse		E <sub>AS</sub> *4	380	mJ
Power dissipation		P <sub>D</sub> *2	192	W
Junction temperature		T <sub>j</sub>	150	°C
Operating junction and stor	age temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Parameter	Symbol	Values			Unit
raidilletei	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R <sub>thJC</sub> *2	-	1	0.65	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Doromotor	Symbol Conditions —		Values			Lleit
Parameter			Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_i} I_D = 1 \text{mA}$ referenced to 25°C		38.9	-	mV/°C
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	5	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	ı	ı	±200	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 1mA$	1.0	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-4.7	-	mV/°C
Static drain - source	D *5	V <sub>GS</sub> = 10V, I <sub>D</sub> = 90A	-	1.42	1.85	O
on - state resistance	R <sub>DS(on)</sub> *5	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 50A	-	1.72	2.41	mΩ
Gate resistance	$R_G$	-	-	0.8	-	Ω
Forward Transfer Admittance	Y <sub>fs</sub>  *5	V <sub>DS</sub> = 5V, I <sub>D</sub> = 50A	64	-	-	S

<sup>\*1</sup> Limited by silicon chip capability.

<sup>\*2</sup>  $T_c$ =25°C, Limited only by maximum temperature allowed.

<sup>\*3</sup> Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

<sup>\*4</sup> L  $\simeq$  0.1mH, V<sub>DD</sub> = 30V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*5</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Daramatar	Cumph of C	Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uniit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	11000	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 30V	-	2540	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	130	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 30V, V_{GS} = 10V$	-	49	-	
Rise time	<b>t</b> r*5	I <sub>D</sub> = 50A	1	31	-	20
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L \simeq 0.6\Omega$	-	230	-	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	140	-	

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymahal	Conditions		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total gate above	O *5		V <sub>GS</sub> = 10V	-	160	-	
Total gate charge	$Q_g^{*5}$	$V_{DD} \simeq 30V$		-	80	-	<b>5</b> C
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 50A	V <sub>GS</sub> = 4.5V	-	30	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5			-	23	-	

## ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I <sub>S</sub> *2		-	-	105	Α
Pulse forward current	I <sub>SP</sub> *3	-	-	-	900	Α
Forward voltage	V <sub>SD</sub> *5	V <sub>GS</sub> = 0V, I <sub>S</sub> = 90A	-	-	1.2	V
Reverse recovery time	t <sub>rr</sub> *5	I <sub>S</sub> = 50A, V <sub>GS</sub> =0V	-	85	-	ns
Reverse recovery charge	Q <sub>rr</sub> *5	di/dt = 100A/μs	-	175	-	nC

Fig.1 Power Dissipation Derating Curve

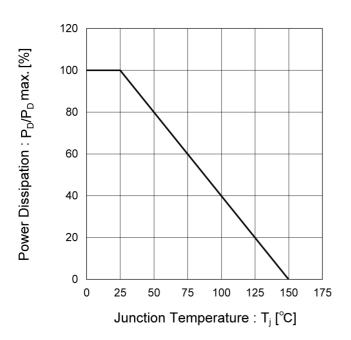
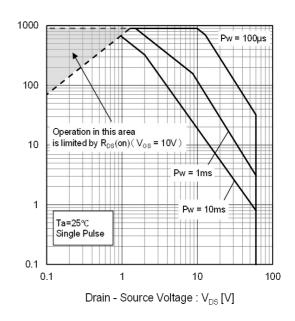


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

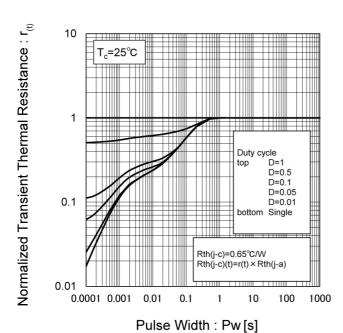


Fig.4 Single Pulse Maximum Power Dissipation

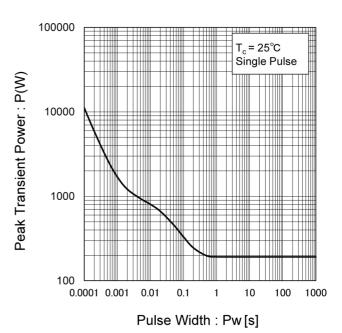


Fig.5 Typical Output Characteristics(I)

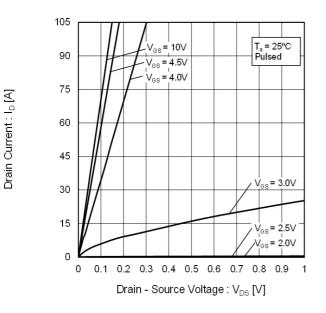
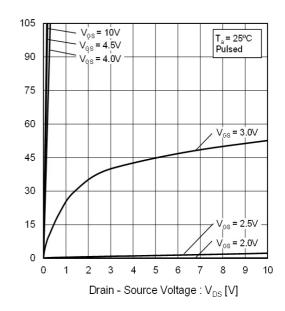


Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Fig.7 Normalized Breakdown Voltage vs. Junction Temperature

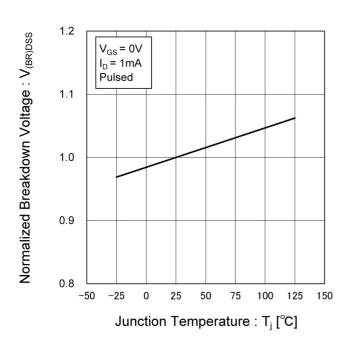


Fig.8 Typical Transfer Characteristics

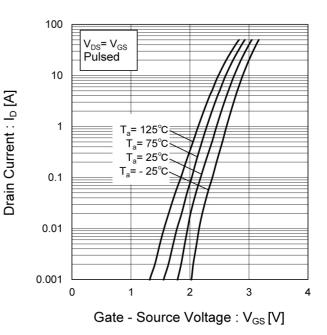


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

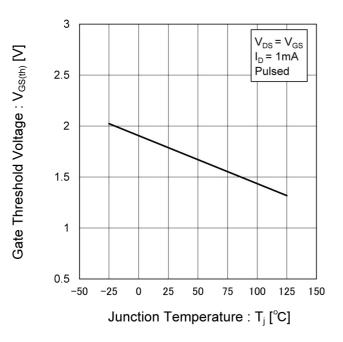


Fig.10 Forward Transfer Admittance vs.
Drain Current

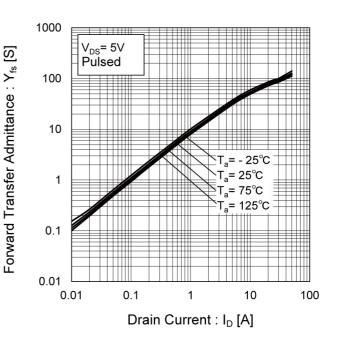


Fig.11 Drain Current Derating Curve

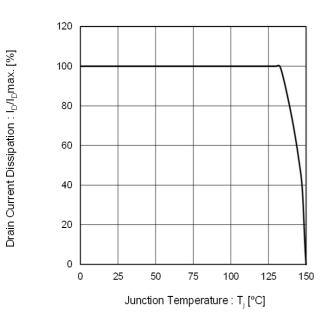
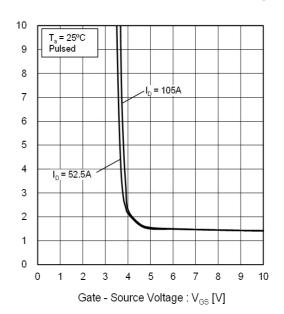


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Static Drain - Source On-State Resistance :  $R_{\mathrm{DS(on)}}$  [m $\Omega]$ 

Static Drain - Source On-State Resistance

Static Drain - Source On-State Resistance

 $:R_{DS(on)}\left[ m\Omega \right]$ 

## • Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

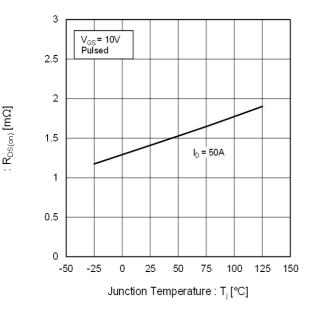
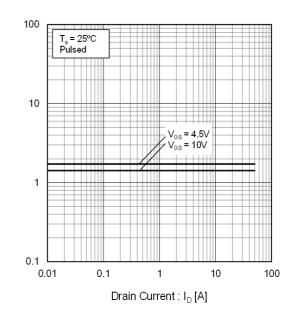


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)



Static Drain - Source On-State Resistance :  $R_{\mathrm{DS(on)}}\left[m\Omega\right]$ 

Static Drain - Source On-State Resistance :  $R_{\mathrm{DS(on)}}\left[m\Omega\right]$ 

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

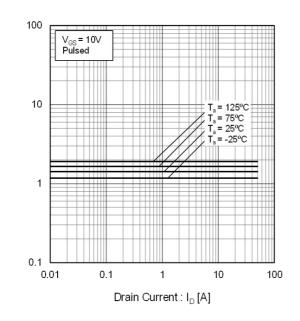


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

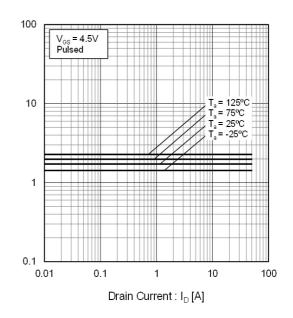


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

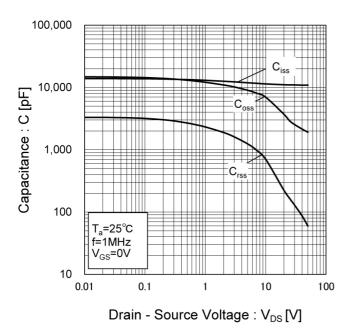


Fig.18 Switching Characteristics

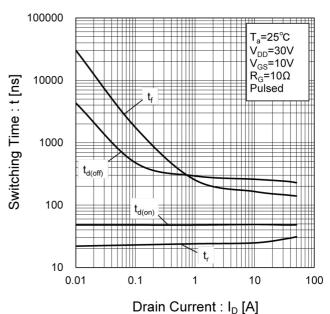


Fig.19 Typical Gate Charge

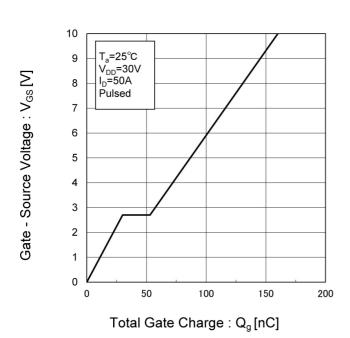
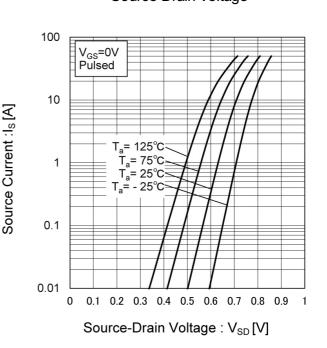


Fig.20 Source Current vs.

Source Drain Voltage



## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

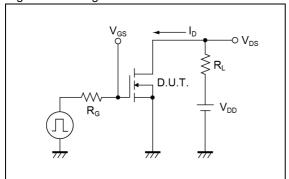


Fig.1-2 Switching Waveforms

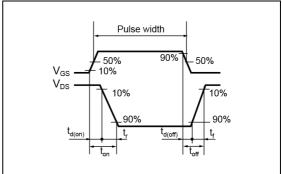


Fig.2-1 Gate Charge Measurement Circuit

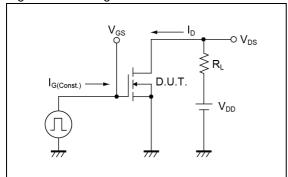


Fig.2-2 Gate Charge Waveform

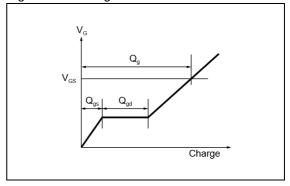


Fig.3-1 Avalanche Measurement Circuit

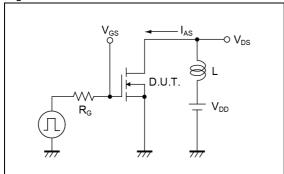
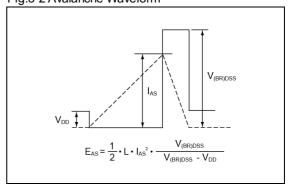
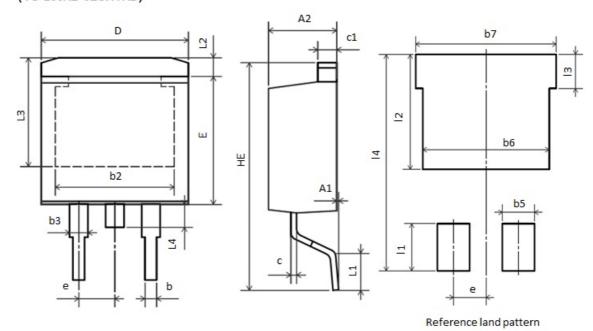


Fig.3-2 Avalanche Waveform



## Dimensions

## TO-263AB (TO-263AB-3LSHYAD)



DIM	MILIM	ETERS	INC	HES
DIN	MIN	MAX	MIN	MAX
A1	0.00	0.25	0.000	0.010
A2	4.37	4.77	0.168	0.188
b	0.70	0.96	0.028	0.038
b2	7.50	_	0.295	200
b3	1.17	1.47	0.046	0.058
С	0.30	0.53	0.012	0.021
c1	1.22	1.42	0.048	0.056
D	9.86	10.36	0.388	0.408
Е	8.50	8.90	0.335	0.350
е	2.	2.54		00
HE	14.70	15.50	0.579	0.610
L1	2.00	2.60	0.079	0.102
L2	1.07	1.47	0.042	0.058
L3	6.60	0.75	0.260	8.77
L4	1.40	1.70	0.055	0.067

DIM	MILIMETERS	INCHES
DIM	NOM	NOM
1	3.5	0.14
12	8.5	0.33
13	2.5	0.10
14	16.0	0.63
b5	2.5	0.10
ь6	10.0	0.39
b7	11.0	0.43

Dimension in mm / inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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