

RJ1N10BBH

V _{DSS}	80V
R _{DS(on)} (Max.)	2.00mΩ
Ι _D	±235A
P _D	189W

Features

- 1) Low on resistance
- 2) High power mold package (TO263AB)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen Free

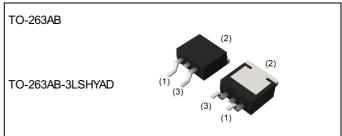
Application

DC/DC converter

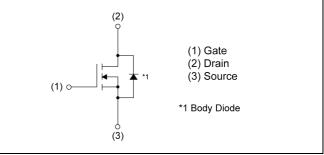
Switching Motor drives

5) 100% Rg and UIS tested

Outline



Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	24
•••	Quantity (pcs)	800
	Taping code	TL1
	Marking	RJ1N10BBH

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Para	meter	Symbol	Value	Unit
Drain - Source voltage		V _{DSS}	80	V
Continuous dusis sumont	Silicon limit (V _{GS} =10V)	۱ _D *1	±235	А
Continuous drain current	$T_c = 25^{\circ}C (V_{GS} = 10V)$	Ι _D *2	±105	А
Pulsed drain current		ا _{DP} * ³	±900	А
Gate - Source voltage		V _{GSS}	±20	V
Avalanche current, single p	ulse	I_{AS}^{*4}	62	A
Avalanche energy, single p	ulse	E_{AS}^{*4}	312	mJ
Power dissipation		P _D *2	189	W
Junction temperature		Т _ј	150	C°
Operating junction and stor	age temperature range	T _{stg}	-55 to +150	°C

•Thermal resistance

Parameter	Symbol	Values			Unit
Falameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}^{*2}	-	-	0.66	°C/W

•Electrical characteristics (T_a = 25°C)

Deremeter	Currence of	Symbol Conditions –		Values			
Parameter	Symbol		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} _{D} = 1 \text{mA}$ referenced to 25°C		-	58	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 80V, V _{GS} = 0V	-	-	5	μA	
Gate - Source leakage current	I_{GSS} V_{GS} = ±20V, V_{DS} = 0V		-	-	±500	nA	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 1mA$	2.0	-	4.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	-5.0	-	mV/°C	
Static drain - source	D *5	V _{GS} = 10V, I _D = 90A	-	1.66	2.00		
on - state resistance	R _{DS(on)} *5	V _{GS} = 6V, I _D = 50A	-	1.91	2.70	mΩ	
Gate resistance	R _G	-	-	0.5	-	Ω	
Forward Transfer Admittance	Y _{fs} * ⁵	V _{DS} = 5V, I _D = 50A	55	-	-	S	

*1 Limited by silicon chip capability.

*2 T_c=25°C, Limited only by maximum temperature allowed.

*3 Pw \leq 10µs, Duty cycle \leq 1%

*4 L \simeq 0.1mH, V_{DD} = 40V, R_G = 25 Ω , Starting T_j = 25°C Fig.3-1,3-2

*5 Pulsed



•Electrical characteristics (T_a = 25°C)

Deremeter	Cumph of	Conditions	Values			Lipit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	11800	-		
Output capacitance	C _{oss}	V _{DS} = 40V	-	2560	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	95	-		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 40V, V_{GS}$ = 10V	-	58	-		
Rise time	t _r *5	I _D = 50A	-	66	-		
Turn - off delay time	t _{d(off)} *5	$R_L \simeq 0.8\Omega$	-	250	-	ns	
Fall time	t _f *5	R _G = 10Ω	-	340	-		

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Sumbol	Conditions		Values			l lait
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Total acta charge	O *5		V _{GS} = 10V	-	185	-	
Total gate charge	Q_g^{*5}	$V_{DD}\simeq 40V$		-	120	-	nC
Gate - Source charge	Q _{gs} *5	I _D = 50A	V _{GS} = 6V	-	39	-	nc
Gate - Drain charge	Q _{gd} *5			-	38	-	

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Deremeter	Symbol Conditions		Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ _S *2		-	-	105	А
Pulse forward current	I_{SP}^{*3}	-	-	-	900	А
Forward voltage	V_{SD}^{*5}	V _{GS} = 0V, I _S = 90A	-	-	1.5	V
Reverse recovery time	t _{rr} *5	I _S = 50A, V _{GS} =0V	-	92	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/µs	-	220	-	nC



• Electrical characteristic curves

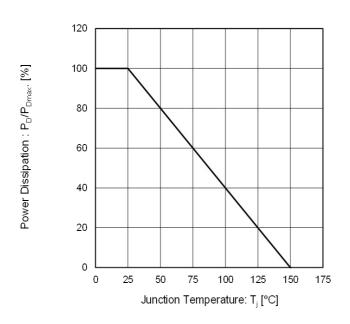
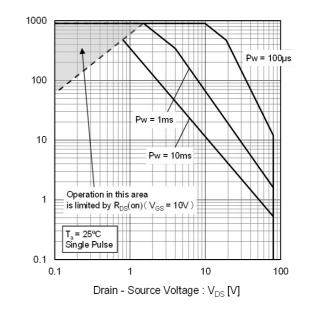


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

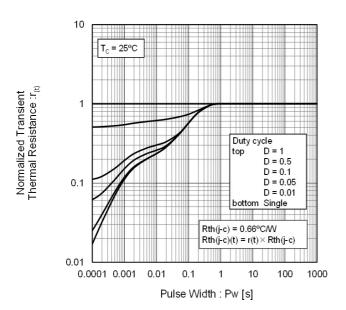
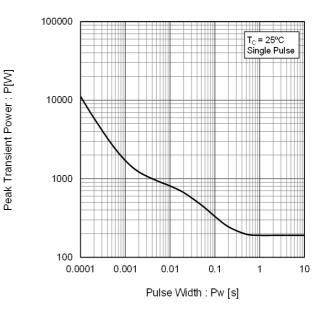


Fig.4 Single Pulse Maximum Power Dissipation





Drain Current : I_D [A]

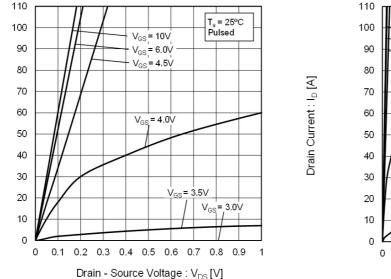


Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)

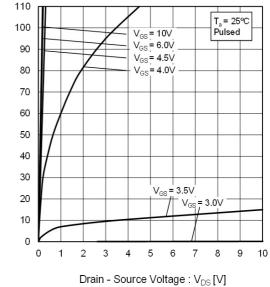


Fig.7 Normalized Breakdown Voltage vs. Junction Temperature

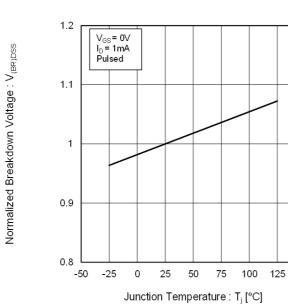
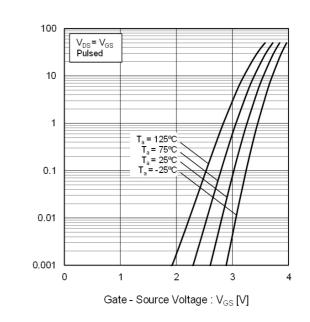


Fig.8 Typical Transfer Characteristics



150

Drain Current : I_D [A]



• Electrical characteristic curves

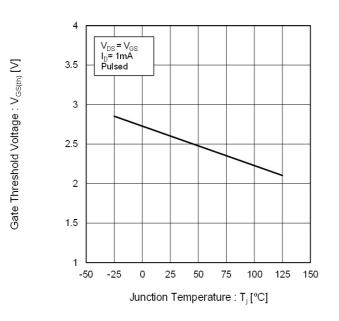


Fig.9 Gate Threshold Voltage vs. Junction Temperature

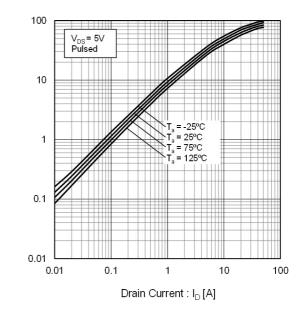


Fig.10 Forward Transfer Admittance vs. Drain Current

Fig.11 Drain Current Derating Curve

Drain Current Dissipation : I_D/I_D max. [%]

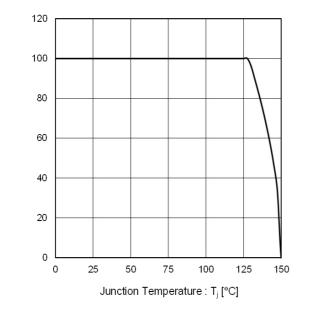
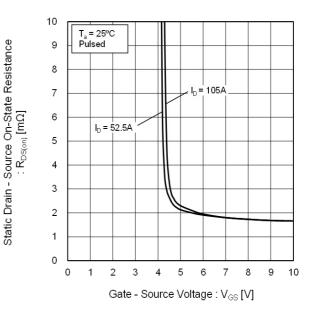


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage





Forward Transfer Admittance : Y_{fs} [S]

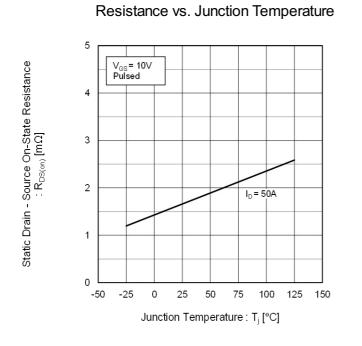


Fig.13 Static Drain - Source On - State

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

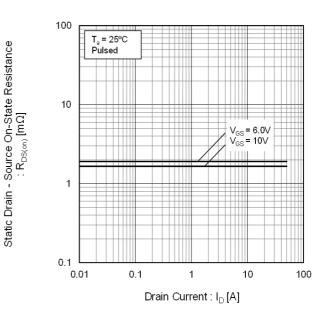


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

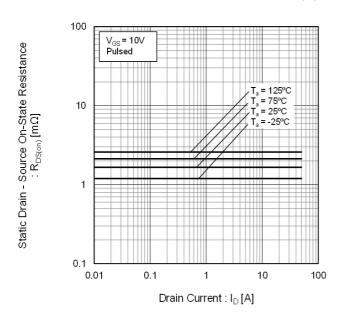
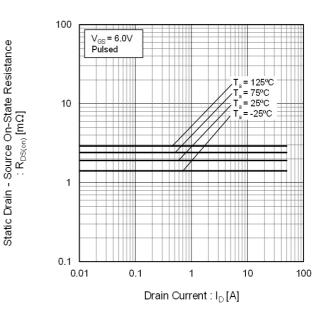


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)





• Electrical characteristic curves

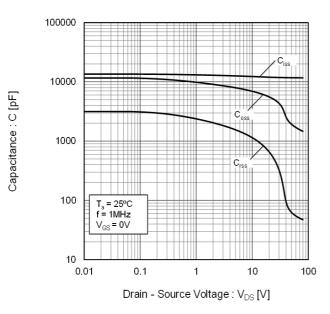


Fig.17 Typical Capacitances vs. Drain - Source Voltage

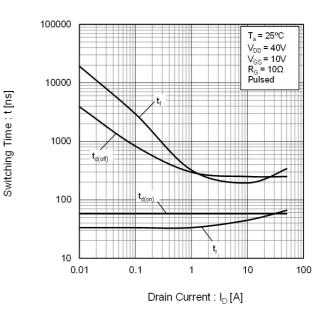


Fig.18 Switching Characteristics

Fig.19 Typical Gate Charge

Gate - Source Voltage : V_{GS} [V]

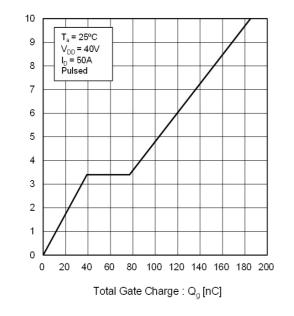
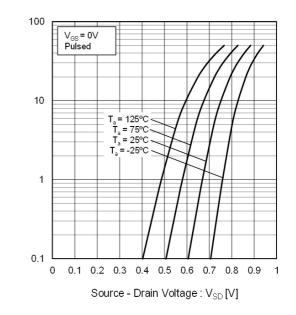


Fig.20 Source Current vs. Source Drain Voltage



Source Current : I_s [A]



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

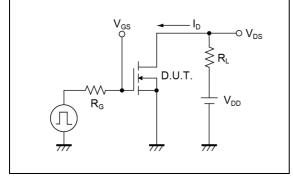


Fig.2-1 Gate Charge Measurement Circuit

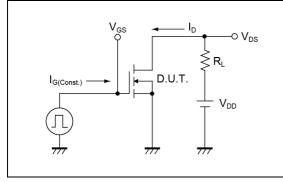


Fig.3-1 Avalanche Measurement Circuit

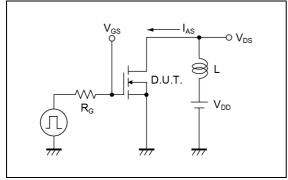


Fig.1-2 Switching Waveforms

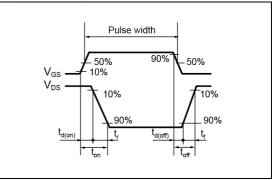


Fig.2-2 Gate Charge Waveform

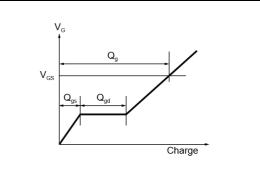
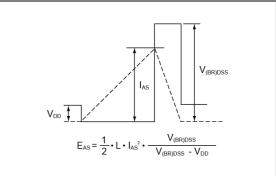
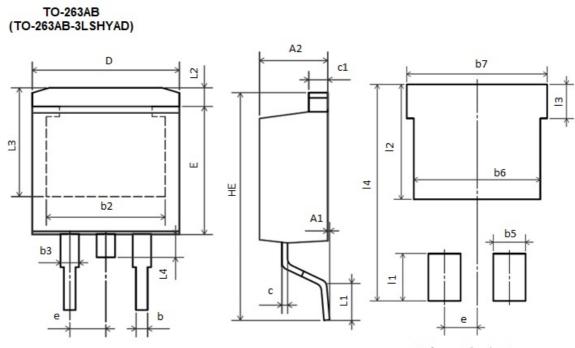


Fig.3-2 Avalanche Waveform



Dimensions



Reference land pattern

DIM	MILIM	ETERS	INC	HES
DIN	MIN	MAX	MIN	MAX
A1	0.00	0.25	0.000	0.010
A2	4.37	4.77	0.168	0.188
b	0.70	0.96	0.028	0.038
b2	7.50		0.295	<u> </u>
b3	1.17	1.47	0.046	0.058
с	0.30	0.53	0.012	0.021
c1	1.22	1.42	0.048	0.056
D	9.86	10.36	0.388	0.408
E	8.50	8.90	0.335	0.350
е	2.5	54	0.100	
HE	14.70	15.50	0.579	0.610
L1	2.00	2.60	0.079	0.102
L2	1.07	1.47	0.042	0.058
L3	6.60	1.0	0.260	10.00
L4	1.40	1.70	0.055	0.067

DIM	MILIMETERS	INCHES
	NOM	NOM
1	3.5	0.14
12	8.5	0.33
13	2.5	0.10
4	16.0	0.63
b5	2.5	0.10
b6	10.0	0.39
b7	11.0	0.43

Dimension in mm / inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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