RJ1P07CBH

Nch 100V 120A Power MOSFET

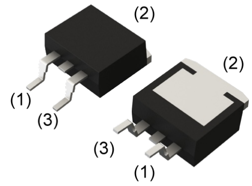
Datasheet

V_{DSS}	100V
$R_{DS(on)}$(Max.)	5.1mΩ
I_D	±120A
P_D	135W

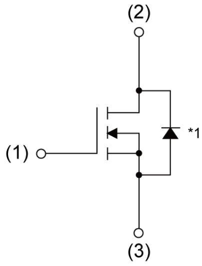
●Outline

TO-263AB

TO-263AB-3LSHYAD



●Inner circuit



(1) Gate
(2) Drain
(3) Source

*1 Body Diode

●Features

1) Low on - resistance
2) High power small mold package (TO263AB)
3) Pb-free plating ; RoHS compliant
4) 100% UIS tested

●Packaging specifications

Type	Packing	Embossed Tape
	Reel size (mm)	330
	Tape width (mm)	24
	Quantity (pcs)	800
	Taping code	TL1
	Marking	RJP07CBH

●Application

Switching

●Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source voltage		V_{DSS}	100	V
Continuous drain current	Silicon limit (V_{GS}=10V)	I_D *1	±120	A
	T_c = 25°C (V_{GS}=10V)	I_D *2	±70	A
Pulsed drain current		I_{DP} *3	±480	A
Gate - Source voltage		V_{GSS}	±20	V
Avalanche current, single pulse		I_{AS} *4	33	A
Avalanche energy, single pulse		E_{AS} *4	89	mJ
Power dissipation		P_D *2	135	W
Junction temperature		T_j	150	℃
Operating junction and storage temperature range		T_{stg}	-55 to +150	℃

●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case	R_{thJC} *2	-	-	0.92	°C/W

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 1mA	100	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I_D = 1mA referenced to 25°C	-	62.3	-	mV/°C
Zero gate voltage drain current	I_{DSS}	V_{DS} = 100V, V_{GS} = 0V	-	-	5	μA
Gate - Source leakage current	I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V	-	-	±500	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 1mA	2.0	-	4.0	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I_D = 1mA referenced to 25°C	-	-4.5	-	mV/°C
Static drain - source on - state resistance	$R_{DS(on)}$ *5	V_{GS} = 10V, I_D = 70A	-	3.9	5.1	mΩ
		V_{GS} = 6V, I_D = 35A	-	4.7	7.1	
Gate resistance	R_G	-	-	0.9	-	Ω
Forward Transfer Admittance	$\|Y_{fs}\|$ *5	V_{DS} = 5V, I_D = 35A	32	-	-	S

*1 Limited by silicon chip capability.

*2 T_C=25°C, Limited only by maximum temperature allowed.

*3 Pw ≤ 10μs, Duty cycle ≤ 1%

*4 L ≃ 0.1mH, V_{DD} = 50V, R_G = 25Ω, Starting T_j = 25°C Fig.3-1,3-2

*5 Pulsed

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input capacitance	C_{iss}	V_{GS} = 0V	-	4650	-	pF
Output capacitance	C_{oss}	V_{DS} = 50V	-	890	-	
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	33	-	
Turn - on delay time	$t_{d(on)}$ *5	$V_{DD} \simeq$ 50V, V_{GS} = 10V	-	40	-	ns
Rise time	t_r *5	I_D = 35A	-	37	-	
Turn - off delay time	$t_{d(off)}$ *5	$R_L \simeq$ 1.42Ω	-	99	-	
Fall time	t_f *5	R_G = 10Ω	-	60	-	

●Gate charge characteristics (T_a = 25°C)

Parameter	Symbol	Conditions		Values			Unit
				Min.	Typ.	Max.	
Total gate charge	Q_g *5	$V_{DD} \simeq$ 50V I_D = 50A	V_{GS} = 10V	-	73.0	-	nC
			V_{GS} = 6V	-	48.0	-	
Gate - Source charge	Q_{gs} *5			-	16.4	-	
Gate - Drain charge	Q_{gd} *5			-	18.9	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Continuous forward current	I_S *2	-	-	-	70	A
Pulse forward current	I_{SP} *3		-	-	480	A
Forward voltage	V_{SD} *5	V_{GS} = 0V, I_S = 70A	-	-	1.2	V
Reverse recovery time	t_{rr} *5	I_S = 50A, V_{GS}=0V di/dt = 100A/μs	-	74	-	ns
Reverse recovery charge	Q_{rr} *5		-	190	-	nC

●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

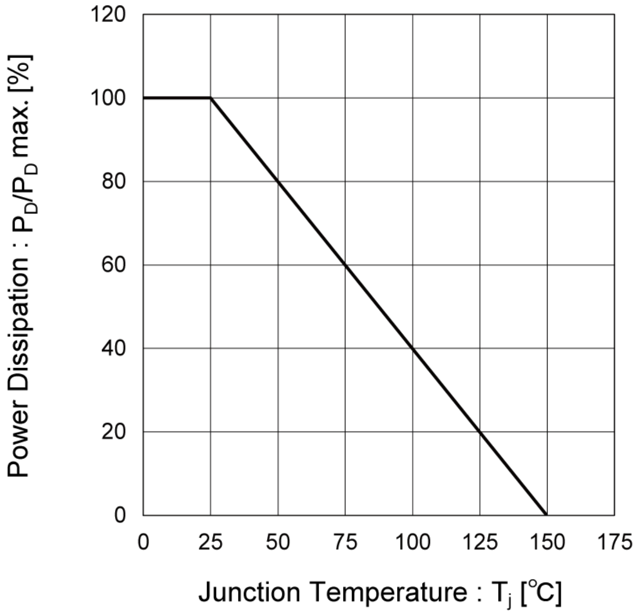


Fig.2 Maximum Safe Operating Area

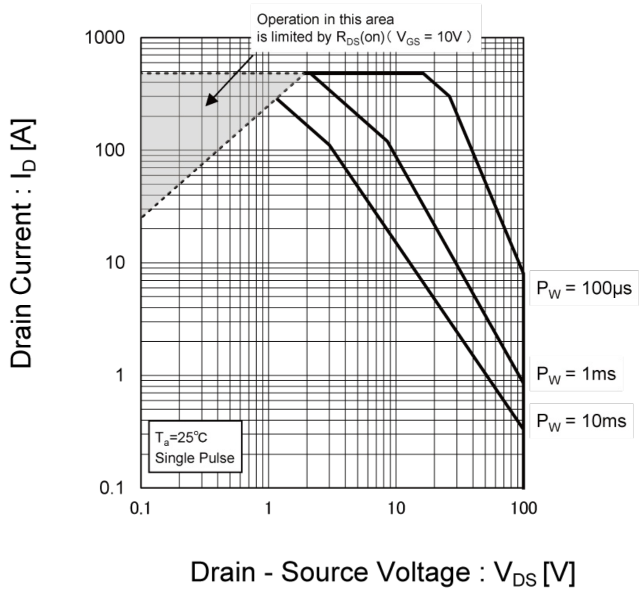


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

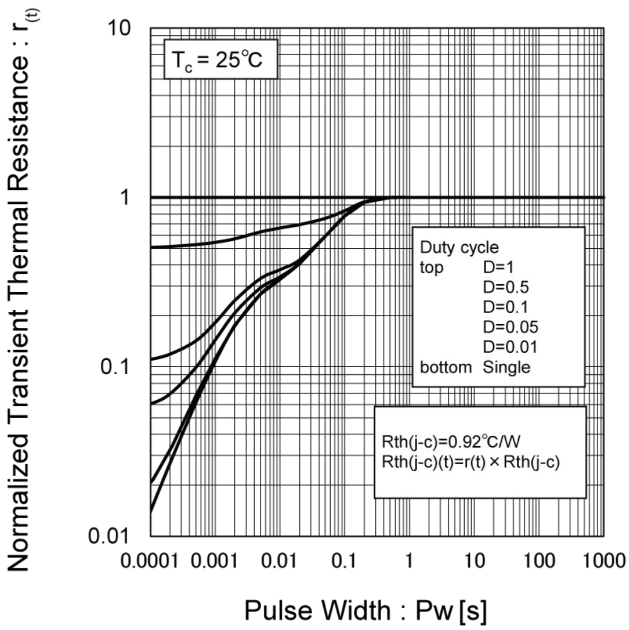
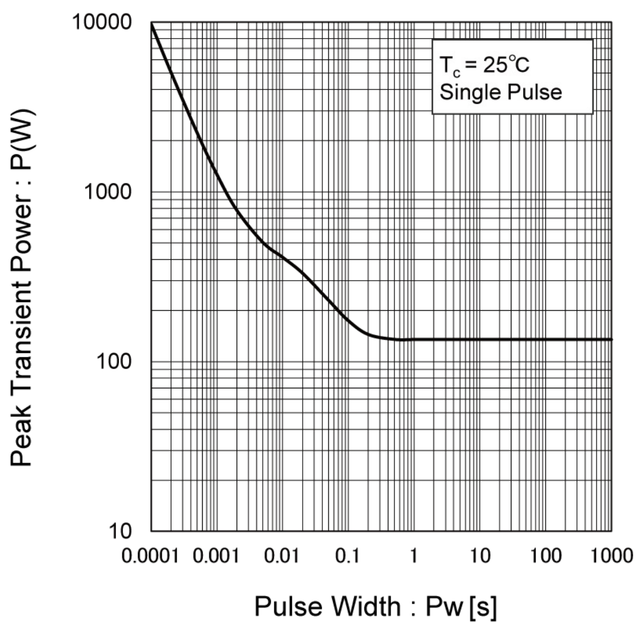


Fig.4 Single Pulse Maximum Power Dissipation



● **Electrical characteristic curves**

Fig.5 Typical Output Characteristics(I)

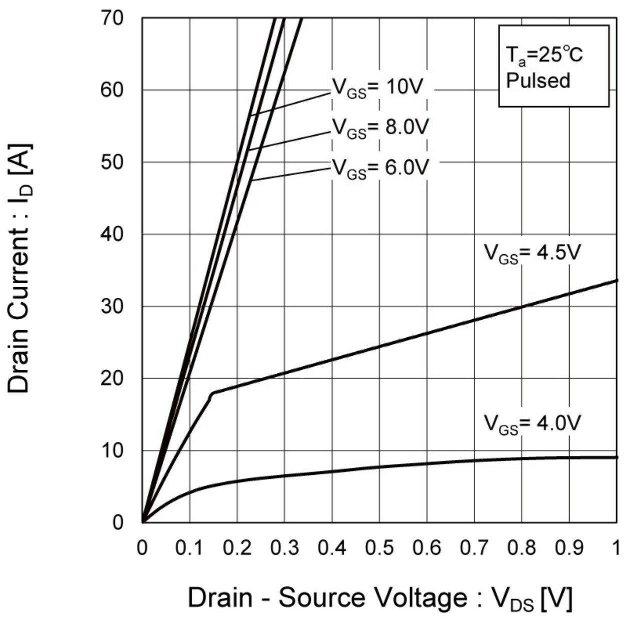


Fig.6 Typical Output Characteristics(II)

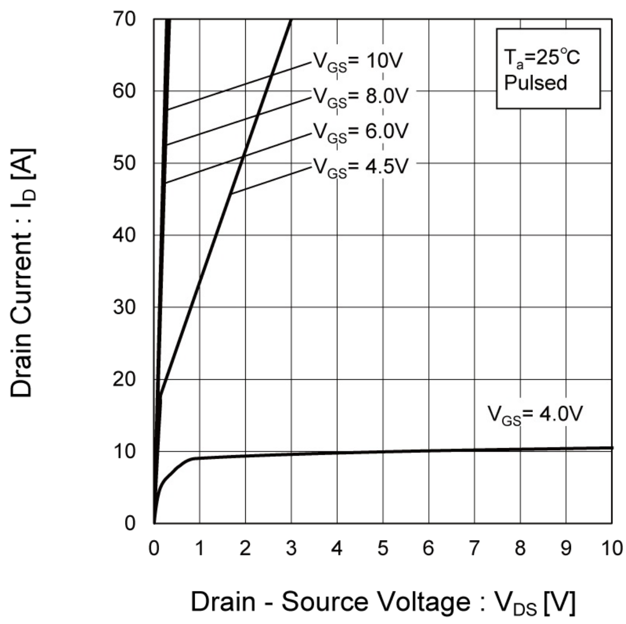


Fig.7 Normalized Breakdown Voltage vs. Junction Temperature

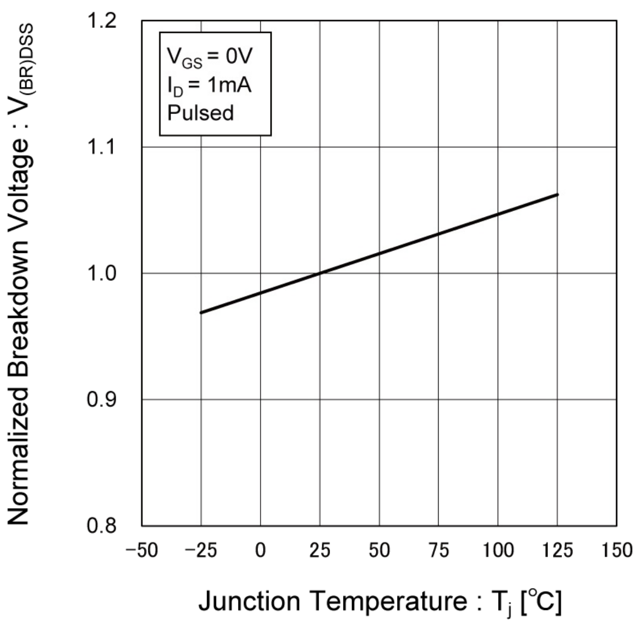
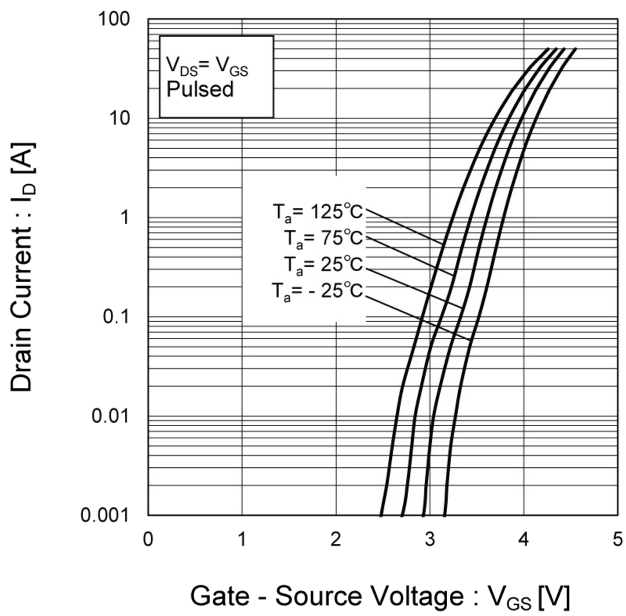


Fig.8 Typical Transfer Characteristics



●Electrical characteristic curves

Fig.9 Gate Threshold Voltage vs. Junction Temperature

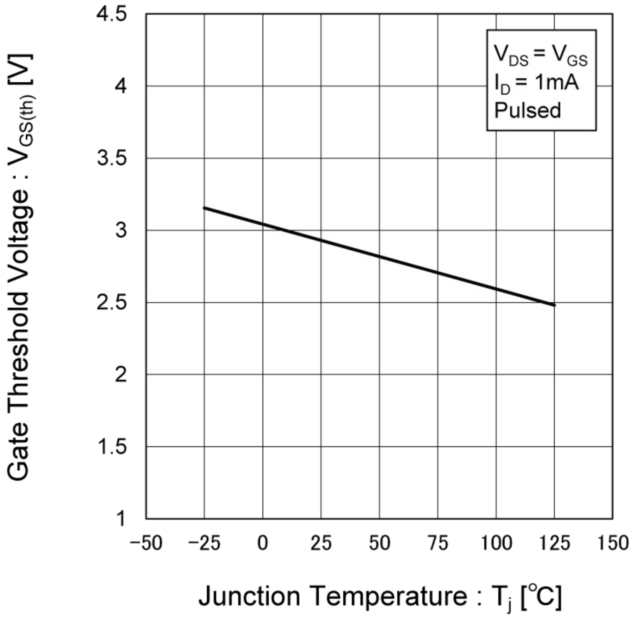


Fig.10 Forward Transfer Admittance vs. Drain Current

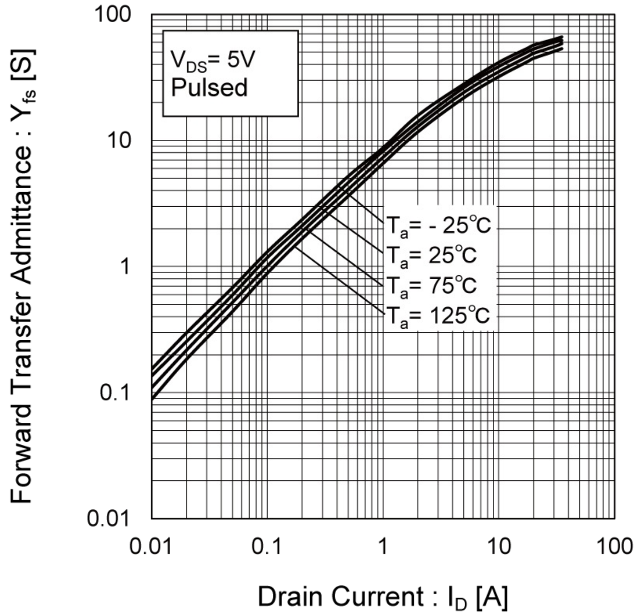


Fig.11 Drain Current Derating Curve

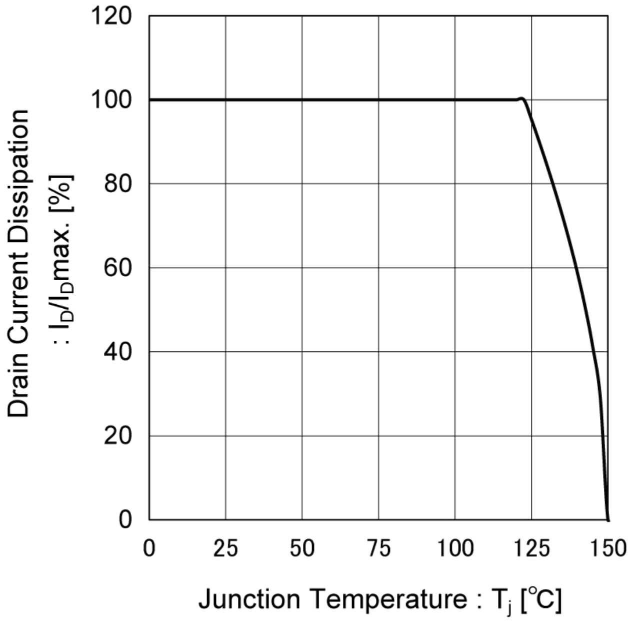
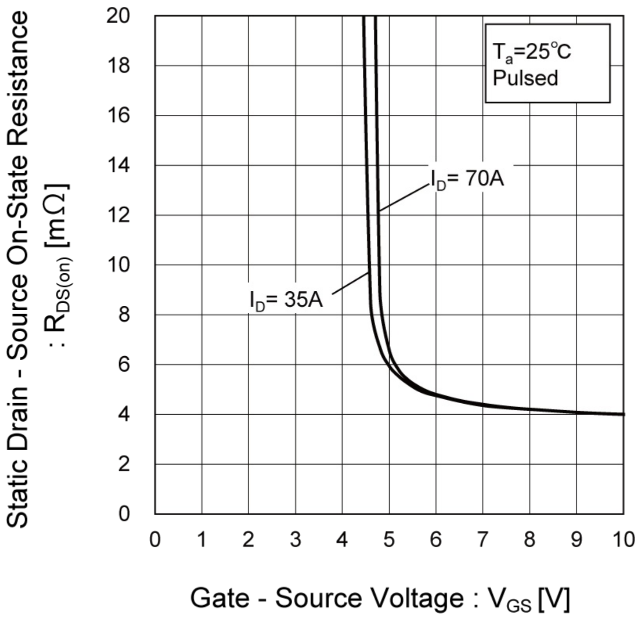


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



●Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

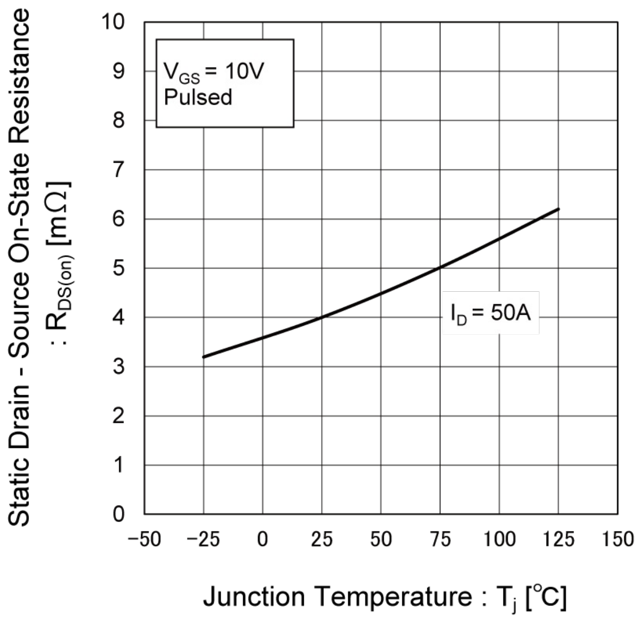


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

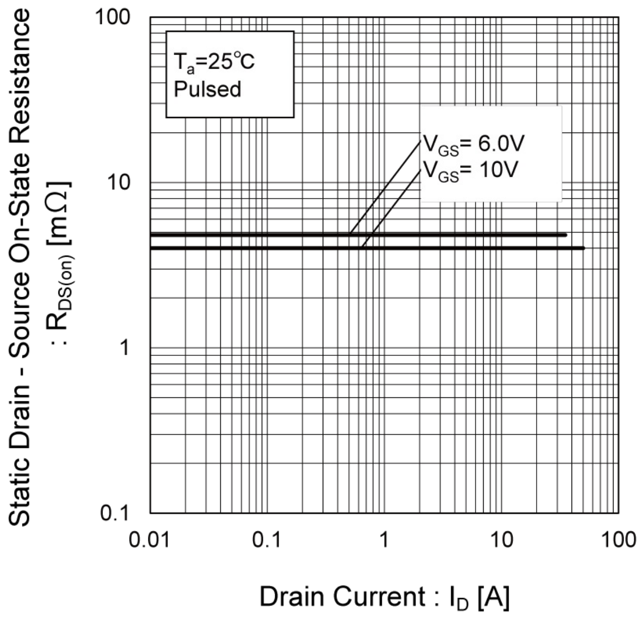


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

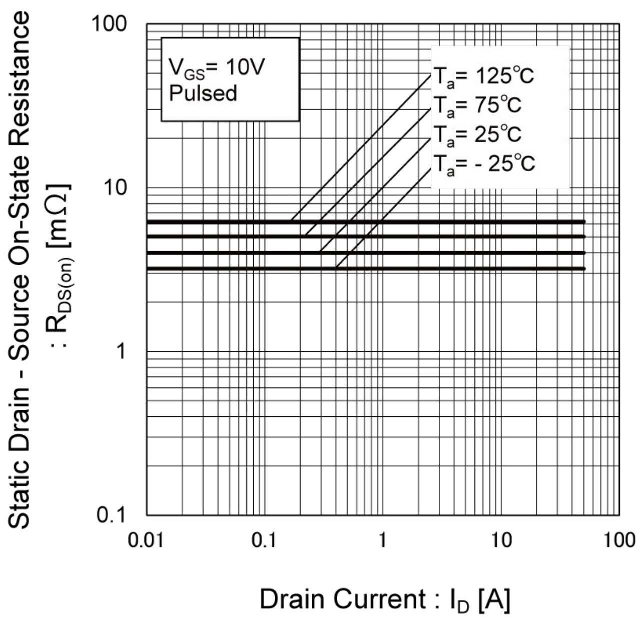
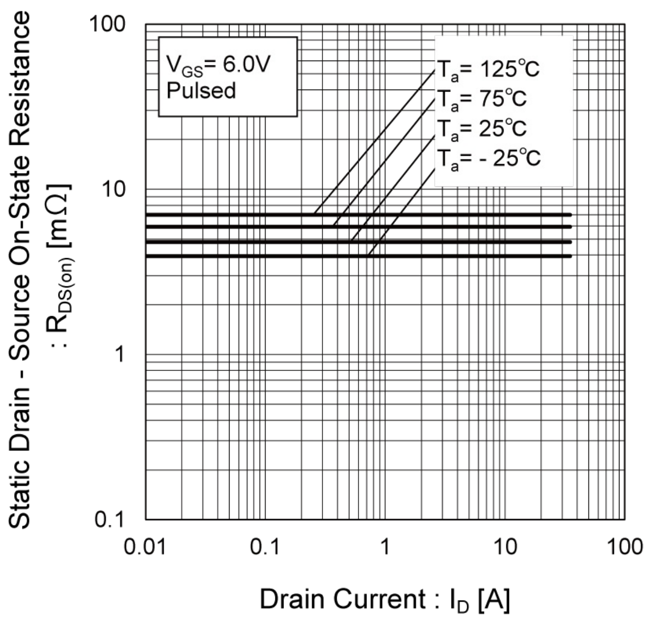


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



●Electrical characteristic curves

Fig.17 Typical Capacitances vs. Drain - Source Voltage

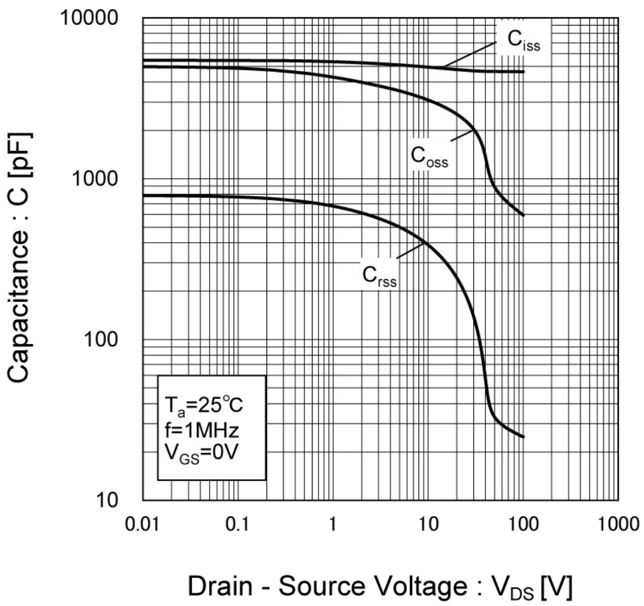


Fig.18 Switching Characteristics

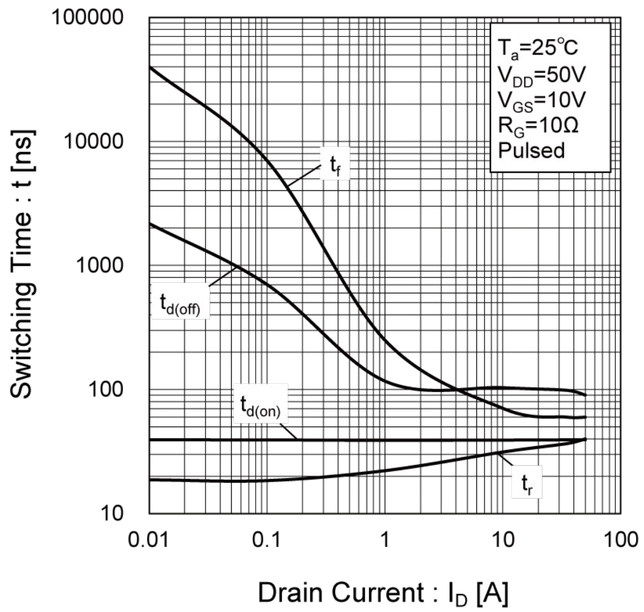


Fig.19 Typical Gate Charge

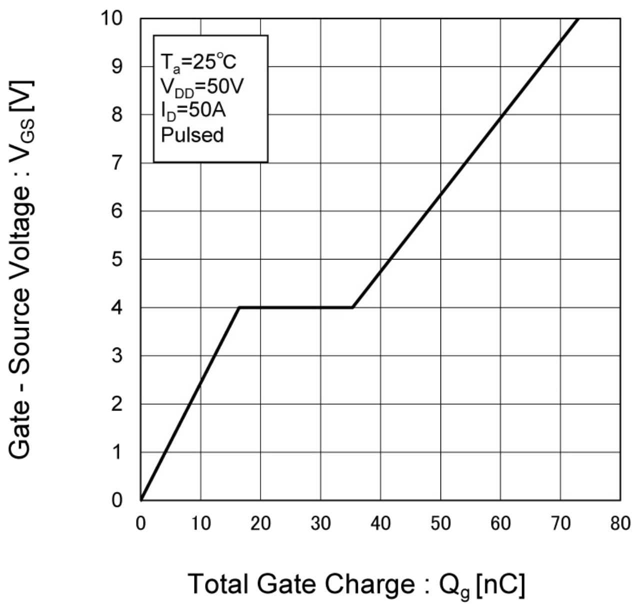
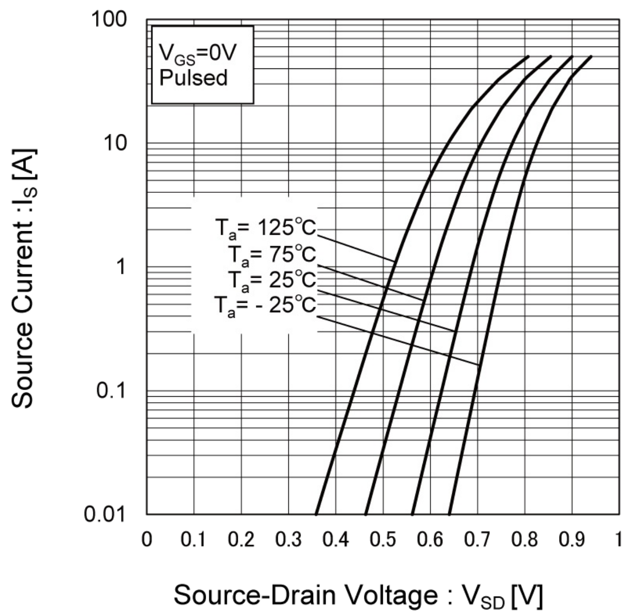


Fig.20 Source Current vs. Source Drain Voltage



●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

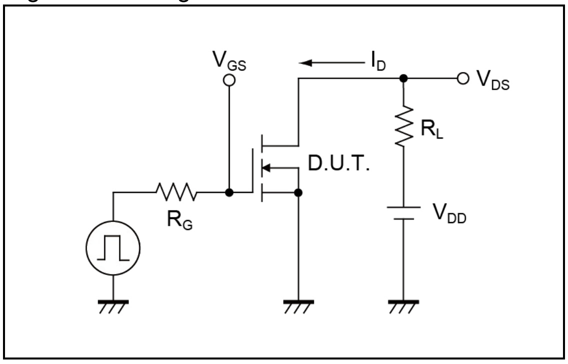


Fig.1-2 Switching Waveforms

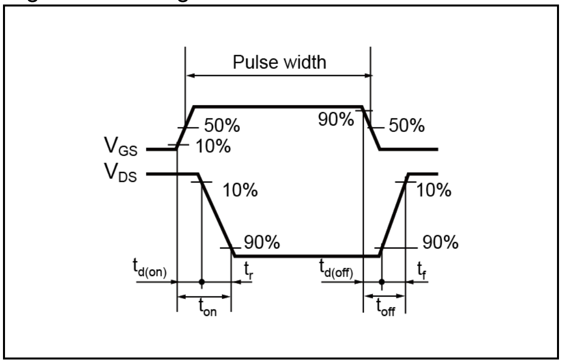


Fig.2-1 Gate Charge Measurement Circuit

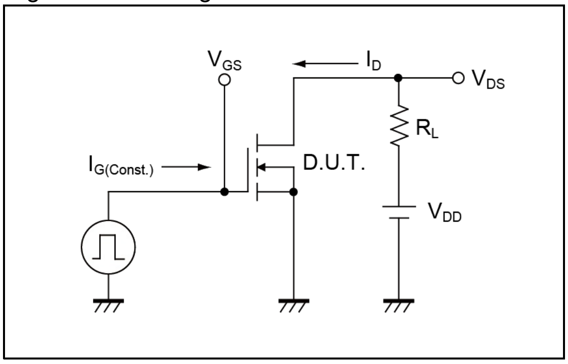


Fig.2-2 Gate Charge Waveform

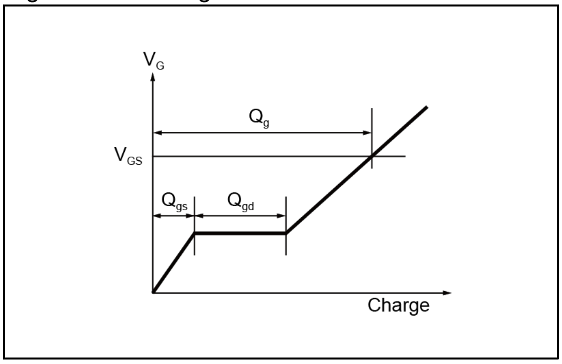


Fig.3-1 Avalanche Measurement Circuit

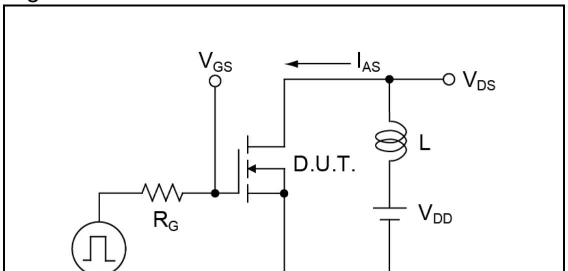
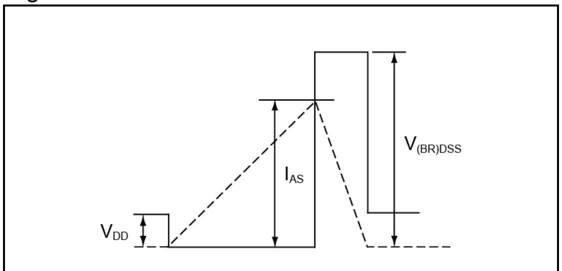
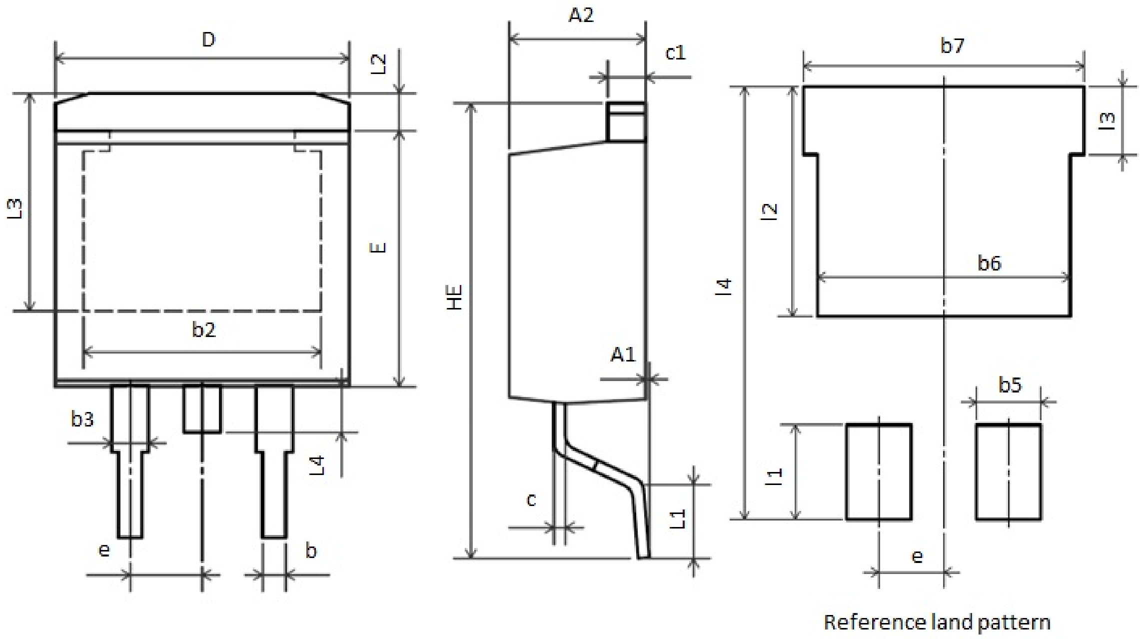


Fig.3-2 Avalanche Waveform



$$E_{AS} = \frac{1}{2} \cdot L \cdot I_{AS}^{2} \cdot \frac{V_{(BR)DSS}}{V_{(BR)DSS} - V_{DD}}$$

●Dimensions



DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A1	0.00	0.25	0.000	0.010
A2	4.37	4.77	0.168	0.188
b	0.70	0.96	0.028	0.038
b2	7.50	–	0.295	–
b3	1.17	1.47	0.046	0.058
c	0.30	0.53	0.012	0.021
c1	1.22	1.42	0.048	0.056
D	9.86	10.36	0.388	0.408
E	8.50	8.90	0.335	0.350
e	2.54		0.100	
HE	14.70	15.50	0.579	0.610
L1	2.00	2.60	0.079	0.102
L2	1.07	1.47	0.042	0.058
L3	6.60	–	0.260	–
L4	1.40	1.70	0.055	0.067

DIM	MILIMETERS	INCHES
	NOM	NOM
l1	3.5	0.14
l2	8.5	0.33
l3	2.5	0.10
l4	16.0	0.63
b5	2.5	0.10
b6	10.0	0.39
b7	11.0	0.43

Dimension in mm / inches

Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

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   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.) ; or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.

2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   - [a] the Products are exposed to sea winds or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   - [b] the temperature or humidity exceeds those recommended by ROHM
   - [c] the Products are exposed to direct sunshine or condensation
   - [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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