

V_{DSS}	100V
$R_{DS(on)}(Max.)$	6.3mΩ
$I_D(\text{Silicon limit})$	±170A
P_D	384W

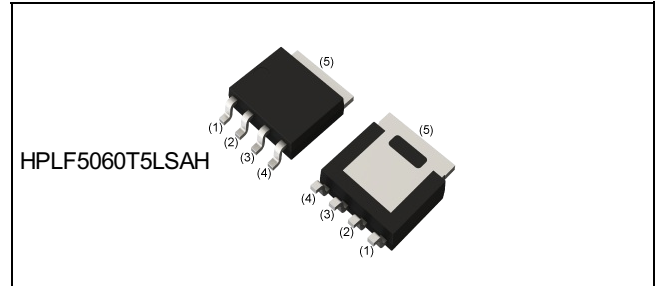
●Features

AEC-Q101 qualified
100% Avalanche tested
Wide-SOA

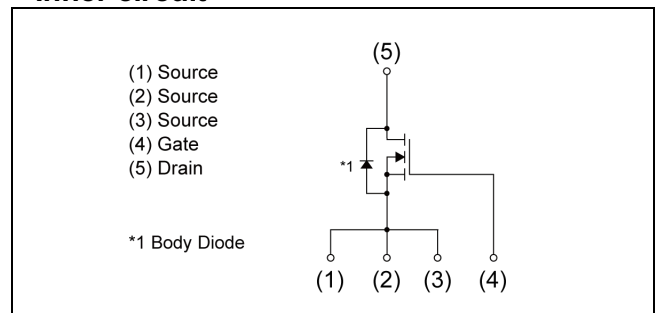
●Application

Air bag, Automotive Systems

●Outline



●Inner circuit



●Packaging specifications

Type	Packing	Embossed Tape
	Reel size (mm)	330.0
	Tape width (mm)	12.0
	Quantity (pcs)	3000
	Taping code	TCB
	Marking	RS4P063

●Absolute maximum ratings ($T_a = 25^\circ\text{C}$, unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source voltage		V_{DSS}	100	V
Continuous drain current	Silicon limit($V_{GS}=10V$)	I_D^{*1}	±170	A
	$T_c=25^\circ\text{C}(V_{GS}=10V)$	I_D^{*2}	±120	A
	$T_a=25^\circ\text{C}(V_{GS}=10V)$	I_D^{*5}	±34	A
Pulsed drain current		I_{DP}^{*3}	±320	A
Gate - Source voltage		V_{GSS}	±20	V
Avalanche current, single pulse		I_{AS}^{*4}	31	A
Avalanche energy, single pulse		E_{AS}^{*4}	78	mJ
Power dissipation		P_D^{*2}	384	W
Junction temperature		T_j	175	°C
Operating junction and storage temperature range		T_{stg}	-55 to +175	°C

● Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case	R_{thJC}	-	-	0.39	°C/W
Thermal resistance, junction - ambient	R_{thJA}^{*5}	-	19.9	-	°C/W

● Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 1mA$	100	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	$I_D = 1mA$ referenced to 25°C	-	58.4	-	mV/°C
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 100V, V_{GS} = 0V$	-	-	1	μA
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±500	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 8.1mA$	2.0	-	4.0	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	$I_D = 8.1mA$ referenced to 25°C	-	-5.6	-	mV/°C
Static drain - source on - state resistance	$R_{DS(on)}^{*6}$	$V_{GS} = 10V, I_D = 10A$	-	4.9	6.3	mΩ
Gate resistance	R_G	f=1MHz, open drain	-	1.9	-	Ω
Forward Transfer Admittance	$ Y_{fs} ^{*6}$	$V_{DS} = 5V, I_D = 10A$	7.1	-	-	S

*1 Limited by silicon chip capability.

*2 $T_c = 25^\circ\text{C}$, Limited only by maximum temperature allowed.

*3 $P_w \leq 10\mu\text{s}$

*4 $L \approx 0.1\text{mH}$, $V_{DD} = 50V$, $R_G = 25\Omega$, Starting $T_j = 25^\circ\text{C}$ Fig.3-1,3-2

*5 Device on 2s2p FR4 PCB defined in accordance with JEDEC standards (JESD51-5,-7).

This parameter is specified by design.

*6 Pulsed

●Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input capacitance	C_{iss}	$V_{GS} = 0V$	-	3590	-	pF
Output capacitance	C_{oss}	$V_{DS} = 50V$	-	600	-	
Reverse transfer capacitance	C_{rss}	$f = 1\text{MHz}$	-	18	-	
Turn - on delay time	$t_{d(on)}^{*6}$	$V_{DD} \approx 50V, V_{GS} = 10V$	-	32	-	ns
Rise time	t_r^{*6}	$I_D = 10A$	-	20	-	
Turn - off delay time	$t_{d(off)}^{*6}$	$R_L \approx 2\Omega$	-	53	-	
Fall time	t_f^{*6}	$R_G = 1\Omega$	-	14	-	

●Gate charge characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Total gate charge	Q_g^{*6}	$V_{DD} \approx 50V$	-	55	-	nC
Gate - Source charge	Q_{gs}^{*6}	$I_D = 10A$	-	18.2	-	
Gate - Drain charge	Q_{gd}^{*6}	$V_{GS} = 10V$	-	12.6	-	

●Body diode electrical characteristics (Source-Drain) ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Continuous forward current	I_S^{*2}	$T_a = 25^\circ\text{C}$	-	-	120	A
Pulse forward current	I_{SP}^{*3}		-	-	320	A
Forward voltage	V_{SD}^{*6}	$V_{GS} = 0V, I_S = 10A$	-	-	1.5	V
Reverse recovery time	t_{rr}^{*6}	$I_S = 10A, V_{GS} = 0V$	-	70	-	ns
Reverse recovery charge	Q_{rr}^{*6}	$di/dt = 100A/\mu s$	-	148	-	nC

●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

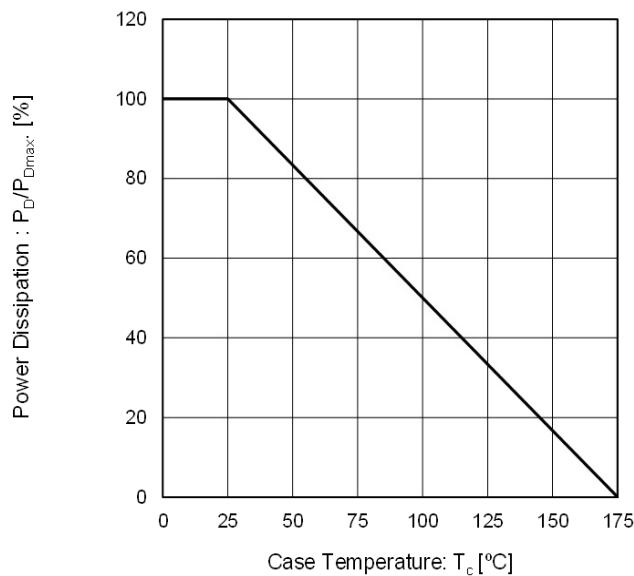


Fig.2 Maximum Safe Operating Area

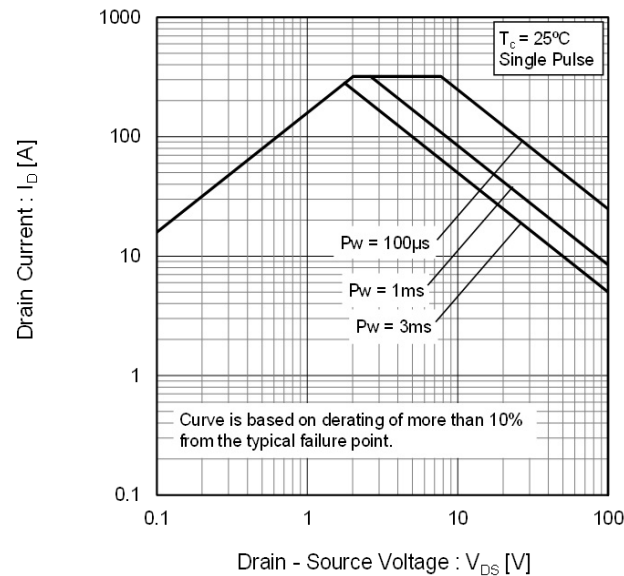


Fig.3 Transient Thermal Resistance vs. Pulse Width

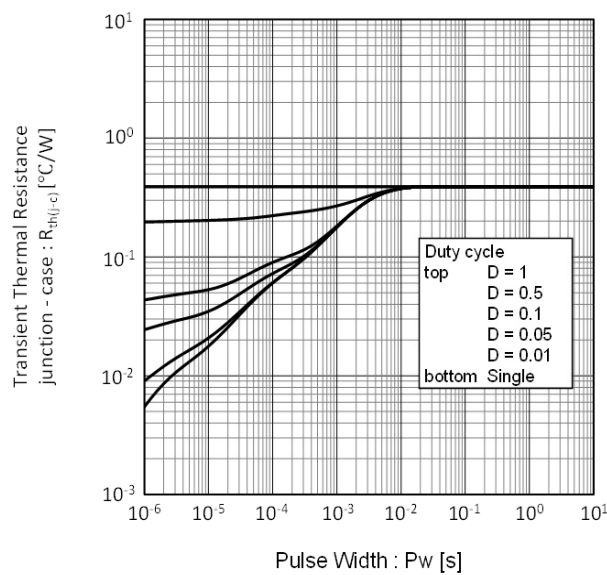
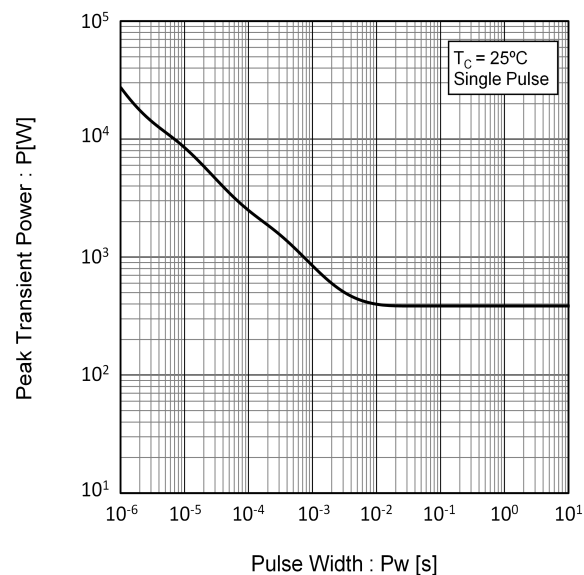


Fig.4 Single Pulse Maximum Power dissipation



●Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

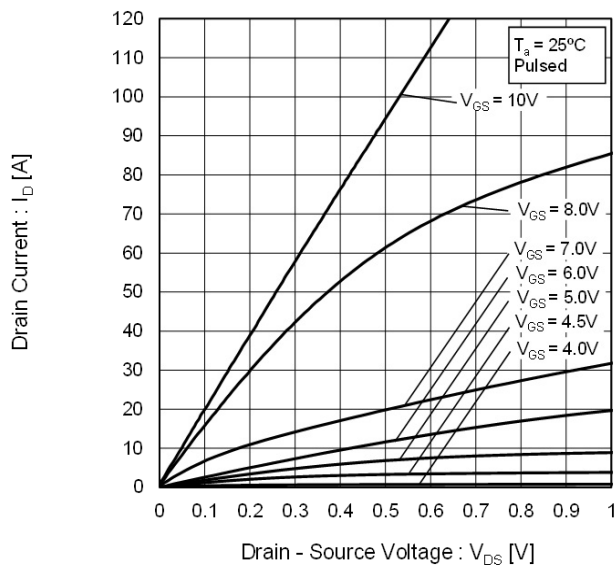


Fig.6 Typical Output Characteristics(II)

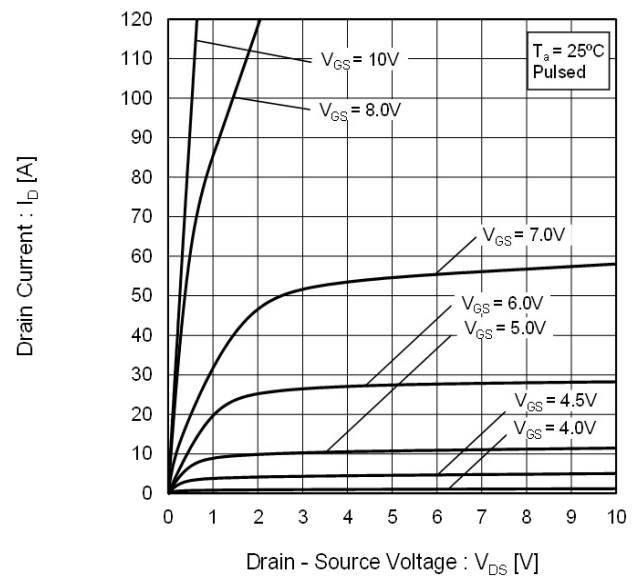


Fig.7 Normalized Breakdown Voltage vs. Junction Temperature

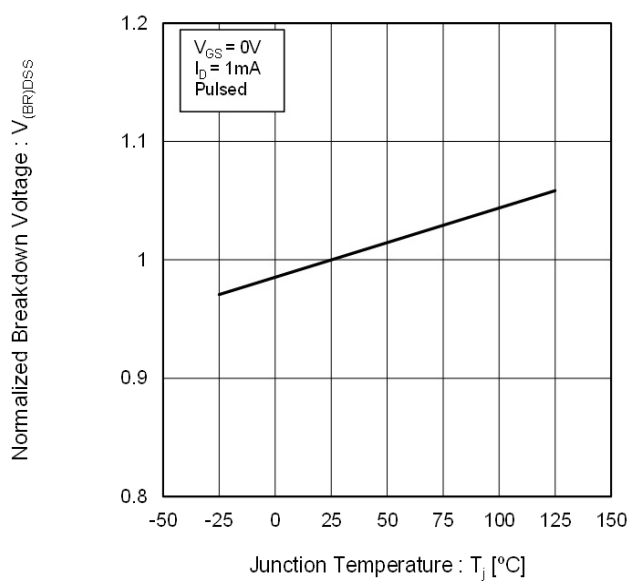
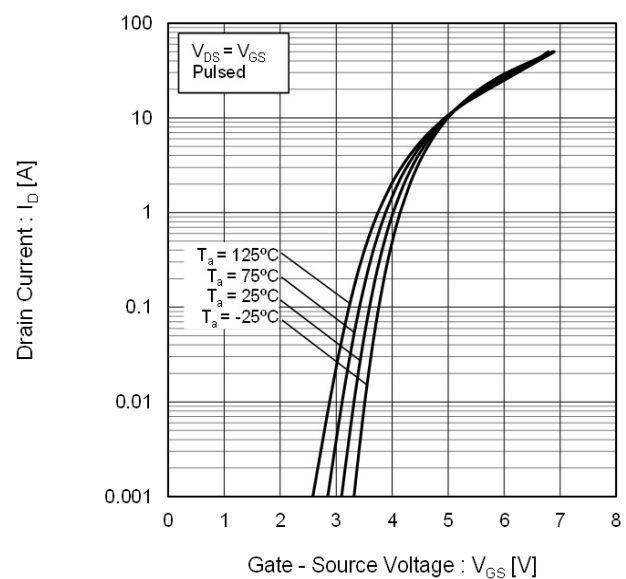


Fig.8 Typical Transfer Characteristics



●Electrical characteristic curves

Fig.9 Forward Transfer Admittance vs. Drain Current

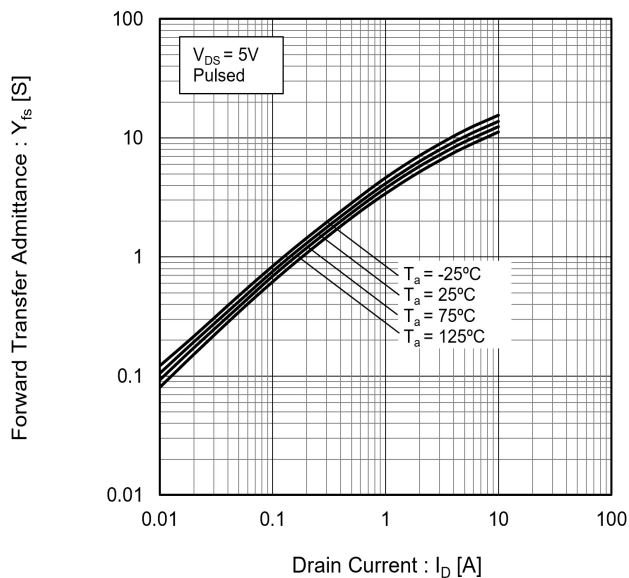


Fig.10 Drain Current Derating Curve

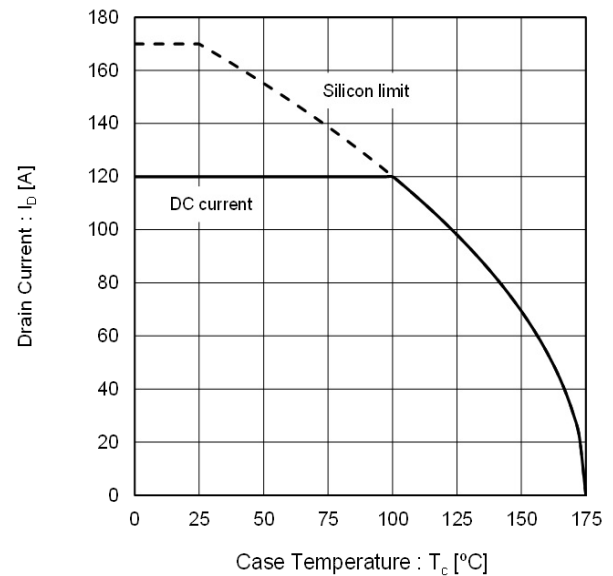


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

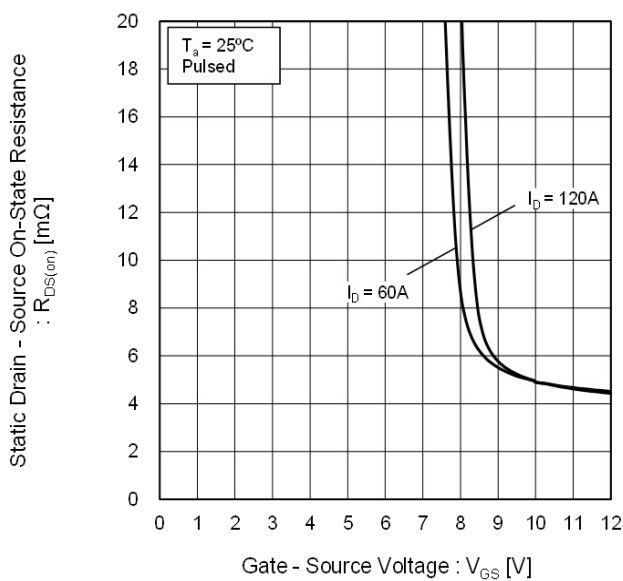
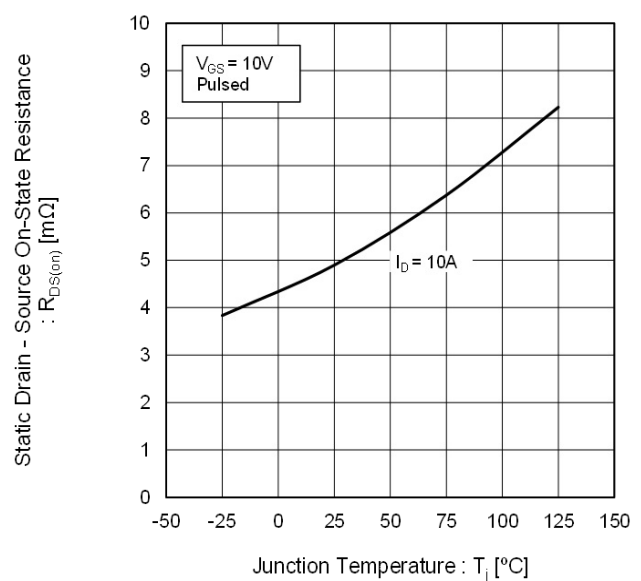


Fig.12 Static Drain - Source On - State Resistance vs. Junction Temperature



●Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

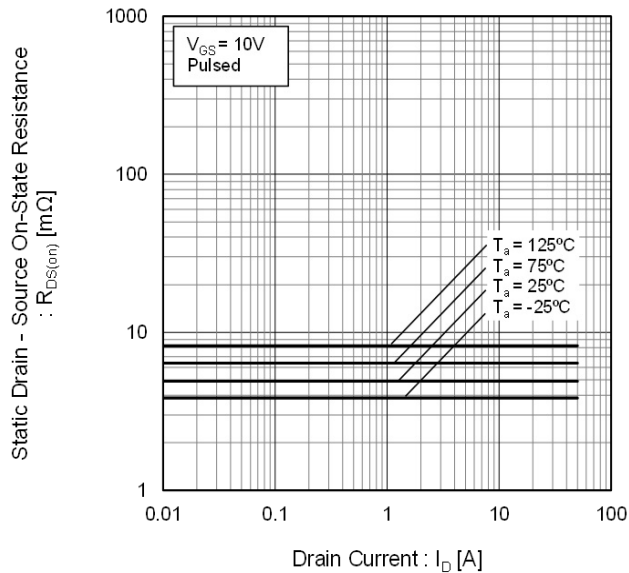


Fig.14 Typical Capacitance vs. Drain - Source Voltage

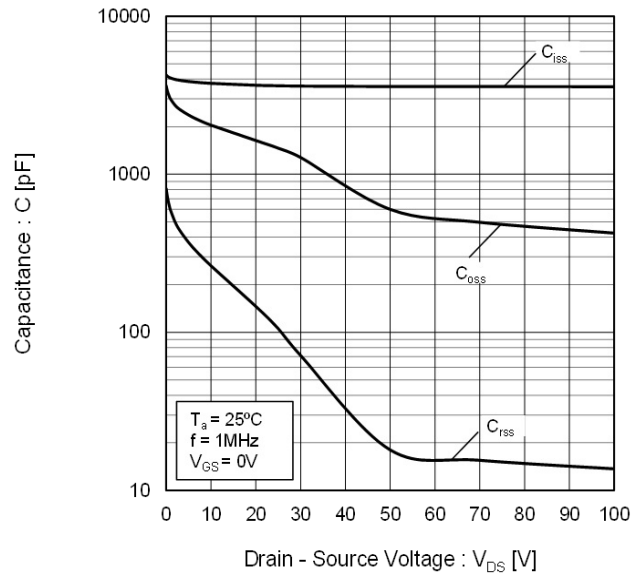


Fig.15 Switching Characteristics

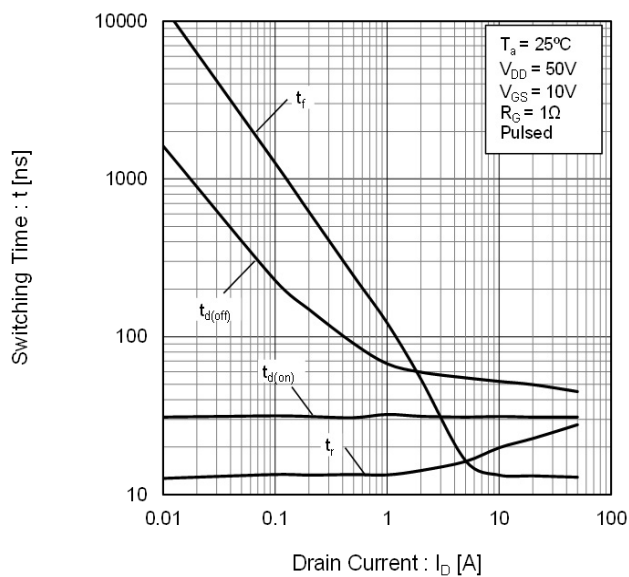
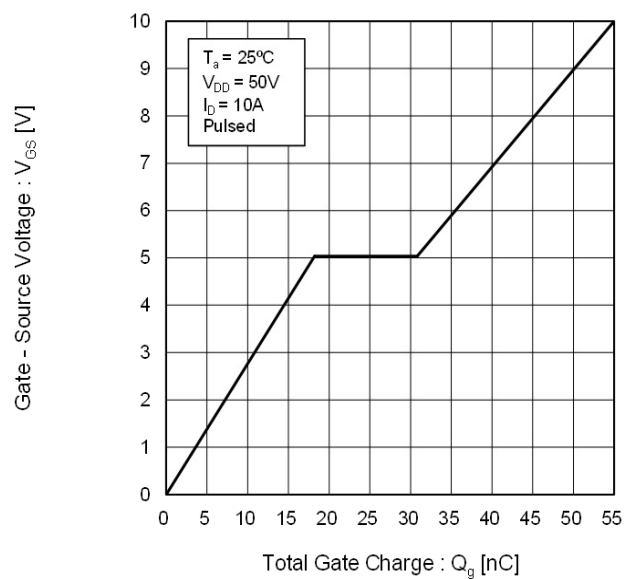
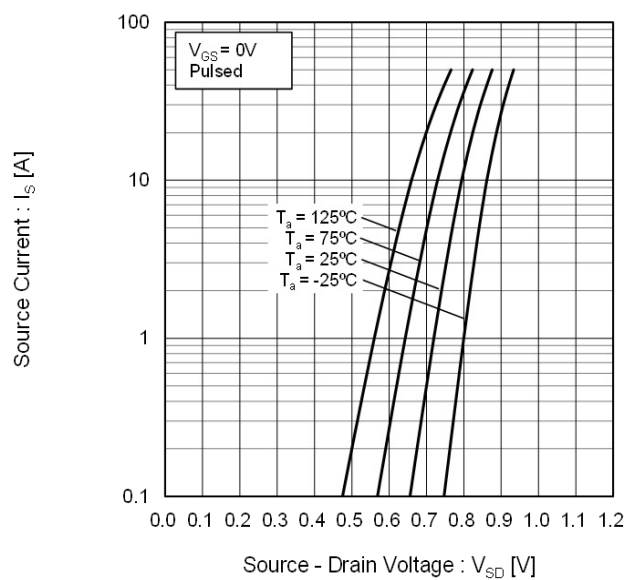


Fig.16 Dynamic Input Characteristics



●Electrical characteristic curves

Fig.17 Source Current vs.
Source Drain Voltage

● Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

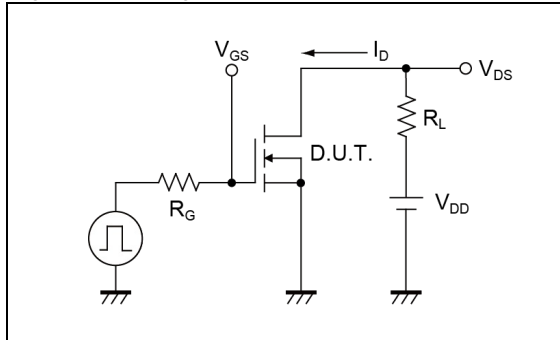


Fig.1-2 Switching Waveforms

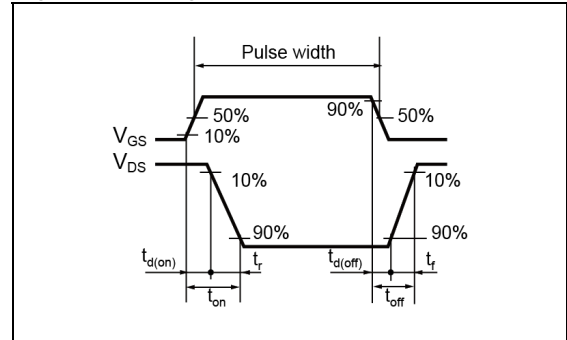


Fig.2-1 Gate Charge Measurement Circuit

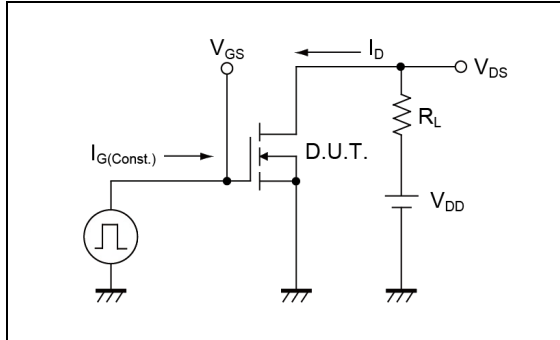


Fig.2-2 Gate Charge Waveform

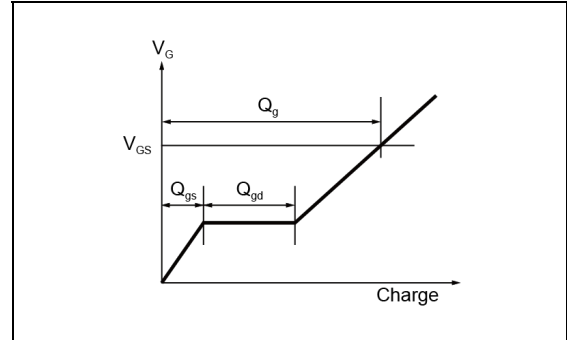


Fig.3-1 Avalanche Measurement Circuit

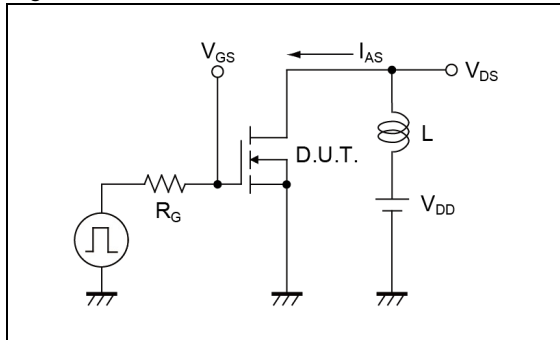
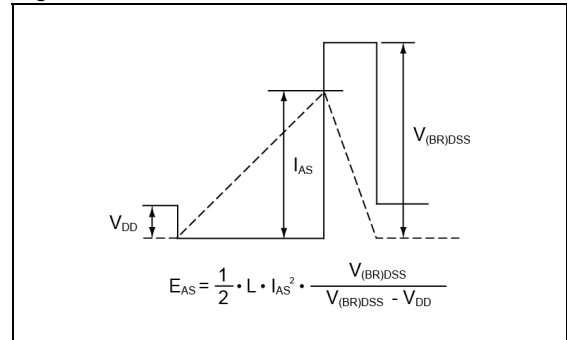
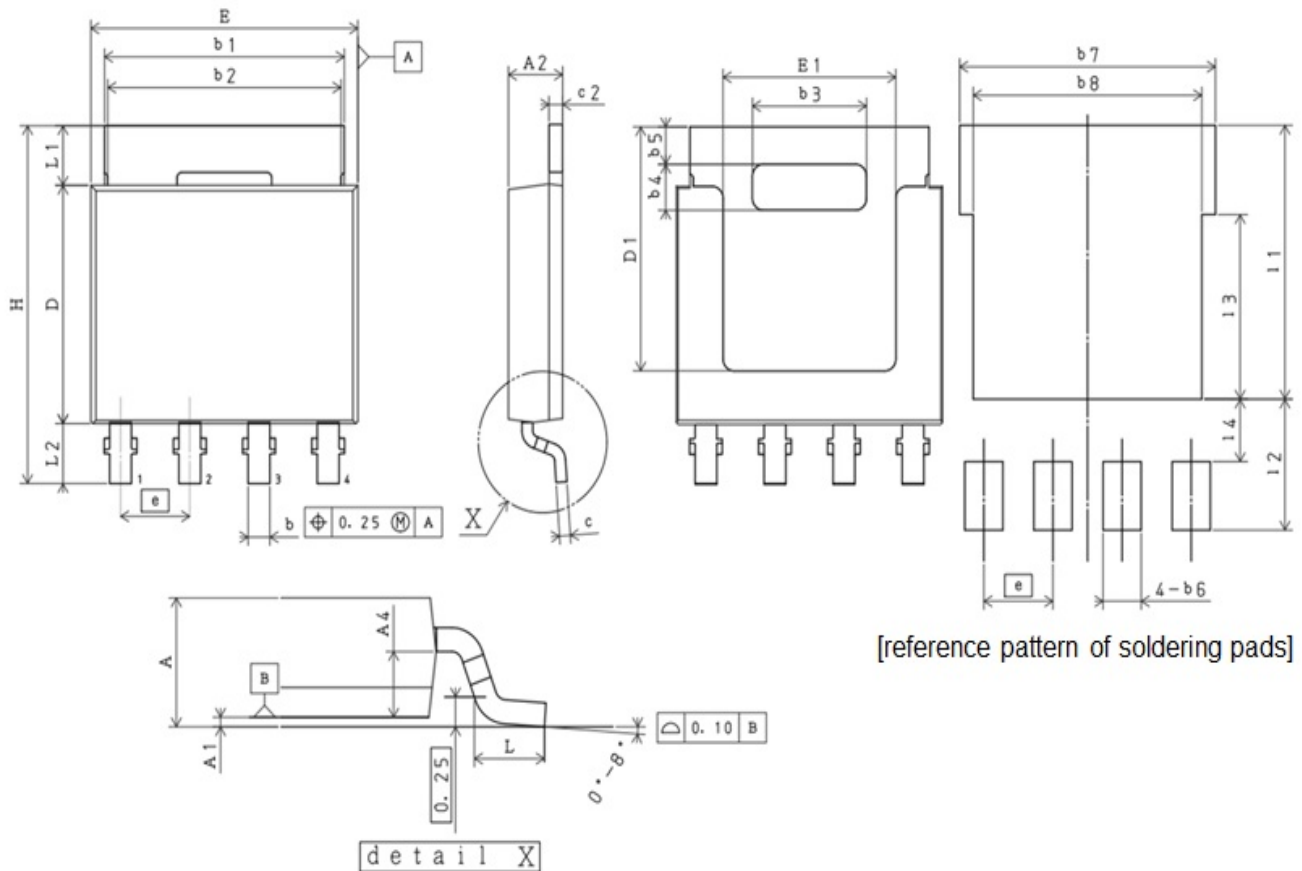


Fig.3-2 Avalanche Waveform



●Dimensions

HPLF5060T5LSAH



[reference pattern of soldering pads]

DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	1.01	1.20	0.040	0.047
A1	0.00	0.15	0.000	0.006
A2	0.95	1.10	0.037	0.043
A4	0.50	0.65	0.020	0.026
b	0.35	0.50	0.014	0.020
b1	4.15	4.60	0.163	0.181
b2	3.62	4.41	0.143	0.174
b3	2.00	2.20	0.079	0.087
b4	0.70	0.90	0.028	0.035
b5	-	0.70	-	0.028
c	0.19	0.25	0.007	0.010
c2	0.24	0.30	0.009	0.012
D	3.80	4.10	0.150	0.161
D1	-	4.20	-	0.165
E	4.80	5.00	0.189	0.197
E1	3.10	3.30	0.122	0.130
e	1.27		0.050	
H	5.80	6.20	0.228	0.244
L	0.40	0.85	0.016	0.033
L1	0.80	1.30	0.031	0.051
L2	0.80	1.30	0.031	0.051

DIM	Millimeters	Inches
	Nom.	Nom.
I1	4.60	0.181
I2	2.20	0.087
I3	3.10	0.122
I4	1.05	0.041
b6	0.70	0.028
b7	4.70	0.185
b8	4.20	0.165

Dimension in mm / inches

Notice

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
3. Our Products are not designed under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
4. The Products are not subject to radiation-proof design.
5. Please verify and confirm characteristics of the final or mounted products in using the Products.
6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
8. Confirm that operation temperature is within the specified range described in the product specification.
9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of ionizer, friction prevention and temperature / humidity control).

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1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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