

$V_{DSS}$	30V
$R_{DS(on)(Max.)}$	0.67m $\Omega$
$I_D$	$\pm 390A$
$P_D$	180W

### ●Features

- 1) Low on - resistance
- 2) High power package (DFN5060T8LSHAAE)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen free
- 5) 100% Rg and UIS tested

### ●Application

- Switching
- Motor drives
- DC/DC converter

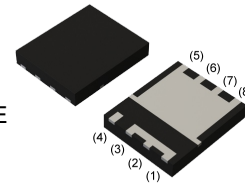
### ●Absolute maximum ratings ( $T_a = 25^\circ C$ ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	$V_{DSS}$	30	V	
Continuous drain current	Silicon limit ( $V_{GS}=10V$ )	$I_D^{*1}$	$\pm 390$	A
	$T_c = 25^\circ C$ ( $V_{GS}=10V$ )	$I_D^{*2}$	$\pm 200$	A
Pulsed drain current	$I_{DP}^{*3}$	$\pm 900$	A	
Gate - Source voltage	$V_{GSS}$	$\pm 20$	V	
Avalanche current, single pulse	$I_{AS}^{*4}$	30	A	
Avalanche energy, single pulse	$E_{AS}^{*4}$	637	mJ	
Power dissipation	$P_D^{*2}$	180	W	
	$P_D^{*5}$	3.0	W	
Junction temperature	$T_j$	150	$^\circ C$	
Operating junction and storage temperature range	$T_{stg}$	-55 to +150	$^\circ C$	

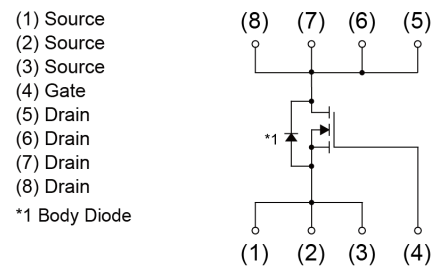
### ●Outline

DFN5060-8S

DFN5060T8LSHAAE



### ●Inner circuit



### ●Packaging specifications

Type	Packing	Embossed Tape
	Reel size (mm)	300
	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	TB1
	Marking	RS7E200BG

### ● Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case	$R_{thJC}^{*2}$	-	-	0.69	°C/W
Thermal resistance, junction - ambient	$R_{thJA}^{*5}$	-	-	41.7	°C/W

### ● Electrical characteristics ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 1mA$	30	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	$I_D = 1mA$ referenced to $25^\circ\text{C}$	-	25.8	-	mV/°C
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 30V, V_{GS} = 0V$	-	-	5	$\mu\text{A}$
Gate - Source leakage current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	$\pm 500$	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 1mA$	1.0	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	$I_D = 1mA$ referenced to $25^\circ\text{C}$	-	-4.5	-	mV/°C
Static drain - source on - state resistance	$R_{DS(on)}^{*6}$	$V_{GS} = 10V, I_D = 50A$	-	0.53	0.67	m $\Omega$
		$V_{GS} = 4.5V, I_D = 50A$	-	0.75	1.06	
Gate resistance	$R_G$	-	-	0.6	-	$\Omega$
Forward Transfer Admittance	$ Y_{fs} ^{*6}$	$V_{DS} = 5V, I_D = 50A$	70	-	-	S

\*1 Limited by silicon chip capability.

\*2  $T_c = 25^\circ\text{C}$ , Limited only by maximum temperature allowed.

\*3  $P_w \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$

\*4  $L \approx 1\text{mH}$ ,  $V_{DD} = 15V$ ,  $R_G = 25\Omega$ , Starting  $T_j = 25^\circ\text{C}$  Fig.3-1,3-2

\*5 Mounted on a Cu board (40×40×0.8mm)

\*6 Pulsed

**●Electrical characteristics (T<sub>a</sub> = 25°C)**

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	9500	-	pF
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15V	-	4390	-	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	550	-	
Turn - on delay time	t <sub>d(on)</sub> <sup>*6</sup>	V <sub>DD</sub> ≈ 15V, V <sub>GS</sub> = 10V	-	49	-	ns
Rise time	t <sub>r</sub> <sup>*6</sup>	I <sub>D</sub> = 50A	-	40	-	
Turn - off delay time	t <sub>d(off)</sub> <sup>*6</sup>	R <sub>L</sub> ≈ 0.3Ω	-	220	-	
Fall time	t <sub>f</sub> <sup>*6</sup>	R <sub>G</sub> = 10Ω	-	155	-	

**●Gate charge characteristics (T<sub>a</sub> = 25°C)**

Parameter	Symbol	Conditions	Values			Unit	
			Min.	Typ.	Max.		
Total gate charge	Q <sub>g</sub> <sup>*6</sup>	V <sub>DD</sub> ≈ 15V I <sub>D</sub> = 50A	V <sub>GS</sub> = 10V	-	135.0	-	nC
Gate - Source charge	Q <sub>gs</sub> <sup>*6</sup>		V <sub>GS</sub> = 4.5V	-	60.0	-	
Gate - Drain charge	Q <sub>gd</sub> <sup>*6</sup>			-	25.0	-	
				-	14.2	-	

**●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)**

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Continuous forward current	I <sub>S</sub> <sup>*2</sup>	-	-	-	150	A
Pulse forward current	I <sub>SP</sub> <sup>*3</sup>		-	-	900	A
Forward voltage	V <sub>SD</sub> <sup>*6</sup>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 50A	-	-	1.2	V
Reverse recovery time	t <sub>rr</sub> <sup>*6</sup>	I <sub>S</sub> = 50A, V <sub>GS</sub> = 0V	-	86	-	ns
Reverse recovery charge	Q <sub>rr</sub> <sup>*6</sup>	di/dt = 100A/μs	-	50	-	nC

●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

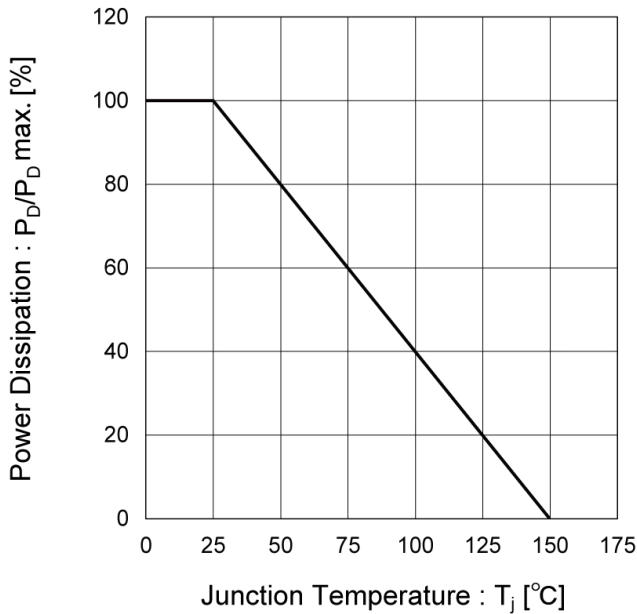


Fig.2 Maximum Safe Operating Area

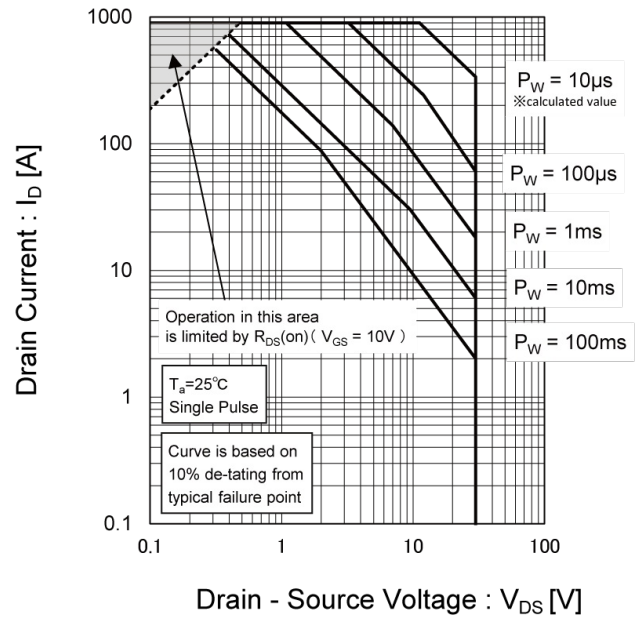


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

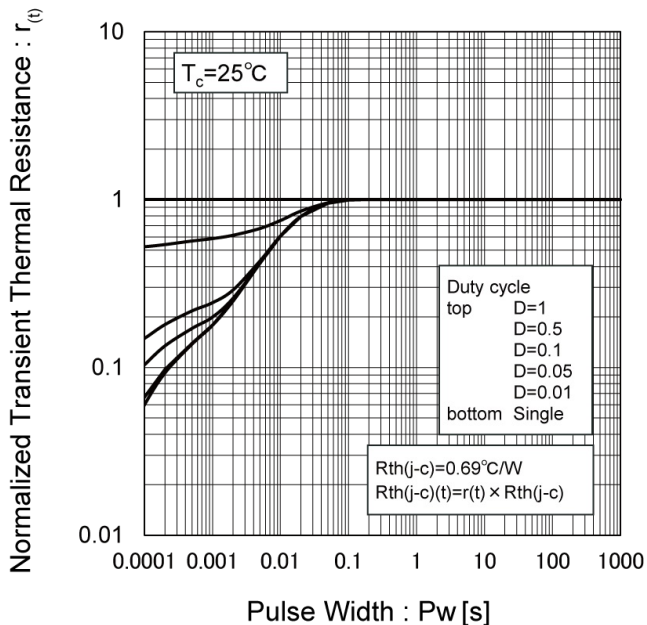
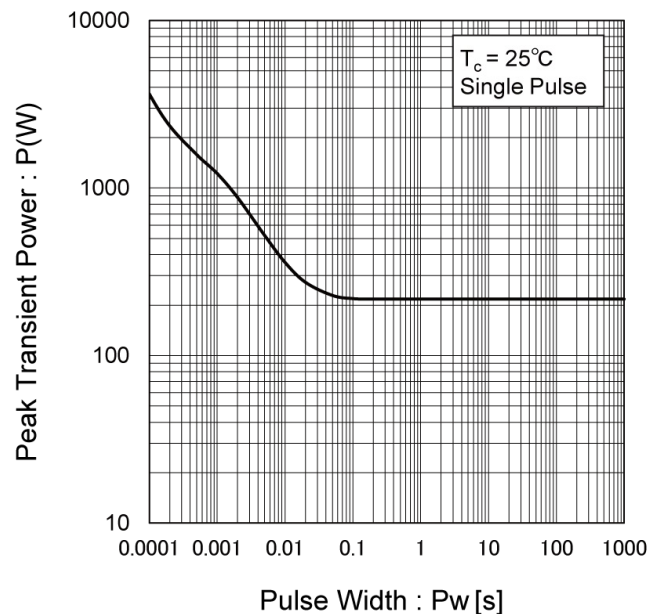


Fig.4 Single Pulse Maximum Power Dissipation



● Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

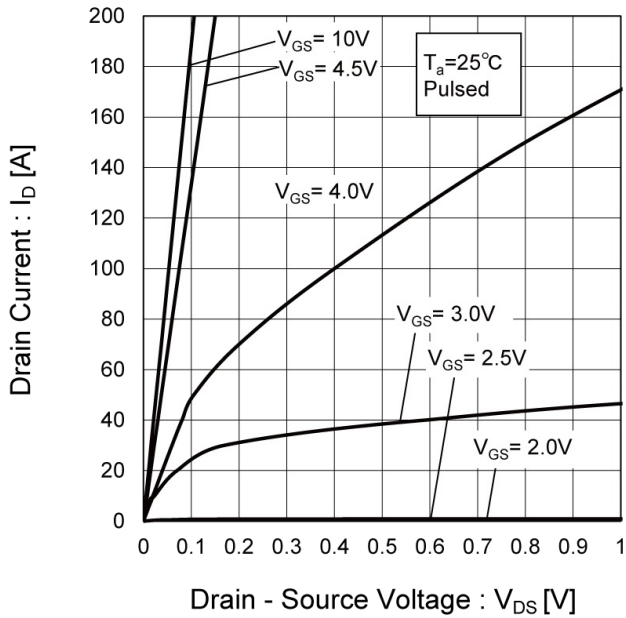


Fig.6 Typical Output Characteristics(II)

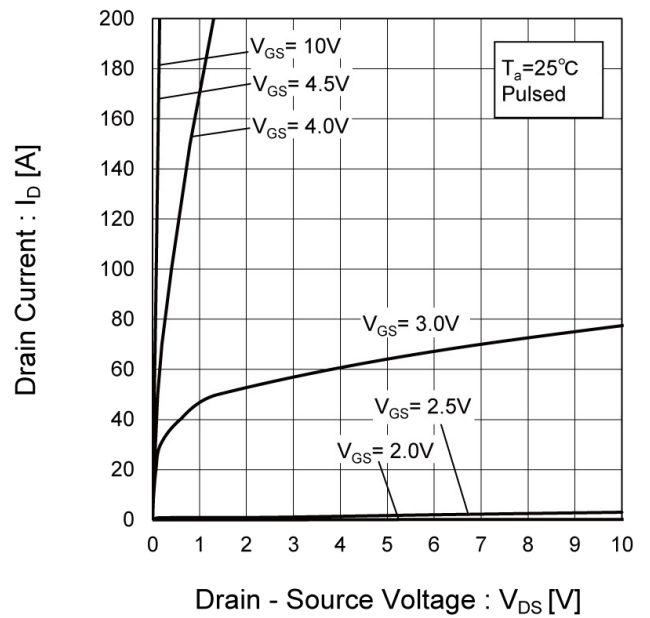


Fig.7 Breakdown Voltage vs. Junction Temperature

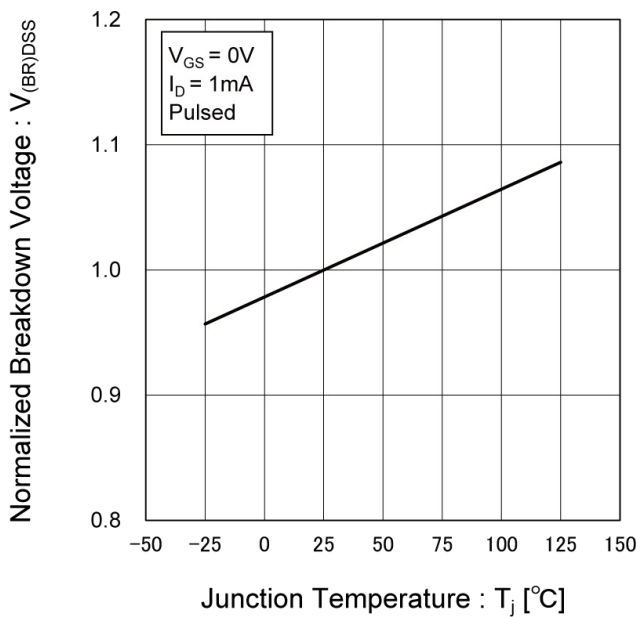
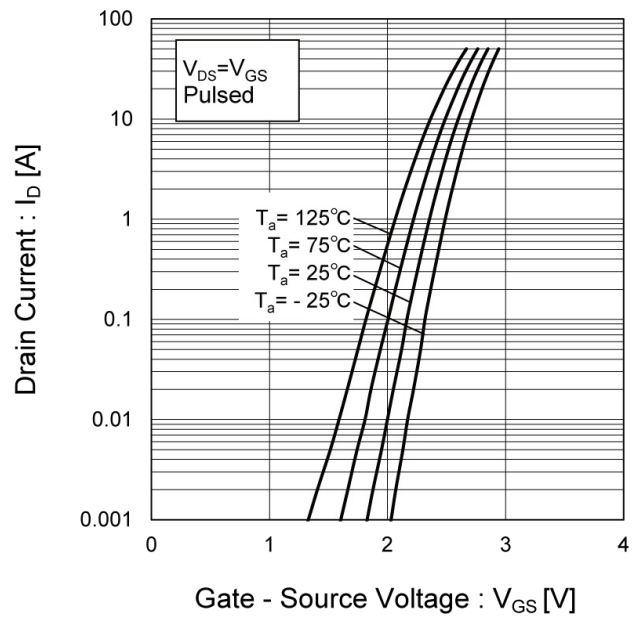


Fig.8 Typical Transfer Characteristics



●Electrical characteristic curves

Fig.9 Gate Threshold Voltage vs. Junction Temperature

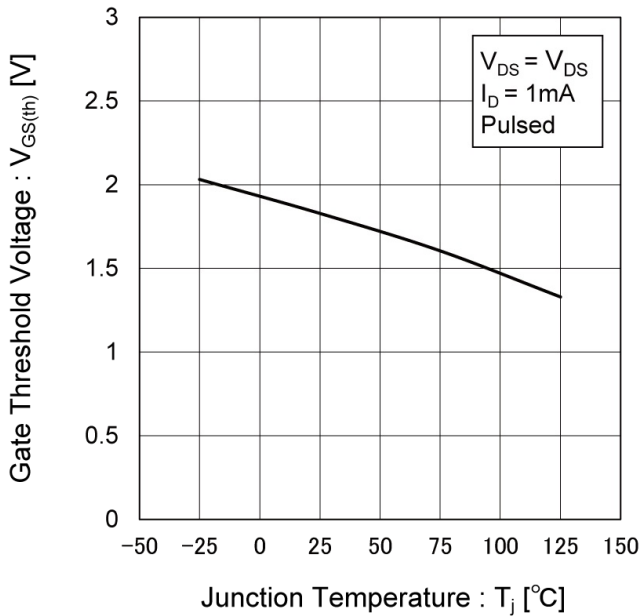


Fig.10 Forward Transfer Admittance vs. Drain Current

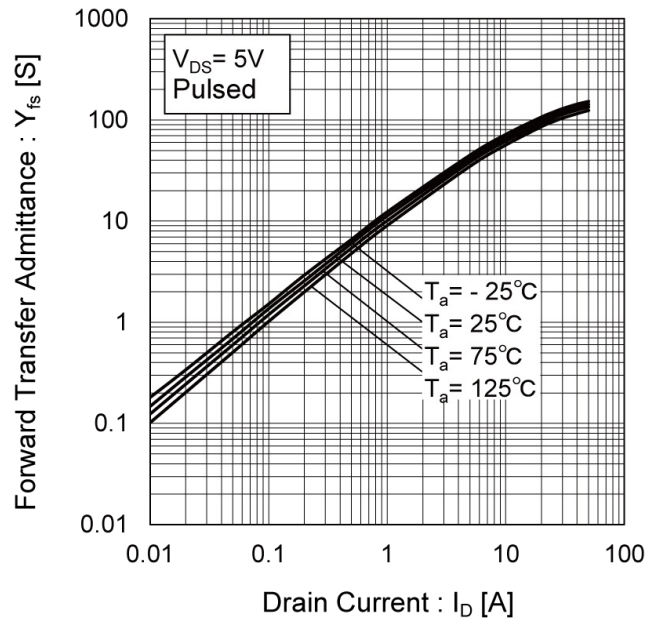


Fig.11 Drain Current Derating Curve

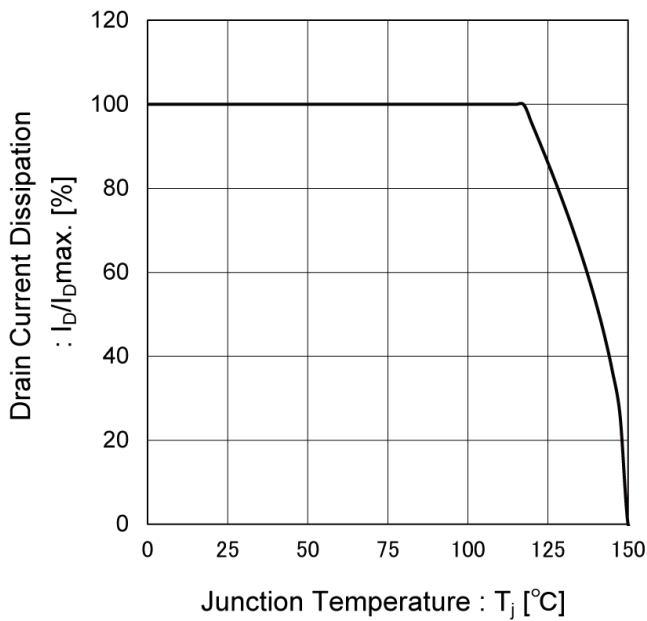
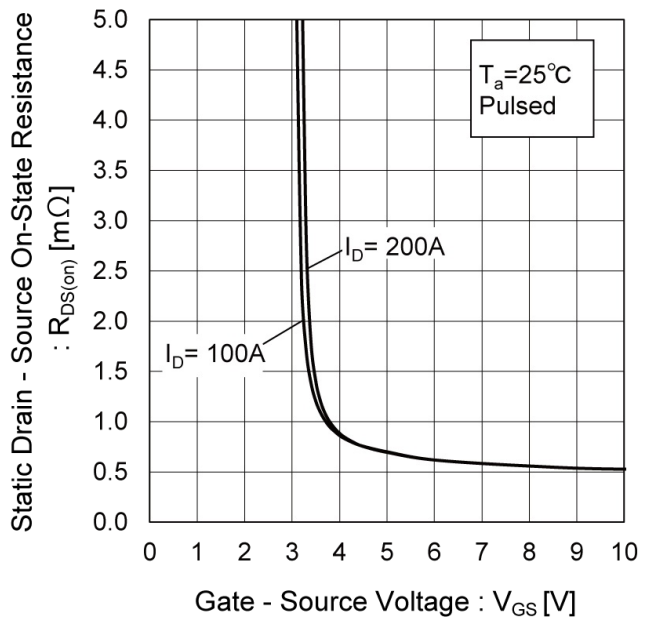


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



● Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

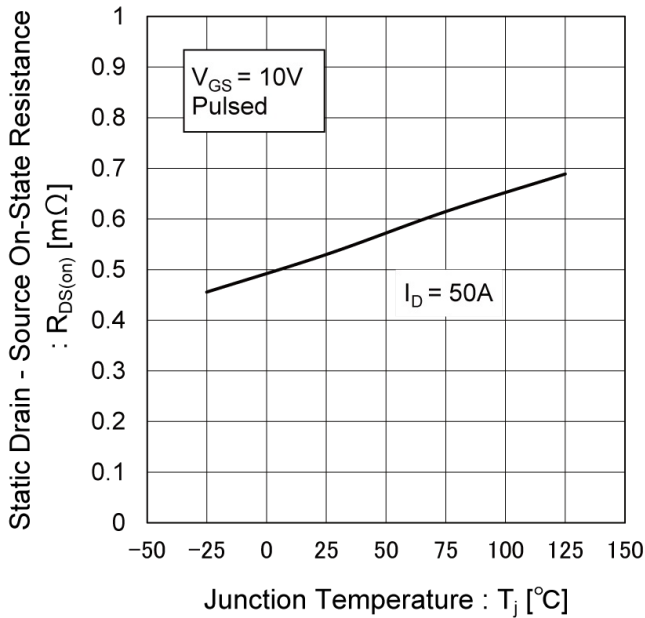


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

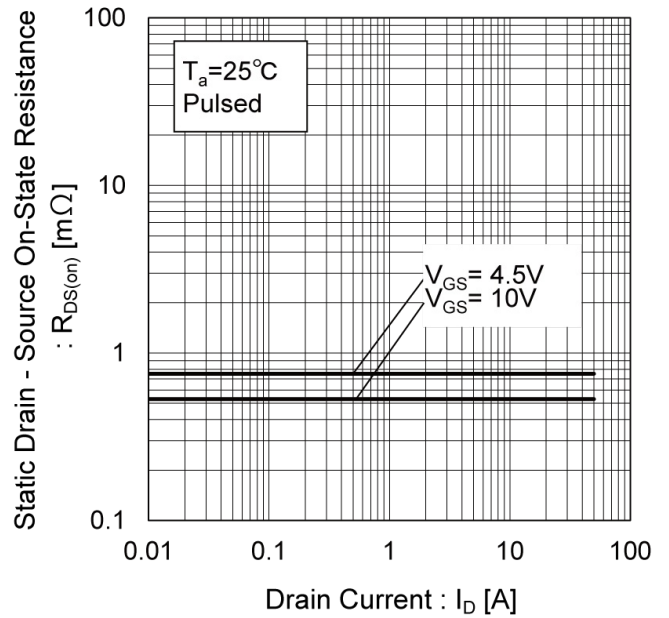


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

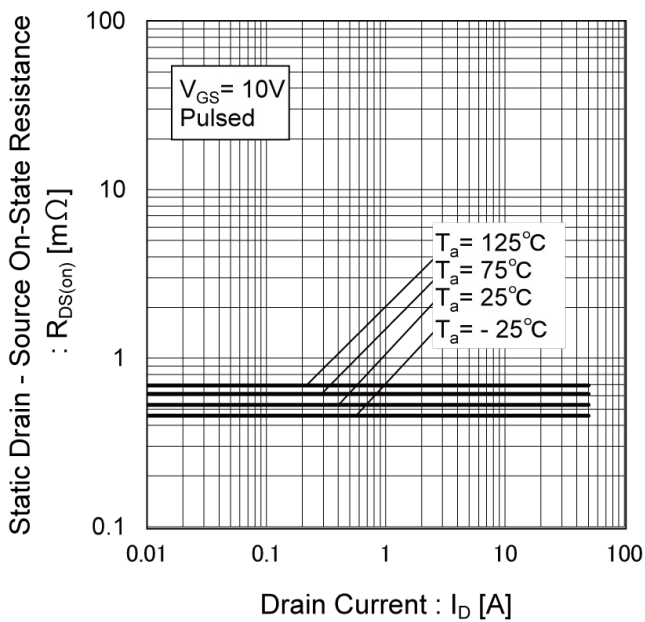
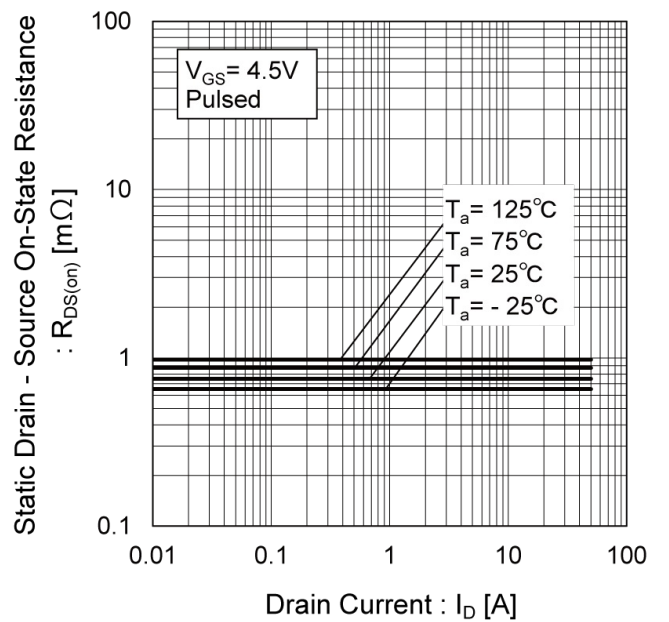


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



●Electrical characteristic curves

Fig.17 Typical Capacitances vs. Drain - Source Voltage

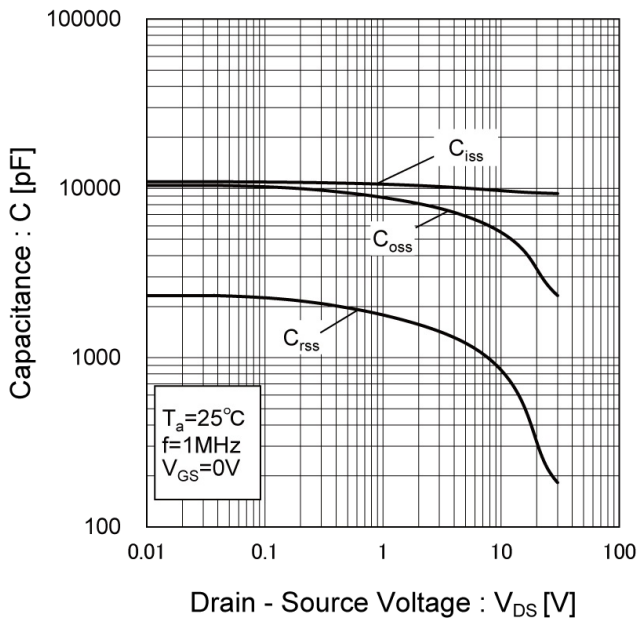


Fig.18 Switching Characteristics

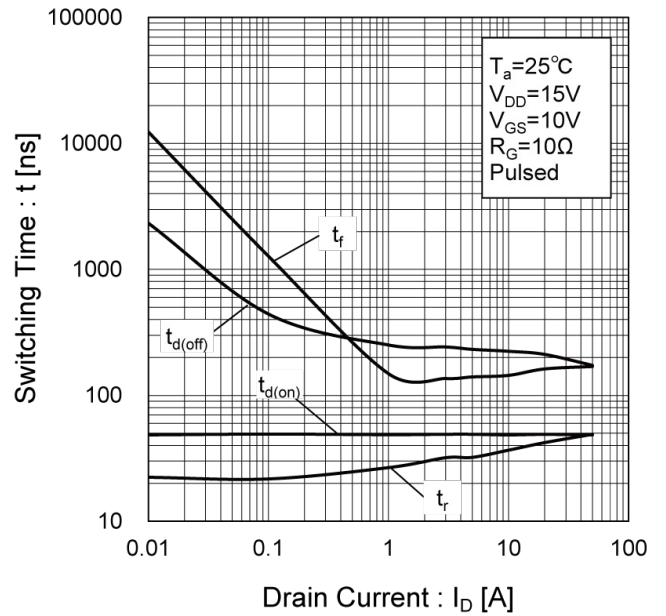


Fig.19 Typical Gate Charge

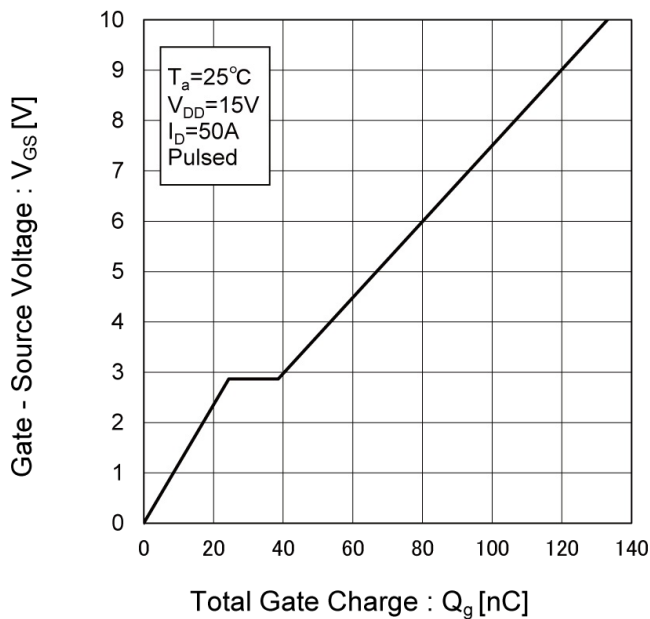
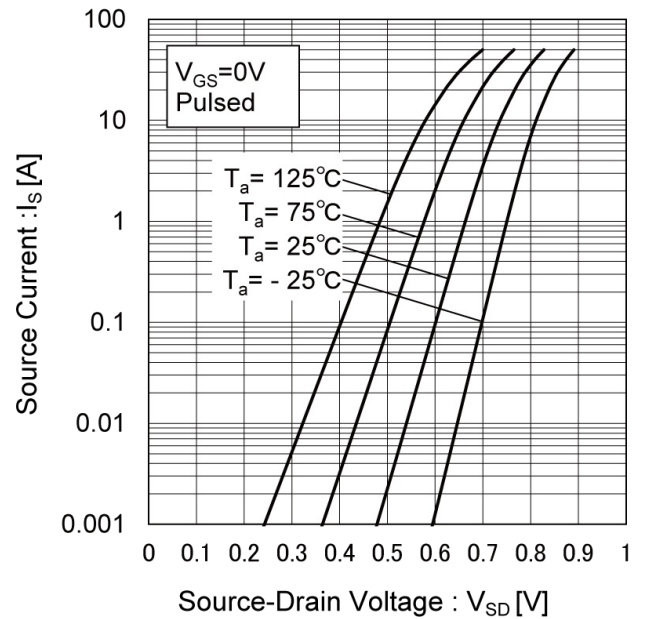


Fig.20 Source Current vs. Source Drain Voltage





● Measurement circuits

Fig.1-1 Switching Time Measurement Circuit



Fig.1-2 Switching Waveforms

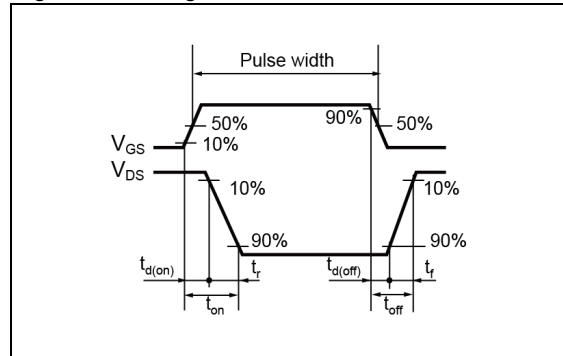


Fig.2-1 Gate Charge Measurement Circuit

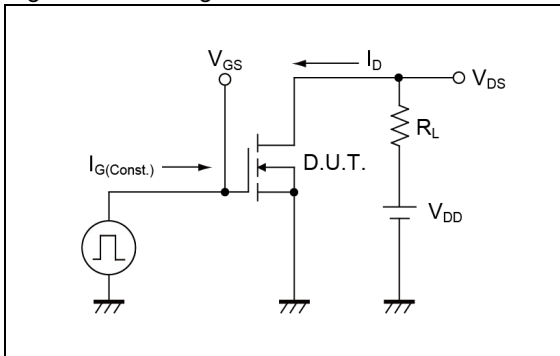


Fig.2-2 Gate Charge Waveform

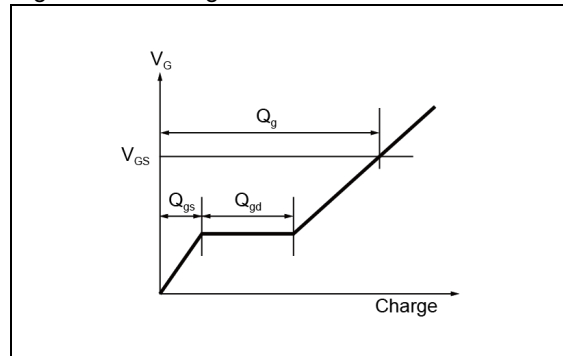


Fig.3-1 Avalanche Measurement Circuit

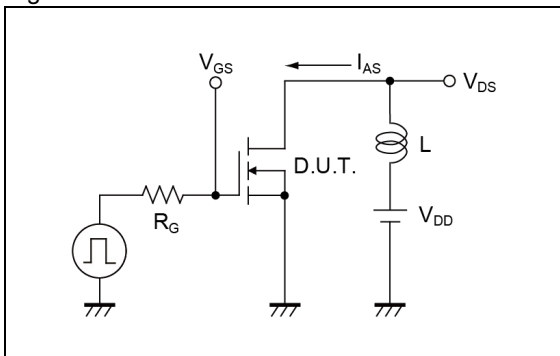
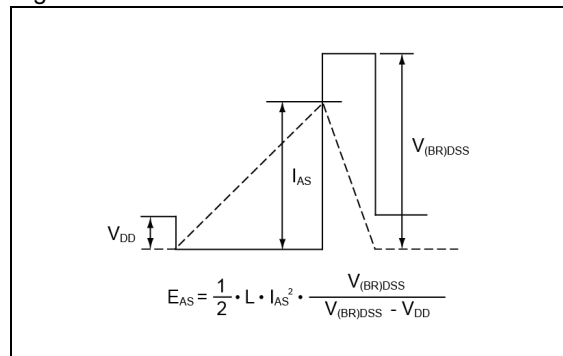


Fig.3-2 Avalanche Waveform



● Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



# Notice

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JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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  - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - Sealing or coating our Products with resin or other coating materials
  - Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.) ; or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

## Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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### Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

### Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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