AEC-Q101 Qualified

4V Drive Nch+Nch MOSFET SP8K31FRA

Structure

Silicon N-channel **MOSFET**

Features

- 1) Built-in G-S Protection Diode.
- 2) Small surface Mount Package (SOP8).

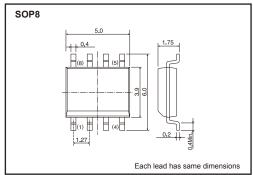
Applications

Switching

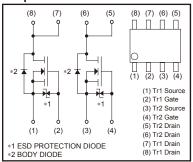
Packaging dimensions

•	Package	Taping
Туре	Code	TB
	Basic ordering unit (pieces)	2500
SP8K31FRA	0	

●Dimensions (Unit:mm)



Equivalent circuit



A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

● Absolute maximum ratings (Ta=25°C)

<It is the same ratings for the Tr1 and Tr2.>

Parameter		Symbol	Limits	Unit	
Drain-source voltage		V _{DSS}	60	V	
Gate-source voltage		Vgss	±20	V	
Dunin assument	Continuous	ID	±3.5	Α	
Drain current	Pulsed	I _{DP} *1	±14	Α	
Source current	Continuous	Is	1.0	Α	
(Body diode)	Pulsed	Isp *1	14	Α	
Total power dissipation		P _D *2	2.0	W	
Channel temperature		Tch	150	°C	
Range of storage temperature		Tstg	-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1%

^{*2} Mounted on a ceramic board.

●Electrical characteristics (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	I _{GSS}	-	-	±10	μА	V _{GS} =±20V, V _{DS} =0V	
Drain-source breakdown voltage	V(BR) DSS	60	_	_	V	Ip= 1mA, Vgs=0V	
Zero gate voltage drain current	I _{DSS}	_	-	1	μА	V _{DS} = 60V, V _{GS} =0V	
Gate threshold voltage	V _{GS (th)}	1.0	-	2.5	V	V _{DS} = 10V, I _D = 1mA	
		-	85	120	mΩ	I _D = 3.5A, V _{GS} = 10V	
Static drain-source on-state resistance	RDS (on)*	_	100	140	mΩ	ID= 3.5A, VGS= 4.5V	
resistance		_	105	150	mΩ	I _D = 3.5A, V _{GS} = 4.0V	
Forward transfer admittance	Y _{fs} *	2.5	-	_	S	V _{DS} = 10V, I _D = 3.5A	
Input capacitance	Ciss	-	250	_	pF	V _{DS} = 10V	
Output capacitance	Coss	_	60	_	pF	V _{GS} =0V	
Reverse transfer capacitance	Crss	_	30	_	pF	f=1MHz	
Turn-on delay time	t _{d (on)} *	_	7	_	ns	V _{DD} = 30V I _D = 1.8A V _{GS} = 10V R _I = 17Ω	
Rise time	tr *	-	14	_	ns		
Turn-off delay time	td (off) *	_	25	_	ns		
Fall time	t _f *	_	7	_	ns	R _G =10Ω	
Total gate charge	Qg *	_	3.7	5.2	nC	V _{DD} ≒30V, V _{GS} =5V	
Gate-source charge	Qgs *	_	1.2	_	nC	I _D = 3.5A	
Gate-drain charge	Qgd *	_	1.2	_	nC	R _L = 8.6Ω, R _G = 10Ω	

^{*}Pulsed

$\bullet \textbf{Body diode characteristics} \ (Source-drain) \ (Ta=25^{\circ}C)$

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	V _{SD} *	_	_	1.2	V	Is=3.5A. V _{GS} =0V

^{*}Pulsed

Electrical characteristic curves

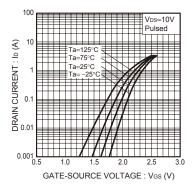


Fig.1 Typical Transfer Characteristics

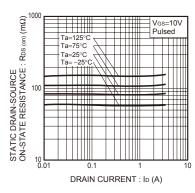


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current(I)

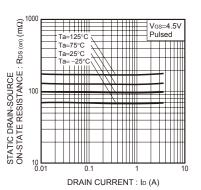


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current(II)

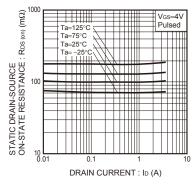


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(III)

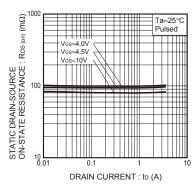


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(IV)

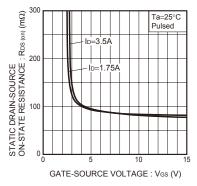


Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

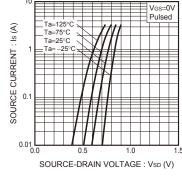


Fig.7 Source Current vs. Source-Drain Voltage

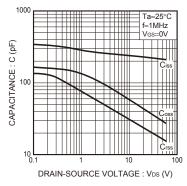


Fig.8 Typical Capacitance vs. Drain-Source Voltage

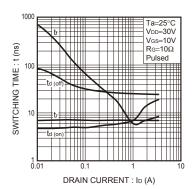


Fig.9 Switching Characteristics

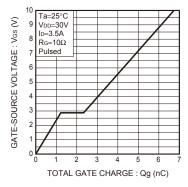


Fig.10 Dynamic Input Characteristics

Measurement circuits

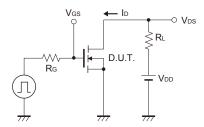


Fig.11 Switching Time Test Circuit

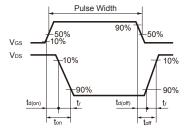


Fig.12 Switching Time Waveforms

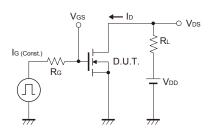


Fig.13 Gate Charge Test Circuit

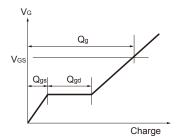


Fig.14 Gate Charge Waveform

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JAPAN	N USA EU		CHINA	
CLASSⅢ	CLACCIII	CLASS II b	CL A C C TT	
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSII	

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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