60V Nch+Nch Power MOSFET

| V _{DSS} | 60V |
|----------------------------|-------|
| R _{DS(on)} (Max.) | 65mΩ |
| I _D | ±4.5A |
| P _D | 2.0W |

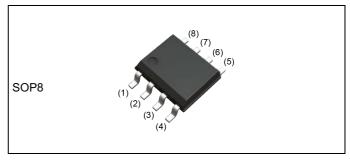
Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating; RoHS compliant
- 4) AEC-Q101 Qualified

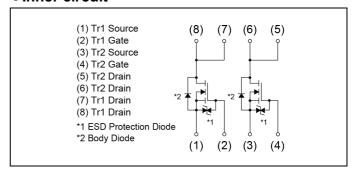
Application

Switching

Outline



•Inner circuit



Packaging specifications

| | Packing | Embossed Tape |
|------|---------------------------|------------------|
| | Reel size (mm) | 330 |
| Туре | Tape width (mm) | 12 |
| | Basic ordering unit (pcs) | 2500 |
| | Taping code | ТВ |
| | Marking | SP8K32 |

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified) < Tr1 and Tr2>

| Parameter | Symbol | Value | Unit | |
|--|--------------------|-------------|------|--|
| Drain - Source voltage | V_{DSS} | 60 | V | |
| Continuous drain current | I _D | ±4.5 | Α | |
| Pulsed drain current | I _{DP} *1 | ±18 | Α | |
| Gate - Source voltage | V _{GSS} | ±20 | V | |
| Down discipation (total) | P _D *2 | 2.0 | \\/ | |
| Power dissipation (total) | P _D *3 | 1.4 | W | |
| Junction temperature | T _j | 150 | °C | |
| Operating junction and storage temperature range | T _{stg} | -55 to +150 | °C | |

●Thermal resistance

| Downwater | Currele el | Values | | | 1.1-:4 |
|--|----------------------|--------|------|------|--------|
| Parameter | Symbol | Min. | Тур. | Max. | Unit |
| Thermal registeres innetion, embient (total) | R _{thJA} *2 | - | - | 62.5 | °C/W |
| Thermal resistance, junction - ambient (total) | R _{thJA} *3 | - | - | 89.2 | C/VV |

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

| Davanastav | Cy reals al | Conditions | Values | | | Unit | |
|--|---|---|--------|------|------|-------|--|
| Parameter | arameter Symbol Conditions - | | Min. | Тур. | Max. | UTIIL | |
| Drain - Source breakdown voltage | V _{(BR)DSS} | $V_{GS} = 0V$, $I_D = 1mA$ | 60 | - | - | V | |
| Breakdown voltage temperature coefficient | $\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$ | I _D = 1mA referenced to 25°C | - | 63.7 | - | mV/°C | |
| Zero gate voltage drain current | I _{DSS} | V _{DS} = 60V, V _{GS} = 0V | | - | 1 | μA | |
| Gate - Source leakage current | I _{GSS} | V _{DS} = 0V, V _{GS} = ±20V | | - | ±10 | μA | |
| Gate threshold voltage | V _{GS(th)} | V _{DS} = 10V, I _D = 1mA | | - | 2.5 | V | |
| Gate threshold voltage temperature coefficient | $\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$ | I _D = 1mA referenced to 25°C | - | -2.8 | - | mV/°C | |
| | | V _{GS} = 10V, I _D = 4.5A | - | 46 | 65 | | |
| Static drain - source on - state resistance | R _{DS(on)} *4 | V _{GS} = 4.5V, I _D = 4.5A | - | 52 | 73 | mΩ | |
| on state resistance | | V _{GS} = 4.0V, I _D = 4.5A | - | 55 | 77 | | |
| Gate resistance | R_G | f = 1MHz, open drain | - | 5.9 | - | Ω | |
| Forward Transfer Admittance | Y _{fs} *4 | V _{DS} = 10V, I _D = 4.5A | 4.0 | - | - | S | |

^{*1} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*2} Mounted on a ceramic board (30×30×0.8mm)

^{*3} Mounted on a FR4 (25×25×0.8mm)

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

| Daramatar | Cymahal | Conditions | | Unit | | | |
|------------------------------|------------------------|---|------|------|------|------|--|
| Parameter | Symbol Conditions — | | Min. | Тур. | Max. | Uill | |
| Input capacitance | C _{iss} | V _{GS} = 0V | - | 500 | - | | |
| Output capacitance | C _{oss} | V _{DS} = 10V | - | 120 | - | pF | |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | - | 55 | - | | |
| Turn - on delay time | t _{d(on)} *4 | V _{DD} ≈ 30V,V _{GS} = 10V | - | 12 | - | | |
| Rise time | t _r *4 | I _D = 2.3A | - | 18 | - | | |
| Turn - off delay time | t _{d(off)} *4 | $R_L = 13\Omega$ | - | 40 | - | ns | |
| Fall time | t _f *4 | $R_G = 10\Omega$ | - | 13 | - | | |

ullet Gate charge characteristics (T_a = 25°C) <Tr1 and Tr2>

| Parameter | Symbol | Conditions | Values | | | Unit |
|----------------------|--------------------|---|--------|------|------|-------|
| raianetei | Symbol | Conditions | Min. | Тур. | Max. | Offic |
| Total gate charge | Q_g^{*4} | | - | 7.0 | 10 | |
| Gate - Source charge | Q _{gs} *4 | $V_{DD} \approx 30V, I_{D} = 4.5A$ $V_{GS} = 5V$ | - | 1.6 | - | nC |
| Gate - Drain charge | Q _{gd} *4 | 1.00 | - | 2.5 | - | |

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1 and Tr2>

| Darameter | Symbol | Conditions | Values | | | Unit |
|----------------------------|--------------------|---|--------|------|------|-------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Offic |
| Continuous forward current | I _S | T - 25°C | - | - | 1.0 | _ |
| Pulse forward current | I _{SP} *1 | T _a = 25°C | - | - | 18 | Α |
| Forward voltage | V _{SD} *4 | V _{GS} = 0V, I _S = 4.5A | - | - | 1.2 | V |

Fig.1 Power Dissipation Derating Curve

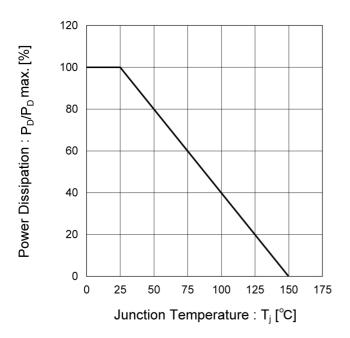
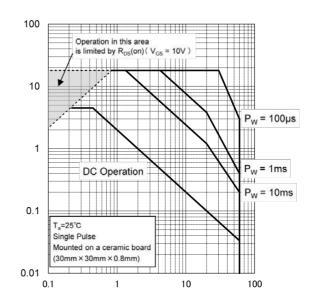


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage: V_{DS}[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

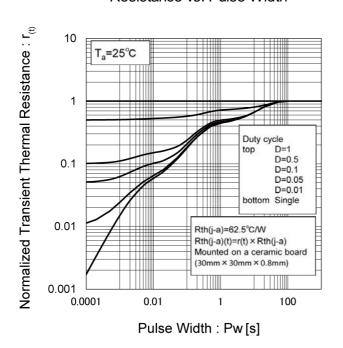
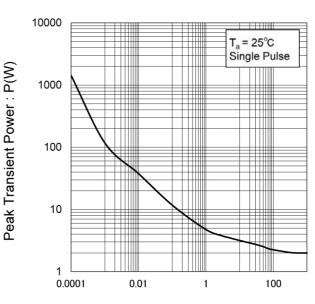


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width: Pw[s]

Fig.5 Typical Output Characteristics(I)

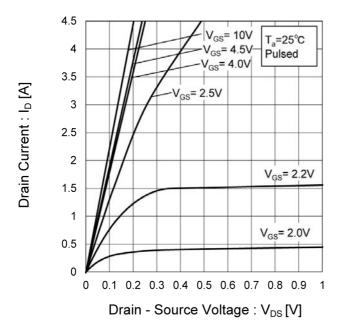
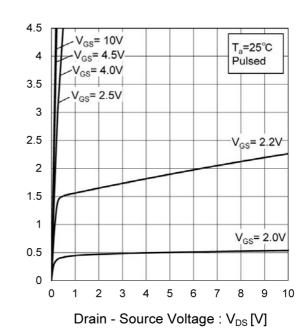


Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.7 Breakdown Voltage vs.
Junction Temperature

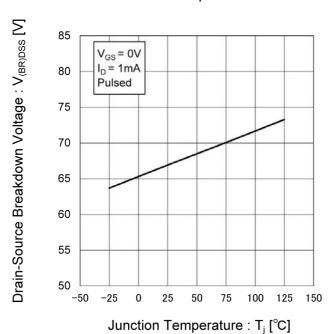


Fig.8 Typical Transfer Characteristics

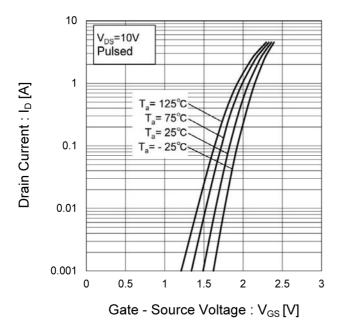


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

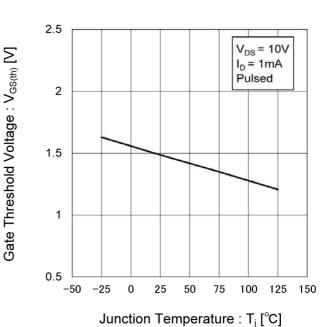


Fig.10 Forward Transfer Admittance vs.
Drain Current

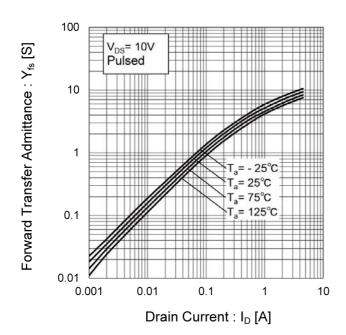


Fig.11 Drain Current Derating Curve

120 100 Drain Current Dissipation 80 : I_D/I_Dmax. [%] 60 40 20 0 -25 0 25 50 75 100 125 150 Junction Temperature : T_j [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

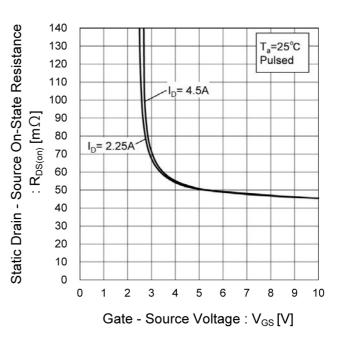


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

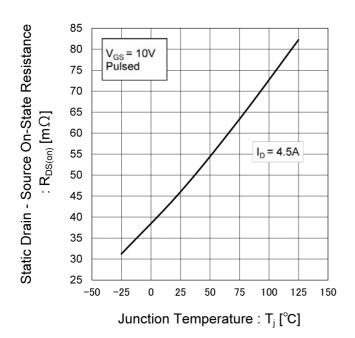


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

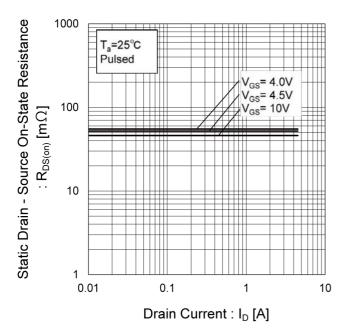


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

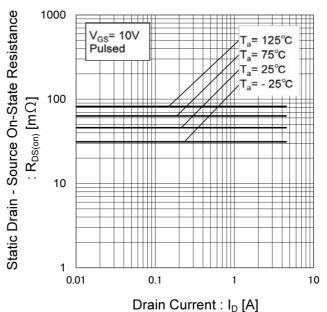


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

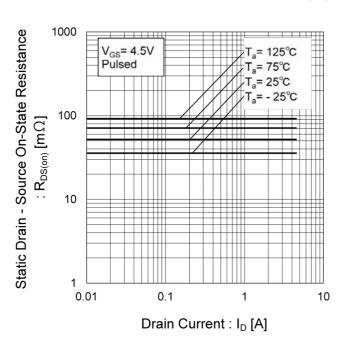


Fig.17 Static Drain - Source On - State
Resistance vs. Drain Current (IV)

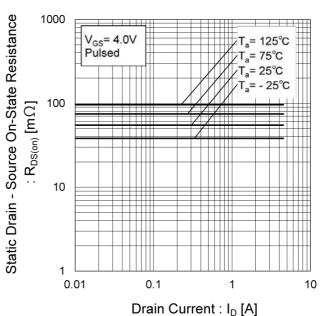


Fig.18 Typical Capacitance vs.

Drain - Source Voltage

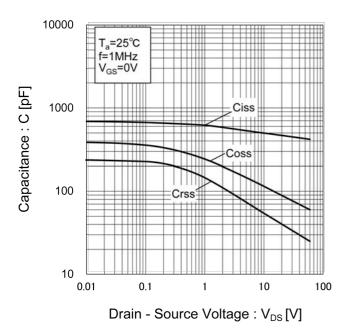


Fig.19 Switching Characteristics

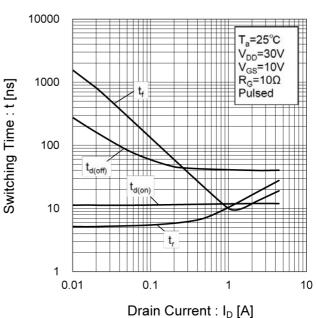


Fig.20 Dynamic Input Characteristics

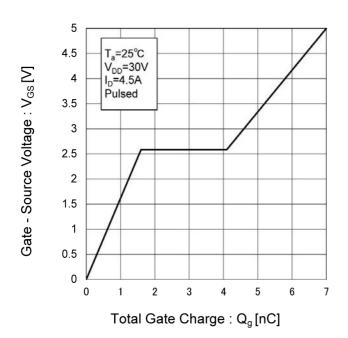
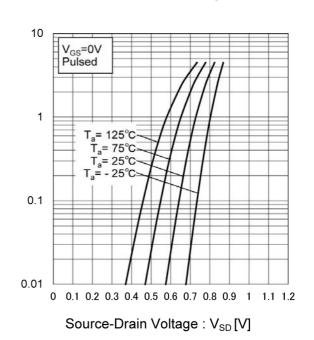


Fig.21 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

• Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

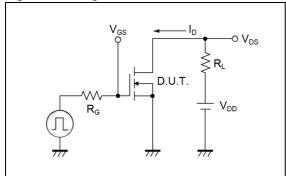


Fig.2-1 Gate Charge Measurement Circuit

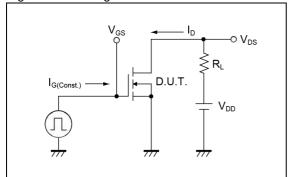


Fig.1-2 Switching Waveforms

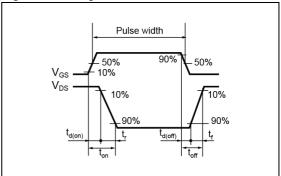
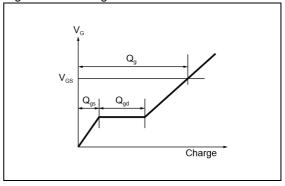
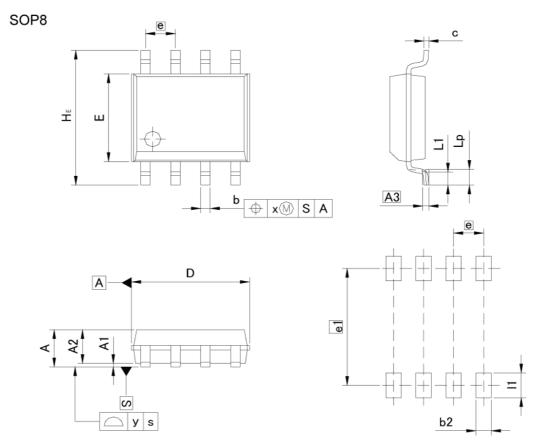


Fig.2-2 Gate Charge Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM - | MILIM | ETERS | INC | HES |
|-------|-----------|-------|-------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | <u>₽3</u> | 1.75 | *** | 0.069 |
| A1 | 0. | 15 | 0.0 | 06 |
| A2 | 1.40 | 1.60 | 0.055 | 0.063 |
| A3 | 0. | 25 | 0.0 | 10 |
| b | 0.30 | 0.50 | 0.012 | 0.020 |
| С | 0.10 | 0.30 | 0.004 | 0.012 |
| D | 4.80 | 5.20 | 0.189 | 0.205 |
| E | 3.75 | 4.05 | 0.148 | 0.159 |
| е | 1. | 27 | 0.0 | 50 |
| HE | 5.70 | 6.30 | 0.224 | 0.248 |
| L1 | 0.40 | 0.60 | 0.016 | 0.024 |
| Lp | 0.65 | 0.85 | 0.026 | 0.033 |
| х | 0.15 | | 0.0 | 06 |
| у | 0. | 10 | 0.004 | |
| - T | MILIM | ETERS | INC | HES |
| DIM | MIN | MAX | MIN | MAX |
| b2 | | 0.65 | | 0.026 |

Dimension in mm/inches

e1



0.045

0.203

1.15

5.15

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| JÁPAN | USA | EU | CHINA |
|---------|------------|------------|----------|
| CLASSⅢ | CL A CC TT | CLASS II b | CLASSⅢ |
| CLASSIV | CLASSⅢ | CLASSⅢ | CLASSIII |

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 - [f] Sealing or coating our Products with resin or other coating materials
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 - [h] Use of the Products in places subject to dew condensation
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
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- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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