60V Nch+Nch Power MOSFET

V_{DSS}	60V
R _{DS(on)} (Max.)	48mΩ
I _D	±5.0A
P _D	2.0W

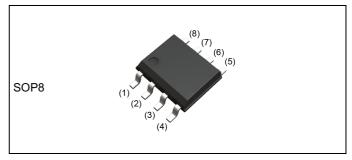
Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free
- 5) Sn100% plating
- 6) AEC-Q101 Qualified

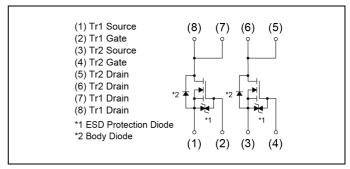
Application

Switching

Outline



•Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Type	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	SP8K33

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V_{DSS}	60	V	
Continuous drain current	I _D	±5.0	Α	
Pulsed drain current	I _{DP} *1	±20	Α	
Gate - Source voltage	V _{GSS}	±20	V	
David dia dia dia dia dia dia dia dia dia d	P_{D}^{*2}	2.0	\\\\	
Power dissipation (total)	P _D *3	1.4	W	
Junction temperature	T _j	150	°C	
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C	

●Thermal resistance

Daramatar	Cymahal	Values			I limit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registance innetion, ambient (total)	R _{thJA} *2	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	R _{thJA} *3	-	-	89.2	C/VV

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Davanatas	Cy made al	Conditions	Values			1 1
Parameter Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	60	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	63.7	-	mV/°C
Zero gate voltage drain current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V		-	1	μА
Gate - Source leakage current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V		-	±10	μА
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 1mA		-	2.5	V
Gate the short voltage		I _D = 1mA referenced to 25°C	-	-2.8	-	mV/°C
		V _{GS} = 10V, I _D = 5.0A	-	34	48	
Static drain - source on - state resistance	R _{DS(on)} *4	V _{GS} = 4.5V, I _D = 5.0A	-	38 54 ms		mΩ
on - state resistance		V _{GS} = 4.0V, I _D = 5.0A	-	40	56	
Gate resistance	R_{G}	f = 1MHz, open drain	-	4.6	-	Ω
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 10V, I _D = 5.0A	5.0	-	-	S

^{*1} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*2} Mounted on a ceramic board (30×30×0.8mm)

^{*3} Mounted on a Cu board (40×40×0.8mm)

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Doromotor	Symbol	Conditions	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Uriit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	620	-		
Output capacitance	C _{oss}	V _{DS} = 10V	-	145	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	70	-		
Turn - on delay time	$t_{d(on)}^{*4}$	V _{DD} ≈ 30V,V _{GS} = 10V	-	12	-		
Rise time	t _r *4	I _D = 2.5A	-	20	-		
Turn - off delay time	t _{d(off)} *4	$R_L = 12\Omega$	-	40	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	20	-		

ullet Gate charge characteristics (T_a = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
raianetei	Symbol	Symbol Conditions		Тур.	Max.	Offic
Total gate charge	Q_g^{*4}		-	8.0	12.0	
Gate - Source charge	Q _{gs} *4	$V_{DD} \approx 30V, I_{D} = 5.0A$ $V_{GS} = 5V$	-	2.0	-	nC
Gate - Drain charge	Q _{gd} *4	1.00	-	2.6	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offit
Continuous forward current	I _S	T - 25°C	-	-	1.0	Δ.
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	20	Α
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 5.0A	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

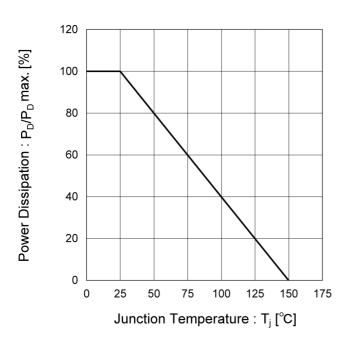
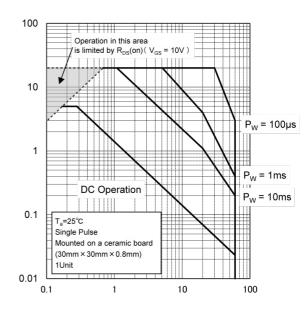


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

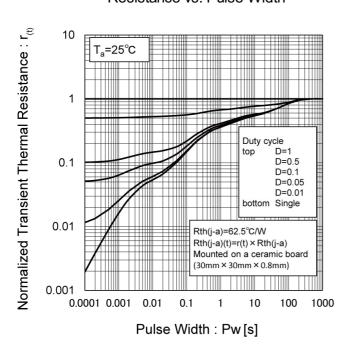


Fig.4 Single Pulse Maximum Power dissipation

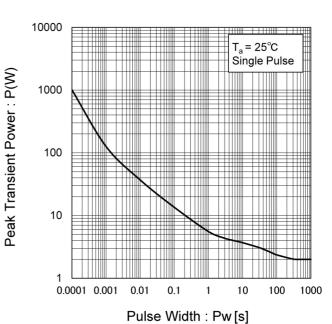


Fig.5 Typical Output Characteristics(I)

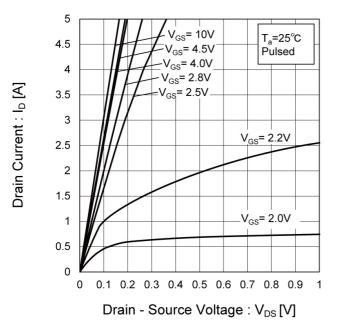
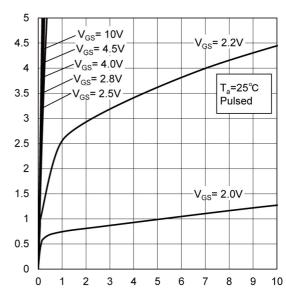


Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

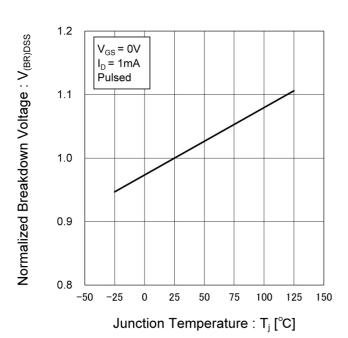


Fig.8 Typical Transfer Characteristics

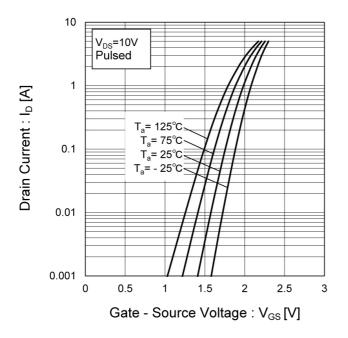
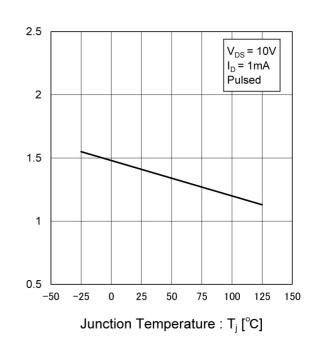


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage : $V_{GS(th)}\left[V\right]$

Fig.10 Forward Transfer Admittance vs.
Drain Current

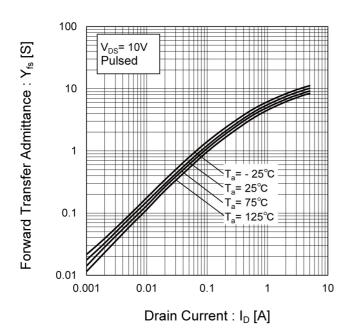


Fig.11 Drain Current Derating Curve

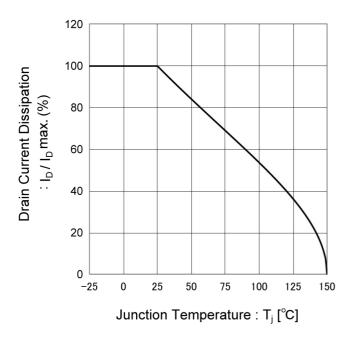


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

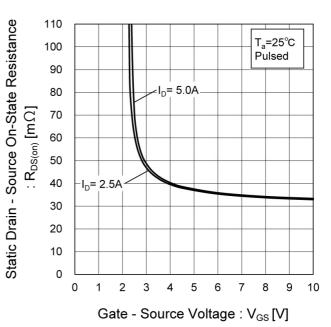
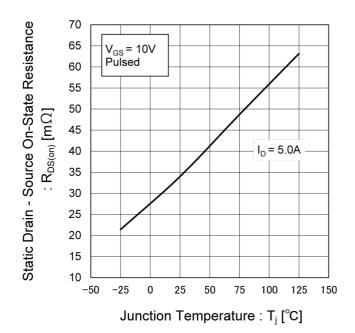


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



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Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

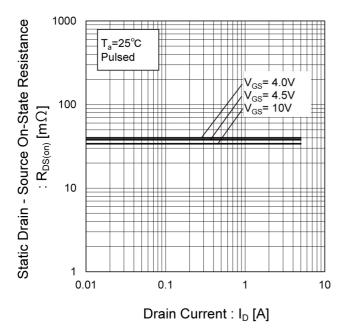


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

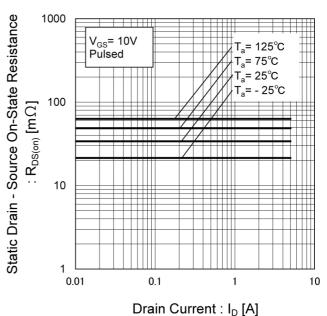


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

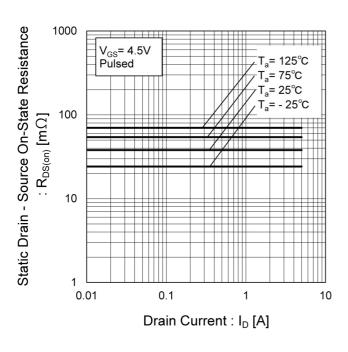
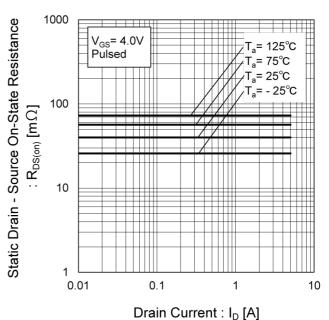


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)



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Fig.18 Typical Capacitance vs.

Drain - Source Voltage

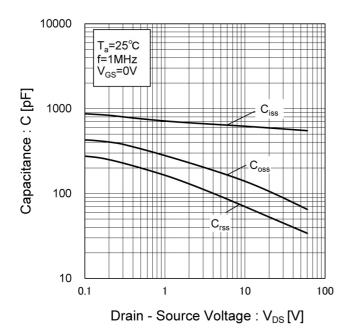


Fig.19 Switching Characteristics

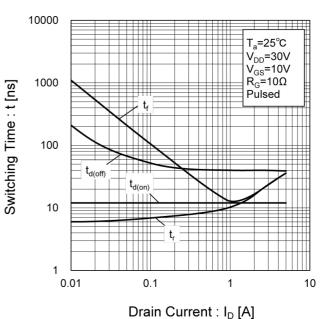


Fig.20 Dynamic Input Characteristics

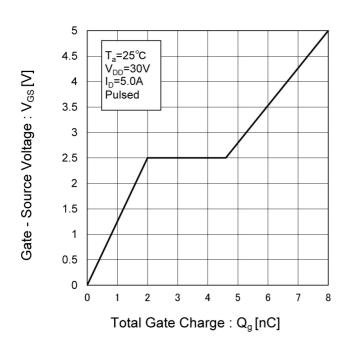
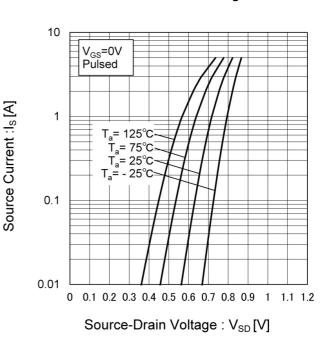


Fig.21 Source Current vs.

Source Drain Voltage



• Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

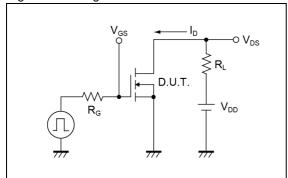


Fig.2-1 Gate Charge Measurement Circuit

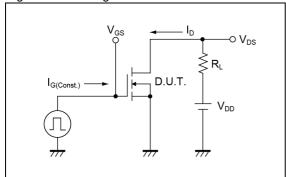


Fig.1-2 Switching Waveforms

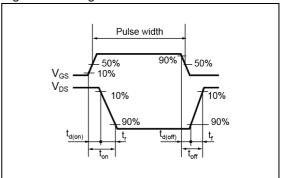
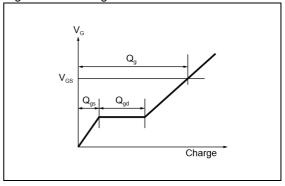
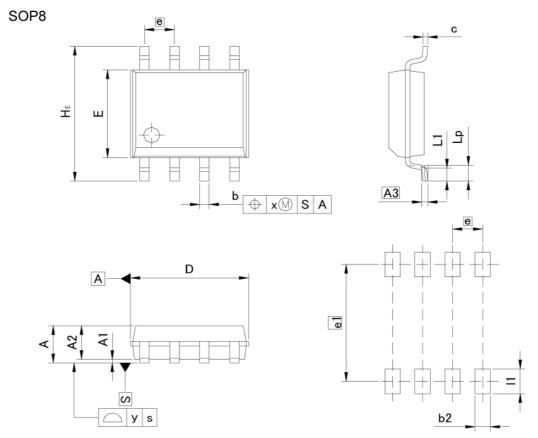


Fig.2-2 Gate Charge Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM MIN		MAX	MIN	MAX
Α	-	1.75	-	0.069
A1	0.	15	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.5	25	0.0	10
b	0.30	0.50	0.012	0.020
С	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
Е	3.75	4.05	0.148	0.159
е	1.3	27	0.050	
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
х	0.15		0.0	06
У	0.10		0.0	04
			0.0	
DIM	MILIM	ETERS	INC	HES
ן ואונט				

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-	0.65	-	0.026	
e1	5.	15	0.2	03	
11	-,7	1.15	- 1	0.045	

Dimension in mm/inches



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ſ	JÁPAN	USA	EU	CHINA
Ī	CLASSⅢ	CL ACCIII	CLASS II b	СГУССШ
ſ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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