4V Drive Nch+SBD MOSFET **US5U2**

●Structure

Silicon N-channel MOSFET / Schottky barrier diode

● Features

- 1) Nch MOSFET and schottky barrier diode are put in TUMT5 package.
- 2) High-speed switching, Low On-resistance.
- 3) 4V drive.
- 4) Built-in Low VF schottky barrier diode.

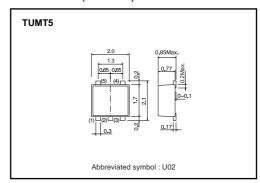
Applications

Switching

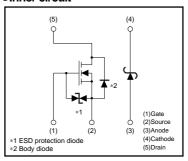
Packaging specifications

	Package	Taping
Type	Code	TR
	Quantity (pcs)	3000
US5U2		0

● Dimensions (Unit: mm)



•Inner circuit



● Absolute maximum ratings (Ta=25°C)

<MOSFET>

Parameter	Symbol	Limits	Unit					
Drain-source voltage		V_{DSS}	30	V				
Gate-source voltage	Vgss	20	V					
Drain augrent	Continuous	I_D	±1.4	Α				
Drain current	Pulsed	I _{DP} *1	±5.6	Α				
Source current	Continuous	Is	0.6	Α				
(Body diode)	Pulsed	Isp *1	5.6	Α				
Power dissipation		P _D *2	0.7	W / ELEMENT				
Channel temperature		Tch	150	°C				



^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

<Di>

Parameter	Symbol	Limits	Unit
Repetitive peak reverse voltage	V _{RM}	30	V
Reverse voltage	V _R	20	V
Forward current	l _F	0.5	A
Forward current surge peak	I _{FSM} *1	2.0	A
Power dissipation	P _D *2	0.5	W / ELEMENT
Junction temperature	Tj	150	°C

<MOSFET and Di>

Parameter	Symbol	Limits	Unit
Total power dissipation	P _D *1	1.0	W / TOTAL
Range of storage temperature	Tstg	-55 to +150	°C

^{*1} Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

<MOSFET>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	10	μА	V _{GS} =20V, V _{DS} =0V
Drain-source breakdown voltage	V _(BR) DSS	30	-	_	٧	I _D = 1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	V _{DS} = 30V, V _{GS} =0V
Gate threshold voltage	VGS (th)	1.0	_	2.5	٧	V _{DS} = 10V, I _D = 1mA
Otatia dasia assuma an atata		-	170	240	mΩ	I _D = 1.4A, V _{GS} = 10V
Static drain-source on-state resistance	R _{DS (on)} *	_	250	350	mΩ	I _D = 1.4A, V _{GS} = 4.5V
resistance		_	270	380	mΩ	I _D = 1.4A, V _{GS} = 4V
Forward transfer admittance	Y _{fs} *	1.0	_	_	S	V _{DS} = 10V, I _D = 1.4A
Input capacitance	Ciss	_	70	-	pF	V _{DS} = 10V
Output capacitance	Coss	-	15	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	_	12	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	_	6	_	ns	V _{DD} ≒ 15V
Rise time	tr *	_	6	_	ns	ID= 0.7A
Turn-off delay time	t _{d (off)} *	_	13	_	ns	Vgs= 10V RL= 21Ω
Fall time	t _f *	-	8	_	ns	R _G =10Ω
Total gate charge	Qg *	-	1.4	2.0	nC	V _{DD} ≒15V, V _{GS} =5V
Gate-source charge	Qgs *	_	0.6	_	nC	ID= 1.4A
Gate-drain charge	Q _{gd} *	_	0.3	_	nC	$R_L=11\Omega$, $R_G=10\Omega$

^{*}Pulsed

<Body diode characteristics (source-drain)>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp	_	_	1.2	V	I _S = 0.6A, V _{GS} =0V

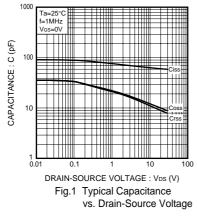
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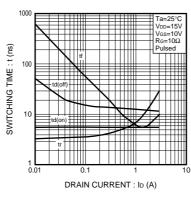
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VF	_	-	0.36	V	I _F = 0.1A
		-	-	0.47	V	I _F 0.5A
Reverse current	l _R	-	-	100	μΑ	V _R = 20V



^{*1 60}Hz •1cycle *2 Mounted on ceramic board

•Electrical characteristics curves





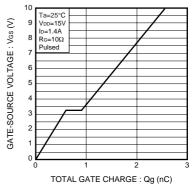


Fig.2 Switching Characteristics

Fig.3 Dynamic Input Characteristics

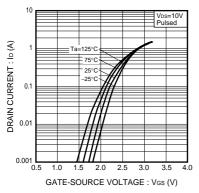
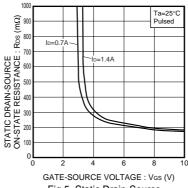


Fig.4 Typical Transfer Characteristics



GATE-SOURCE VOLTAGE: Vos (V)
Fig.5 Static Drain-Source
On-State Resistance vs.
Gate-source Voltage

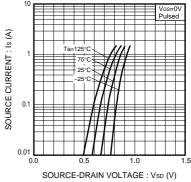


Fig.6 Source Current vs.
Source-Drain Voltage

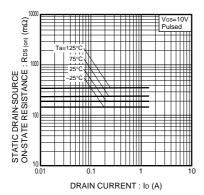


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

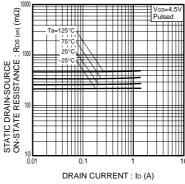


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

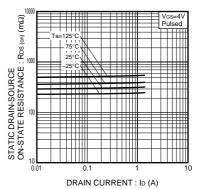


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)



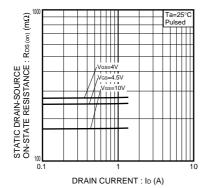


Fig.10 Static Drain-Source On-State Resistance vs. Drain Current (IV)



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JAPAN	USA	EU	CHINA	
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CLASSIV	CLASSIII	CLASSⅢ	CLASSIII	

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 - [d] the Products are exposed to high Electrostatic
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 exceeding the recommended storage time period.
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