Operational Amplifiers

Input/Output Full Swing
High Voltage Operation
Low Supply Current
CMOS Operational Amplifiers

BD7541G  BD7541SG  BD7542xxx  BD7542Sxxx

General Descriptions
BD7541G/BD7542xxx are high voltage operation input/output full swing CMOS operational amplifiers. BD7541SG/BD7542Sxxx have an expanded operating temperature range. They have wide operating voltage range, from +5V to +14.5V with low supply current and low input bias current. There are suitable for industrial equipment and sensor amplifiers.

Features
- Operable High Operating Voltage
- Input and Output Full Swing
- Low Supply Current
- High Large Signal Voltage Gain
- Wide Operating Voltage Range

Applications
- Sensor Amplifier
- Industrial Equipment
- Consumer Equipment

Key Specifications
- Operating Supply Voltage:
  - Single supply: +5V to +14.5V
  - Split supply: ±2.5V to ±7.25V
- Temperature Range:
  - BD7541G/BD7542xxx: -40°C to +85°C
  - BD7541SG/BD7542Sxxx: -40°C to +105°C
- Supply Current:
  - BD7541G/BD7541SG: 170µA (Typ)
  - BD7542xxx/BD7542Sxxx: 340µA (Typ)
  - BD7542Sxxx: 340µA (Typ)
- Input Offset Current: 1pA (Typ)
- Input Bias Current: 1pA (Typ)

Package
- SSOP5: W(Typ) x D(Typ) x H(Max) 2.90mm x 2.80mm x 1.25mm
- SOP8: 5.00mm x 6.20mm x 1.61mm
- MSOP8: 2.90mm x 4.00mm x 0.90mm

Simplified Schematic

Figure 1. Simplified Schematic

Product structure: Silicon monolithic integrated circuit
This product is not designed protection against radioactive rays.
Pin Configuration

BD7541G, BD7541SG: SSOP5

BD7542F, BD7542SF: SOP8
BD7542FVM, BD7542SFVM: MSOP8

Ordering Information

Part Number | Package | Packaging and forming specification
--- | --- | ---
BD7541G | G : SSOP5 | E2: Embossed tape and reel (SOP8)
BD75461SG | F : SOP8 | TR: Embossed tape and reel (SSOP5/MSOP8)
BD7542xxx | FVM : MSOP8 |
BD7542Sxxx |

Line-up

<table>
<thead>
<tr>
<th>$T_{opr}$</th>
<th>Channels</th>
<th>Package</th>
<th>Orderable Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>-40°C to +85°C</td>
<td>1ch</td>
<td>SSOP5</td>
<td>Reel of 3000</td>
</tr>
<tr>
<td></td>
<td>2ch</td>
<td>SOP8</td>
<td>Reel of 2500</td>
</tr>
<tr>
<td></td>
<td></td>
<td>MSOP8</td>
<td>Reel of 3000</td>
</tr>
<tr>
<td>-40°C to +105°C</td>
<td>1ch</td>
<td>SSOP5</td>
<td>Reel of 3000</td>
</tr>
<tr>
<td></td>
<td>2ch</td>
<td>SOP8</td>
<td>Reel of 2500</td>
</tr>
<tr>
<td></td>
<td></td>
<td>MSOP8</td>
<td>Reel of 3000</td>
</tr>
</tbody>
</table>
### Absolute Maximum Ratings ($T_A=25^\circ C$)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Rating</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>VDD-VSS</td>
<td>$+15.5$</td>
<td>V</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>$P_D$</td>
<td>SSOP5 $0.54$ ($\text{Note 1,4}$)</td>
<td>W</td>
</tr>
<tr>
<td></td>
<td></td>
<td>SOP8 $0.55$ ($\text{Note 2,4}$)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>MSOP8 $0.47$ ($\text{Note 3,4}$)</td>
<td></td>
</tr>
<tr>
<td>Differential Input Voltage</td>
<td>$V_{\text{ID}}$</td>
<td>VDD - VSS</td>
<td>V</td>
</tr>
<tr>
<td>Input Common-mode Voltage Range</td>
<td>$V_{\text{ICM}}$</td>
<td>(VSS - 0.3) to (VDD + 0.3)</td>
<td>V</td>
</tr>
<tr>
<td>Input Current (Note 6)</td>
<td>$I_I$</td>
<td>±10</td>
<td>mA</td>
</tr>
<tr>
<td>Operating Supply Voltage</td>
<td>$V_{\text{opr}}$</td>
<td>+5 to +14.5</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>±2.5 to ±7.25</td>
<td></td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>$T_{\text{opr}}$</td>
<td>-40 to +85</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>$T_{\text{stg}}$</td>
<td>-55 to +125</td>
<td>°C</td>
</tr>
<tr>
<td>Maximum Junction Temperature</td>
<td>$T_{\text{Jmax}}$</td>
<td>+125</td>
<td>°C</td>
</tr>
</tbody>
</table>

(Note 1) To use at temperature above $T_A=25^\circ C$ reduce 5.4mW/°C.
(Note 2) To use at temperature above $T_A=25^\circ C$ reduce 5.5 mW/°C.
(Note 3) To use at temperature above $T_A=25^\circ C$ reduce 4.7mW/°C.
(Note 4) Mounted on a FR4 glass epoxy PCB 70mm×70mm×1.6mm (Copper foil area less than 3%).
(Note 5) The voltage difference between inverting input and non-inverting input is the differential input voltage. Then input pin voltage is set to more than VSS.
(Note 6) An excessive input current will flow when input voltages of more than $V_{\text{DD}}+0.6V$ or less than $V_{\text{SS}}-0.6V$ are applied. The input current can be set to less than the rated current by adding a limiting resistor.

Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.
### Electrical Characteristics

BD7541G, BD7541SG (Unless otherwise specified VDD=+12V, VSS=0V, T_{A}=25°C)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Temperature Range</th>
<th>Limit</th>
<th>Unit</th>
<th>Condition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Offset Voltage (Note 7, 9)</td>
<td>V_{IO}</td>
<td>25°C</td>
<td>-</td>
<td>1</td>
<td>9 mV VDD=5 to 14.5V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Full range</td>
<td>-</td>
<td>-</td>
<td>10</td>
</tr>
<tr>
<td>Input Offset Current (Note 7)</td>
<td>I_{IO}</td>
<td>25°C</td>
<td>-</td>
<td>1</td>
<td>- pA</td>
</tr>
<tr>
<td>Input Bias Current (Note 7)</td>
<td>I_{IB}</td>
<td>25°C</td>
<td>-</td>
<td>1</td>
<td>- pA</td>
</tr>
<tr>
<td>Supply Current (Note 8)</td>
<td>I_{DD}</td>
<td>25°C</td>
<td>-</td>
<td>170</td>
<td>300 μA R_{L}=∞, A_{V}=0dB, VDD=5V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Full range</td>
<td>-</td>
<td>-</td>
<td>400</td>
</tr>
<tr>
<td></td>
<td></td>
<td>25°C</td>
<td>-</td>
<td>180</td>
<td>320</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Full range</td>
<td>-</td>
<td>-</td>
<td>420</td>
</tr>
<tr>
<td>Maximum Output Voltage (High)</td>
<td>V_{OH}</td>
<td>25°C</td>
<td>VDD-0.1</td>
<td>-</td>
<td>V V_{SS} to VDD</td>
</tr>
<tr>
<td>Maximum Output Voltage (Low)</td>
<td>V_{OL}</td>
<td>25°C</td>
<td>VSS+0.1</td>
<td>-</td>
<td>V V_{RL}=10kΩ</td>
</tr>
<tr>
<td>Large Single Voltage Gain</td>
<td>A_{V}</td>
<td>25°C</td>
<td>70</td>
<td>95</td>
<td>- dB R_{L}=10kΩ</td>
</tr>
<tr>
<td>Input Common-mode Voltage Range</td>
<td>V_{ICM}</td>
<td>25°C</td>
<td>0</td>
<td>12</td>
<td>- V V_{SS} to VDD</td>
</tr>
<tr>
<td>Common-mode Rejection Ratio</td>
<td>CMRR</td>
<td>25°C</td>
<td>45</td>
<td>60</td>
<td>- dB</td>
</tr>
<tr>
<td>Power Supply Rejection Ratio</td>
<td>PSRR</td>
<td>25°C</td>
<td>60</td>
<td>80</td>
<td>- dB</td>
</tr>
<tr>
<td>Output Source Current (Note 9)</td>
<td>I_{SOURCE}</td>
<td>25°C</td>
<td>2</td>
<td>4</td>
<td>- mA OUT=VDD-0.4V</td>
</tr>
<tr>
<td>Output Sink Current (Note 9)</td>
<td>I_{SINK}</td>
<td>25°C</td>
<td>3</td>
<td>7</td>
<td>- mA OUT=VSS+0.4V</td>
</tr>
<tr>
<td>Slew Rate</td>
<td>SR</td>
<td>25°C</td>
<td>-</td>
<td>0.3</td>
<td>- V/μs C_{L}=25pF</td>
</tr>
<tr>
<td>Gain Bandwidth</td>
<td>GBW</td>
<td>25°C</td>
<td>-</td>
<td>0.6</td>
<td>- MHz C_{L}=25pF, A_{V}=40dB</td>
</tr>
<tr>
<td>Phase Margin</td>
<td>θ</td>
<td>25°C</td>
<td>-</td>
<td>50</td>
<td>- deg C_{L}=25pF, A_{V}=40dB</td>
</tr>
<tr>
<td>Total Harmonic Distortion + Noise</td>
<td>THD+N</td>
<td>25°C</td>
<td>-</td>
<td>0.05</td>
<td>- % OUT=1V_{P-P}, f=1kHz</td>
</tr>
</tbody>
</table>

(Note 7) Absolute value.

(Note 8) Full range: BD7541G: T_{A}=-40°C to +85°C BD7541SG: T_{A}=-40°C to +105°C.

(Note 9) Under the high temperature environment, consider the power dissipation of IC when selecting the output current.

When the terminal short circuits are continuously output, the output current is reduced to climb to the temperature inside IC.
## Electrical Characteristics – continued

**BD7542xxx / BD7542Sxxx (Unless otherwise specified VDD=+12V, VSS=0V, \( T_A=25^\circ\text{C} \))**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Temperature Range</th>
<th>Limit</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Offset Voltage</td>
<td>( V_{IO} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Full range</td>
<td>-</td>
<td>1</td>
<td>9</td>
<td>mV</td>
<td>VDD=5 to 14.5V</td>
</tr>
<tr>
<td>Input Offset Current</td>
<td>( I_{IO} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Input Bias Current</td>
<td>( I_{IB} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Supply Current</td>
<td>( I_{DD} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Full range</td>
<td>-</td>
<td>400</td>
<td>780</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Maximum Output Voltage (High)</td>
<td>( V_{OH} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Full range</td>
<td>-</td>
<td>400</td>
<td>780</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Maximum Output Voltage (Low)</td>
<td>( V_{OL} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Full range</td>
<td>-</td>
<td>850</td>
<td>25°C to 400</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Large Single Voltage Gain</td>
<td>( A_V )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Input Common-mode Voltage Range</td>
<td>( V_{ICM} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Common-mode Rejection Ratio</td>
<td>( CMRR )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Power Supply Rejection Ratio</td>
<td>( PSRR )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Output Source Current</td>
<td>( I_{SOURCE} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Output Sink Current</td>
<td>( I_{SINK} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Slew Rate</td>
<td>( SR )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Gain Bandwidth Product</td>
<td>( GBW )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Phase Margin</td>
<td>( \theta )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Total Harmonic Distortion + Noise</td>
<td>( \text{THD+N} )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Channel Separation</td>
<td>( CS )</td>
<td>( 25^\circ\text{C} )</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
</tbody>
</table>

(Note 10) Absolute value.

(Note 11) Full range: BD7542xxx: \( T_A=-40^\circ\text{C} \) to +85°C BD7542Sxxx: \( T_A=-40^\circ\text{C} \) to +105°C.

(Note 12) Under the high temperature environment, consider the power dissipation of IC when selecting the output current.

When the terminal short circuits are continuously output, the output current is reduced to climb to the temperature inside IC.
Description of Electrical Characteristics

Described below are descriptions of the relevant electrical terms used in this datasheet. Items and symbols used are also shown. Note that item name and symbol and their meaning may differ from those on another manufacturer’s document or general document.

1. Absolute maximum ratings

Absolute maximum rating items indicate the condition which must not be exceeded. Application of voltage in excess of absolute maximum rating or use out of absolute maximum rated temperature environment may cause deterioration of characteristics.

(1) Supply Voltage (VDD/VSS)
Indicates the maximum voltage that can be applied between the VDD terminal and VSS terminal without deterioration or destruction of characteristics of internal circuit.

(2) Differential Input Voltage (Vid)
Indicates the maximum voltage that can be applied between non-inverting and inverting terminals without damaging the IC.

(3) Input Common-mode Voltage Range (VCM)
Indicates the maximum voltage that can be applied to the non-inverting and inverting terminals without deterioration or destruction of electrical characteristics. Input common-mode voltage range of the maximum ratings does not assure normal operation of IC. For normal operation, use the IC within the input common-mode voltage range characteristics.

(4) Power Dissipation (PD)
Indicates the power that can be consumed by the IC when mounted on a specific board at the ambient temperature 25°C (normal temperature). As for package product, PD is determined by the temperature that can be permitted by the IC in the package (maximum junction temperature) and the thermal resistance of the package.

2. Electrical characteristics

(1) Input Offset Voltage (VIO)
Indicates the voltage difference between non-inverting terminal and inverting terminals. It can be translated into the input voltage difference required for setting the output voltage at 0 V.

(2) Input Offset Current (IO)
Indicates the difference of input bias current between the non-inverting and inverting terminals.

(3) Input Bias Current (IB)
Indicates the current that flows into or out of the input terminal. It is defined by the average of input bias currents at the non-inverting and inverting terminals.

(4) Supply Current (IDC)
Indicates the current that flows within the IC under specified no-load conditions.

(5) Maximum Output Voltage(High) / Maximum Output Voltage(Low) (VOH/VOH)
Indicates the voltage range of the output under specified load condition. It is typically divided into maximum output voltage high and low. Maximum output voltage high indicates the upper limit of output voltage. Maximum output voltage low indicates the lower limit.

(6) Large Signal Voltage Gain (AV)
Indicates the amplifying rate (gain) of output voltage against the voltage difference between non-inverting terminal and inverting terminal. It is normally the amplifying rate (gain) with reference to DC voltage.

\[
A_V = \frac{\text{Output voltage}}{\text{Differential Input voltage}}
\]

(7) Input Common-mode Voltage Range (VCM)
Indicates the input voltage range where IC normally operates.

(8) Common-mode Rejection Ratio (CMRR)
Indicates the ratio of fluctuation of input offset voltage when the input common mode voltage is changed. It is normally the fluctuation of DC.

\[
\text{CMRR} = \frac{\text{Change of Input common-mode voltage}}{\text{Input offset fluctuation}}
\]

(9) Power Supply Rejection Ratio (PSRR)
Indicates the ratio of fluctuation of input offset voltage when supply voltage is changed.

\[
\text{PSRR} = \frac{\text{Change of power supply voltage}}{\text{Input offset fluctuation}}
\]

(10) Output Source Current/ Output Sink Current (IOSOURCE / IOSINK)
The maximum current that can be output from the IC under specific output conditions. The output source current indicates the current flowing out from the IC, and the output sink current indicates the current flowing into the IC.

(11) Slew Rate (SR)
Indicates the ratio of the change in output voltage with time when a step input signal is applied.

(12) Gain Bandwidth (GBW)
The product of the open-loop voltage gain and the frequency at which the voltage gain decreases 6dB/octave.

(13) Phase Margin (θ)
Indicates the margin of phase from 180 degree phase lag at unity gain frequency.
Description of Electrical Characteristics - continued

(14) Total Harmonic Distortion + Noise (THD+N)
Indicates the fluctuation of input offset voltage or that of output voltage with reference to the change of output voltage of driven channel.

(15) Channel Separation (CS)
Indicates the fluctuation in the output voltage of the driven channel with reference to the change of output voltage of the channel which is not driven.
Typical Performance Curves

BD7541G, BD7541SG

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BD7541G: -40°C to +85°C  BD7541SG: -40°C to +105°C
Typical Performance Curves - continued

BD7541G BD7541SG BD7542xxx BD7542Sxxx

OBD7541G. BD7541SG

Figure 6.
Maximum Output Voltage (High) vs Supply Voltage
\((R_L=10k\Omega)\)

Figure 7.
Maximum Output Voltage (High) vs Ambient Temperature
\((R_L=10k\Omega)\)

Figure 8.
Maximum Output Voltage (Low) vs Supply Voltage
\((R_L=10k\Omega)\)

Figure 9.
Maximum Output Voltage (Low) vs Ambient Temperature
\((R_L=10k\Omega)\)

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BD7541G: -40°C to +85°C  BD7541SG: -40°C to +105°C
Typical Performance Curves - continued

OBD7541G, BD7541SG

Figure 10. Output Source Current vs Output Voltage (VDD=12V)
Figure 11. Output Source Current vs Ambient Temperature (OUT=VDD-0.4V)

Figure 12. Output Sink Current vs Output Voltage (VDD=12V)
Figure 13. Output Sink Current vs Ambient Temperature (OUT=VSS+0.4V)

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BD7541G: -40°C to +85°C BD7541SG: -40°C to +105°C
Typical Performance Curves - continued

BD7541G, BD7541SG

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BD7541G: -40°C to +85°C  BD7541SG: -40°C to +105°C
Typical Performance Curves - continued

BD7541G, BD7541SG

(*) The above characteristics are measurements of typical sample, they are not guaranteed.
BD7541G: -40°C to +85°C  BD7541SG: -40°C to +105°C
Typical Performance Curves - continued

OBD7541G, BD7541SG

(*) The above characteristics are measurements of typical sample, they are not guaranteed.
BD7541G: -40°C to +85°C  BD7541SG: -40°C to +105°C
Typical Performance Curves - continued

BD7542xxx, BD7542Sxxx

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

**BD7542xxx**: -40°C to +85°C

**BD7542Sxxx**: -40°C to +105°C
Typical Performance Curves - continued

BD7542xxx, BD7542Sxxx

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BD7542xxx: -40°C to +85°C  BD7542Sxxx: -40°C to +105°C
Typical Performance Curves - continued

BD7542xxx, BD7542Sxxx

Figure 33. Output Source Current vs Output Voltage (VDD=12V)

Figure 34. Output Source Current vs Ambient Temperature (OUT=VDD-0.4V)

Figure 35. Output Sink Current vs Output Voltage (VDD=12V)

Figure 36. Output Sink Current vs Ambient Temperature (OUT=VSS+0.4V)

(*) The above characteristics are measurements of typical sample, they are not guaranteed.
BD7542xxx: -40°C to +85°C  BD7542Sxxx: -40°C to +105°C
Typical Performance Curves - continued

OBD7542xxx, BD7542Sxxx

(*) The above characteristics are measurements of typical sample, they are not guaranteed.
BD7542xxx: -40°C to +85°C  BD7542Sxxx: -40°C to +105°C
Typical Performance Curves - continued

BD7542xxx, BD7542Sxxx

1. Large Signal Voltage Gain vs Ambient Temperature

2. Common Mode Rejection Ratio vs Supply Voltage (VDD=12V)

3. Common Mode Rejection Ratio vs Ambient Temperature (VDD=12V)

4. Power Supply Rejection Ratio vs Ambient Temperature

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BD7542xxx: -40°C to +85°C  BD7542Sxxx: -40°C to +105°C
Typical Performance Curves - continued

○ BD7542xxx, BD7542Sxxx

(*) The above characteristics are measurements of typical sample, they are not guaranteed.
BD7542xxx: -40°C to +85°C   BD7542Sxxx: -40°C to +105°C
Application Information

NULL method condition for Test Circuit 1

<table>
<thead>
<tr>
<th>Parameter</th>
<th>( V_F )</th>
<th>SW1</th>
<th>SW2</th>
<th>SW3</th>
<th>VDD</th>
<th>VSS</th>
<th>( E_K )</th>
<th>( V_{ICM} )</th>
<th>Calculation</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Offset Voltage</td>
<td>( V_{F1} )</td>
<td>ON</td>
<td>ON</td>
<td>OFF</td>
<td>12</td>
<td>0</td>
<td>-6</td>
<td>12</td>
<td>1</td>
</tr>
<tr>
<td>Large Signal Voltage Gain</td>
<td>( V_{F2} )</td>
<td>ON</td>
<td>ON</td>
<td>ON</td>
<td>12</td>
<td>0</td>
<td>-0.5</td>
<td>6</td>
<td>2</td>
</tr>
<tr>
<td>Common-Mode Rejection Ratio (Input Common-Mode Voltage Range)</td>
<td>( V_{F4} )</td>
<td>ON</td>
<td>ON</td>
<td>OFF</td>
<td>12</td>
<td>0</td>
<td>-6</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>Power Supply Rejection Ratio</td>
<td>( V_{F6} )</td>
<td>ON</td>
<td>ON</td>
<td>OFF</td>
<td>5</td>
<td>14.5</td>
<td>-2.5</td>
<td>0</td>
<td>4</td>
</tr>
</tbody>
</table>

**Figure 48. Test Circuit 1 (one channel only)**

\[ V_{IO} = \frac{|V_{F1}|}{1 + R_S/R_F} \text{ [V]} \]

\[ A_v = 20 \log \frac{\Delta E_K \times (1+R_I/R_S)}{|V_{F3} - V_{F2}|} \text{ [dB]} \]

\[ CMRR = 20 \log \frac{\Delta V_{ICM} \times (1+R_F/R_S)}{|V_{F5} - V_{F4}|} \text{ [dB]} \]

\[ PSRR = 20 \log \frac{\Delta V_{DD} \times (1+R_L/R_S)}{|V_{F7} - V_{F6}|} \text{ [dB]} \]
Switch Condition for Test Circuit 2

<table>
<thead>
<tr>
<th>SW No.</th>
<th>SW 1</th>
<th>SW 2</th>
<th>SW 3</th>
<th>SW 4</th>
<th>SW 5</th>
<th>SW 6</th>
<th>SW 7</th>
<th>SW 8</th>
<th>SW 9</th>
<th>SW 10</th>
<th>SW 11</th>
<th>SW 12</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Current</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>Maximum Output Voltage (R_L=10kΩ)</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>Output Current</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>Slew Rate</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
</tr>
<tr>
<td>Gain Bandwidth</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
</tr>
</tbody>
</table>

![Diagram of Test Circuit 2](image1)

Figure 49. Test Circuit 2 (each channel)

![Diagram of Test Circuit 3](image2)

Figure 50. Slew Rate Input Output Wave

![Diagram of Test Circuit 4](image3)

Figure 51. Test Circuit 3 (Channel Separation)
Examples of Circuit

○ Voltage Follower

![Voltage Follower Circuit](image1)

Voltage gain is 0dB.

Using this circuit, the output voltage (OUT) is configured to be equal to the input voltage (IN). This circuit also stabilizes the output voltage (OUT) due to high input impedance and low output impedance. Computation for output voltage (OUT) is shown below.

\[ \text{OUT} = \text{IN} \]

○ Inverting Amplifier

![Inverting Amplifier Circuit](image2)

For inverting amplifier, input voltage (IN) is amplified by a voltage gain and depends on the ratio of R1 and R2. The out-of-phase output voltage is shown in the next expression.

\[ \text{OUT} = -\left(\frac{R_2}{R_1}\right) \cdot \text{IN} \]

This circuit has input impedance equal to R1.

○ Non-inverting Amplifier

![Non-inverting Amplifier Circuit](image3)

For non-inverting amplifier, input voltage (IN) is amplified by a voltage gain, which depends on the ratio of R1 and R2. The output voltage (OUT) is in-phase with the input voltage (IN) and is shown in the next expression.

\[ \text{OUT} = \left(1 + \frac{R_2}{R_1}\right) \cdot \text{IN} \]

Effectively, this circuit has high input impedance since its input side is the same as that of the operational amplifier.
Power Dissipation

Power dissipation (total loss) indicates the power that the IC can consume at $T_A=25^\circ C$ (normal temperature). As the IC consumes power, it heats up, causing its temperature to be higher than the ambient temperature. The allowable temperature that the IC can accept is limited. This depends on the circuit configuration, manufacturing process, and consumable power.

Power dissipation is determined by the allowable temperature within the IC (maximum junction temperature) and the thermal resistance of the package used (heat dissipation capability). Maximum junction temperature is typically equal to the maximum storage temperature. The heat generated through the consumption of power by the IC radiates from the mold resin or lead frame of the package. Thermal resistance, represented by the symbol $\theta_{JA}$ °C/W, indicates this heat dissipation capability. Similarly, the temperature of an IC inside its package can be estimated by thermal resistance.

Figure 55(a) shows the model of the thermal resistance of a package. The equation below shows how to compute for the Thermal resistance ($\theta_{JA}$), given the ambient temperature ($T_A$), maximum junction temperature ($T_{J,max}$), and power dissipation ($P_D$).

$$\theta_{JA} = \frac{(T_{J,max} - T_A)}{P_D} \ \degree C/W$$

The Derating curve in Figure 55(b) indicates the power that the IC can consume with reference to ambient temperature. Power consumption of the IC begins to attenuate at certain temperatures. This gradient is determined by Thermal resistance ($\theta_{JA}$), which depends on the chip size, power consumption, package, ambient temperature, package condition, wind velocity, etc. This may also vary even when the same of package is used. Thermal reduction curve indicates a reference value measured at a specified condition. Figure 55(c) to (f) shows an example of the derating curve for BD7541G, BD7541SG, BD7542xxx, and BD7542Sxxx.

---

Figure 55. Thermal Resistance and Derating Curve
Power Dissipation - continued

<table>
<thead>
<tr>
<th>(Note 13)</th>
<th>(Note 14)</th>
<th>(Note 15)</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>5.4</td>
<td>5.5</td>
<td>4.7</td>
<td>mW/°C</td>
</tr>
</tbody>
</table>

When using the unit above \( T_a \leq 25^\circ \text{C} \), subtract the value above per Celsius degree. Power dissipation is the value when FR4 glass epoxy board 70mm × 70mm × 1.6mm (copper foil area less than 3%) is mounted.

Figure 55. Thermal Resistance and Derating Curve
Operational Notes

1. **Reverse Connection of Power Supply**
   Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC’s power supply pins.

2. **Power Supply Lines**
   Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. **Ground Voltage**
   Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. **Ground Wiring Pattern**
   When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. **Thermal Consideration**
   Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. The absolute maximum rating of the $P_D$ stated in this specification is when the IC is mounted on a 70mm x 70mm x 1.6mm glass epoxy board. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the $P_D$ rating.

6. **Recommended Operating Conditions**
   These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

7. **Inrush Current**
   When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

8. **Operation Under Strong Electromagnetic Field**
   Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

9. **Testing on Application Boards**
   When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC’s power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

10. **Inter-pin Short and Mounting Errors**
    Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

11. **Unused Input Pins**
    Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.
12. **Regarding the Input Pin of the IC**
   In the construction of this IC, P-N junctions are inevitably formed creating parasitic diodes or transistors. The operation of these parasitic elements can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions which cause these parasitic elements to operate, such as applying a voltage to an input pin lower than the ground voltage should be avoided. Furthermore, do not apply a voltage to the input pins when no power supply voltage is applied to the IC. Even if the power supply voltage is applied, make sure that the input pins have voltages within the values specified in the electrical characteristics of this IC.

13. **Unused circuits**
   When there are unused op-amps, it is recommended that they are connected as in Figure 56, setting the non-inverting input terminal to a potential within the in-phase input voltage range ($V_{ICM}$).

14. **Input Voltage**
   Applying $V_{DD}+0.3\,\text{V}$ to the input terminal is possible without causing deterioration of the electrical characteristics or destruction, regardless of the supply voltage. However, this does not ensure normal circuit operation. Please note that the circuit operates normally only when the input voltage is within the common mode input voltage range of the electric characteristics.

15. **Power Supply (single/dual)**
   The operational amplifiers operate when the voltage supplied is between $V_{DD}$ and $V_{SS}$. Therefore, the single supply operational amplifiers can be used as dual supply operational amplifiers as well.

16. **Output Capacitor**
   If a large capacitor is connected between the output pin and $V_{SS}$ pin, current from the charged capacitor will flow into the output pin and may destroy the IC when the $V_{DD}$ pin is shorted to ground or pulled down to 0V. Use a capacitor smaller than 0.1µF between output pin and $V_{SS}$ pin.

17. **Oscillation by Output Capacitor**
   Please pay attention to the oscillation by output capacitor and in designing an application of negative feedback loop circuit with these ICs.

18. **Latch up**
   Be careful of input voltage that exceed the $V_{DD}$ and $V_{SS}$. When CMOS device have sometimes occur latch up and protect the IC from abnormal noise.
### Physical Dimension, Tape and Reel Information

<table>
<thead>
<tr>
<th>Package Name</th>
<th>SSOP5</th>
</tr>
</thead>
</table>

#### Physical Dimensions

![Physical Dimension Diagram]

**UNIT:** mm  
**PKG:** SSOP5  
**Drawing No.:** EX106-5001-2

#### Tape and Reel Information

<table>
<thead>
<tr>
<th>Tape</th>
<th>Embossed carrier tape</th>
</tr>
</thead>
<tbody>
<tr>
<td>Quantity</td>
<td>3000 pcs</td>
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</tbody>
</table>

**Direction of feed:** TR  
*The direction is the 1 pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand.*
Physical Dimension Tape and Reel Information – continued

<table>
<thead>
<tr>
<th>Package Name</th>
<th>SOP8</th>
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**<Tape and Reel Information>**

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<tr>
<th>Tape</th>
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</tr>
</thead>
<tbody>
<tr>
<td>Quantity</td>
<td>2500pcs</td>
</tr>
<tr>
<td>Direction of feed</td>
<td>E2</td>
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</tbody>
</table>

The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand.

Order quantity needs to be multiple of the minimum quantity.
Physical Dimension Tape and Reel Information – continued

<table>
<thead>
<tr>
<th>Package Name</th>
<th>MSOP8</th>
</tr>
</thead>
</table>

![Diagram of MSOP8 package](image)

<sup单位:mm</sup>

**Drawing No. EX181-5002**

### Tape and Reel Information

<table>
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<th>Tape</th>
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</thead>
<tbody>
<tr>
<td>Quantity</td>
<td>3000pcs</td>
</tr>
</tbody>
</table>

**Direction of feed**

- **TR**: The direction of the 1pin of product is at the upper right when you hold the reel on the left hand and you pull out the tape on the right hand.

**1pin**

Order quantity needs to be a multiple of the minimum quantity.
### Marking Diagram

#### SSOP5 (TOP VIEW)
- **Part Number Marking**
- **LOT Number**

#### SOP8 (TOP VIEW)
- **Part Number Marking**
- **LOT Number**

#### MSOP8 (TOP VIEW)
- **Part Number Marking**
- **LOT Number**

### Product Name

<table>
<thead>
<tr>
<th>Product Name</th>
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<tbody>
<tr>
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<td>SSOP5</td>
</tr>
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<td>BD7541S</td>
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<td></td>
<td>FVM</td>
<td>MSOP8</td>
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<td>BD7542F</td>
<td>F</td>
<td>SOP8</td>
</tr>
<tr>
<td></td>
<td>FVM</td>
<td>MSOP8</td>
</tr>
<tr>
<td>BD7542S</td>
<td>F</td>
<td>SOP8</td>
</tr>
<tr>
<td></td>
<td>FVM</td>
<td>MSOP8</td>
</tr>
</tbody>
</table>

### Land Pattern Data

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<th>Land length</th>
<th>Land width</th>
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</thead>
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<td>0.6</td>
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<tr>
<td>SOP8</td>
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<td>4.60</td>
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<tr>
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<td>0.65</td>
<td>2.62</td>
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All dimensions in mm
Revision History

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<th>Date</th>
<th>Revision</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>20. Feb. 2015</td>
<td>002</td>
<td>Correction of Figure number (Page. 23 Power Dissipation)</td>
</tr>
</tbody>
</table>
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1. Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM’s Products for Specific Applications.

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   - [h] Use of the Products in places subject to dew condensation

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6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

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   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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