**Operational Amplifiers**

**Input/Output Full Swing**

**Low Supply Current**

**CMOS Operational Amplifiers**

**BU7241G  BU7241SG  BU7242xxx  BU7242Sxxx  BU7244xx  BU7244Sxx**

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**General Description**

BU7241G, BU7242xxx, BU7244xx are CMOS operational amplifiers of the input/output full swing low voltage operation. Also, BU7241SG, BU7242Sxxx, BU7244Sxx which expanded the operating temperature range perform a lineup. It is most suitable for a sensor amplifier and a battery-powered equipment to have a low input bias current, the characteristic of the low supply current.

**Features**

- Operable with Low Voltage
- Operable Input-Output Full Swing
- Wide Temperature Range
- Low Supply Current
- Low Input Bias Current

**Applications**

- Sensor Amplifier
- Battery Powered Equipment
- Portable Equipment
- Consumer Equipment

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**Key Specifications**

- **Operating Supply Voltage Range:** +1.8V to +5.5V
- **Operating Temperature Range:**
  - BU7241G, BU7242xxx, BU7244xx: -40°C to +85°C
  - BU7241SG, BU7242Sxxx, BU7244Sxx: -40°C to +105°C
- **Supply Current:**
  - BU7241, BU7241S: 70µA (Typ)
  - BU7242, BU7242S: 180µA (Typ)
  - BU7244, BU7244S: 360µA (Typ)
- **Input Offset Current:** 1pA (Typ)
- **Input Bias Current:** 1pA (Typ)

**Packages**

- SSOP5: 2.90mm x 2.80mm x 1.25mm
- SOP8: 5.00mm x 6.20mm x 1.71mm
- MSOP8: 2.90mm x 4.00mm x 0.90mm
- VSON008X2030: 2.00mm x 3.00mm x 0.60mm
- SOP14: 8.70mm x 6.20mm x 1.71mm
- SSOP-B14: 5.00mm x 6.40mm x 1.35mm

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**Simplified Schematic**

![Simplified Schematic](https://example.com/schematic.png)

Figure 1. Simplified Schematic (1ch Only)

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OP: Product structure: Silicon monolithic integrated circuit

O: This product has no designed protection against radioactive rays.
Pin Configuration

BU7241G, BU7241SG : SSOP5

BU7241G, BU7241SG : SSOP5

BU7242F, BU7242SF : SOP8
BU7242FVM, BU7242SFVM : MSOP8
BU7242NUX, BU7242SNUX : VSON008X2030

BU7242F, BU7242SF : SOP8
BU7242FVM, BU7242SFVM : MSOP8
BU7242NUX, BU7242SNUX : VSON008X2030

BU7242F, BU7242SF : SOP8
BU7242FVM, BU7242SFVM : MSOP8
BU7242NUX, BU7242SNUX : VSON008X2030

BU7244F, BU7244SF : SOP14
BU7244FV, BU7244SFV : SSOP-B14

BU7244F, BU7244SF : SOP14
BU7244FV, BU7244SFV : SSOP-B14

Package

<table>
<thead>
<tr>
<th>SSOP5</th>
<th>SOP8</th>
<th>VSON008X2030</th>
<th>MSOP8</th>
<th>SOP14</th>
<th>SSOP-B14</th>
</tr>
</thead>
<tbody>
<tr>
<td>BU7241G</td>
<td>BU7242F</td>
<td>BU7242NUX</td>
<td>BU7242FVM</td>
<td>BU7244F</td>
<td>BU7244FV</td>
</tr>
<tr>
<td>BU7241SG</td>
<td>BU7242SF</td>
<td>BU7242SNUX</td>
<td>BU7242SFVM</td>
<td>BU7244SF</td>
<td>BU7244SFV</td>
</tr>
</tbody>
</table>
Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
<th>Packaging and forming specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>BU7241G</td>
<td>G : SSOP5</td>
<td>E2: Embossed tape and reel (SOP8/SOP14/SSOP-B14)</td>
</tr>
<tr>
<td>BU7241SG</td>
<td>F : SOP8, SOP14</td>
<td>(SSOP5/MSOP8/VSON008X2030)</td>
</tr>
<tr>
<td>BU7242xxx</td>
<td>FV : SSOP-B14</td>
<td>TR: Embossed tape and reel</td>
</tr>
<tr>
<td>BU7242Sxxx</td>
<td>FVM : MSOP8</td>
<td></td>
</tr>
<tr>
<td>BU7244xx</td>
<td>NUX : VSON008X2030</td>
<td></td>
</tr>
<tr>
<td>BU7244Sxxx</td>
<td></td>
<td></td>
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</table>

Line-up

<table>
<thead>
<tr>
<th>Operating Temperature Range</th>
<th>Channels</th>
<th>Package</th>
<th>Orderable Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>-40°C to +85°C</td>
<td>1ch</td>
<td>SSOP5</td>
<td>Reel of 3000</td>
</tr>
<tr>
<td></td>
<td></td>
<td>SOP8</td>
<td>Reel of 2500</td>
</tr>
<tr>
<td></td>
<td></td>
<td>MSOP8</td>
<td>Reel of 3000</td>
</tr>
<tr>
<td></td>
<td></td>
<td>VSON008X2030</td>
<td>Reel of 4000</td>
</tr>
<tr>
<td></td>
<td>2ch</td>
<td>SOP14</td>
<td>Reel of 2500</td>
</tr>
<tr>
<td></td>
<td></td>
<td>SSOP-B14</td>
<td>Reel of 2500</td>
</tr>
<tr>
<td>-40°C to +105°C</td>
<td>1ch</td>
<td>SSOP5</td>
<td>Reel of 3000</td>
</tr>
<tr>
<td></td>
<td></td>
<td>SOP8</td>
<td>Reel of 2500</td>
</tr>
<tr>
<td></td>
<td></td>
<td>MSOP8</td>
<td>Reel of 3000</td>
</tr>
<tr>
<td></td>
<td></td>
<td>VSON008X2030</td>
<td>Reel of 4000</td>
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<tr>
<td></td>
<td>2ch</td>
<td>SOP14</td>
<td>Reel of 2500</td>
</tr>
<tr>
<td></td>
<td></td>
<td>SSOP-B14</td>
<td>Reel of 2500</td>
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### Absolute Maximum Ratings (TA=25°C)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>BU7241G</th>
<th>BU7242xxx</th>
<th>BU7244xxx</th>
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</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>VDD-VSS</td>
<td>+7</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>PDP</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>BU7241G</td>
<td>SSOP5</td>
<td>0.54(Note 1,7)</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>SOP8</td>
<td>0.55(Note 2,7)</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>MSOP8</td>
<td>0.47(Note 3,7)</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>VSON008X2030</td>
<td>0.41(Note 4,7)</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>SOP14</td>
<td>0.45(Note 5,7)</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>SSOP-B14</td>
<td>0.70(Note 6,7)</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>Differential Input Voltage (Note 8)</td>
<td>VID</td>
<td>VDD - VSS</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Input Common-mode Voltage</td>
<td>VCM</td>
<td>(VSS - 0.3) to (VDD + 0.3)</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Input Current (Note 9)</td>
<td>I</td>
<td>±10</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>Operating Supply Voltage Range</td>
<td>Vopr</td>
<td>+1.8 to +5.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>Topr</td>
<td>-40 to +85</td>
<td>ºC</td>
<td></td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>Styg</td>
<td>-55 to +125</td>
<td>ºC</td>
<td></td>
</tr>
<tr>
<td>Maximum Junction Temperature</td>
<td>Tjmax</td>
<td>+125</td>
<td>ºC</td>
<td></td>
</tr>
</tbody>
</table>

(Note 1)  To use at temperature above TA=25°C reduce 5.4mW/°C.
(Note 2)  To use at temperature above TA=25°C reduce 5.5mW/°C.
(Note 3)  To use at temperature above TA=25°C reduce 4.7mW/°C.
(Note 4)  To use at temperature above TA=25°C reduce 4.1mW/°C.
(Note 5)  To use at temperature above TA=25°C reduce 4.5mW/°C.
(Note 6)  To use at temperature above TA=25°C reduce 4.1mW/°C.
(Note 7)  Mounted on a FR4 glass epoxy PCB 70mm×70mm×1.6mm (Copper foil area less than 3%).
(Note 8)  The voltage difference between inverting input and non-inverting input is the differential input voltage.
Then input pin voltage is set to more than VSS.
(Note 9)  An excessive input current will flow when input voltages of more than VDD+0.6V or less than VSS-0.6V are applied.
The input current can be set to less than the rated current by adding a limiting resistor.
Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.
### Absolute Maximum Ratings (T_A=25°C) - continued

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>BU7241SG</th>
<th>BU7242Sxxx</th>
<th>BU7244Sxxx</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Supply Voltage</strong></td>
<td>VDD-VSS</td>
<td>+7</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td><strong>Power Dissipation</strong></td>
<td>P_D</td>
<td></td>
<td></td>
<td></td>
<td>W</td>
</tr>
<tr>
<td>SSOP5</td>
<td></td>
<td>0.54(^{\text{Note 10,16}})</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>SOP8</td>
<td></td>
<td>-</td>
<td>0.55(^{\text{Note 11,16}})</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>MSOP8</td>
<td></td>
<td>-</td>
<td>0.47(^{\text{Note 12,16}})</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>VSON008X2030</td>
<td></td>
<td>-</td>
<td>0.41(^{\text{Note 13,16}})</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>SOP14</td>
<td></td>
<td>-</td>
<td>-</td>
<td>0.45(^{\text{Note 14,16}})</td>
<td></td>
</tr>
<tr>
<td>SSOP-B14</td>
<td></td>
<td>0.70(^{\text{Note 15,16}})</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Differential Input Voltage</strong>(^{\text{Note 17}})</td>
<td>V_ID</td>
<td>VDD - VSS</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td><strong>Input Common-mode Voltage</strong></td>
<td>V_ICM</td>
<td>(VSS - 0.3) to (VDD + 0.3)</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td><strong>Input Current</strong>(^{\text{Note 18}})</td>
<td>I_I</td>
<td>±10</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td><strong>Operating Supply Voltage Range</strong></td>
<td>V_opr</td>
<td>+1.8 to +5.5</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>T喷</td>
<td>-40 to +105</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td><strong>Storage Temperature Range</strong></td>
<td>T_STG</td>
<td>-55 to +125</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td><strong>Maximum Junction Temperature</strong></td>
<td>T_Jmax</td>
<td>+125</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
</tbody>
</table>

\(^{\text{Note 10}}\) To use at temperature above T_A=25°C reduce 5.4mW/°C.

\(^{\text{Note 11}}\) To use at temperature above T_A=25°C reduce 5.5mW/°C.

\(^{\text{Note 12}}\) To use at temperature above T_A=25°C reduce 4.7mW/°C.

\(^{\text{Note 13}}\) To use at temperature above T_A=25°C reduce 4.1mW/°C.

\(^{\text{Note 14}}\) To use at temperature above T_A=25°C reduce 4.5mW/°C.

\(^{\text{Note 15}}\) To use at temperature above T_A=25°C reduce 7.0mW/°C.

\(^{\text{Note 16}}\) Mounted on a FR4 glass epoxy PCB 70mm×70mm×1.6mm (Copper foil area less than 3%).

\(^{\text{Note 17}}\) The voltage difference between inverting input and non-inverting input is the differential input voltage. Then input pin voltage is set to more than VSS.

\(^{\text{Note 18}}\) An excessive input current will flow when input voltages of more than VDD+0.6V or less than VSS-0.6V are applied. The input current can be set to less than the rated current by adding a limiting resistor.

**Caution:** Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.
### Electrical Characteristics

OBU7241 (Unless otherwise specified VDD=+3V, VSS=0V, $T_A=25^\circ$C)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Temperature Range</th>
<th>Limit</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Offset Voltage (Note 19, 20)</td>
<td>$V_{IO}$</td>
<td>25$^\circ$C</td>
<td>1</td>
<td>9</td>
<td>mV VDD=1.8 to 5.5V</td>
</tr>
<tr>
<td>Input Offset Current (Note 19)</td>
<td>$I_{IO}$</td>
<td>25$^\circ$C</td>
<td>1</td>
<td>-</td>
<td>pA</td>
</tr>
<tr>
<td>Input Bias Current (Note 19)</td>
<td>$I_{IB}$</td>
<td>25$^\circ$C</td>
<td>1</td>
<td>-</td>
<td>pA</td>
</tr>
<tr>
<td>Supply Current (Note 20)</td>
<td>$I_{DD}$</td>
<td>25$^\circ$C</td>
<td>70</td>
<td>150</td>
<td>$\mu$A $R_L=\infty$, $A_V=0$dB $IN+=1.5$V</td>
</tr>
<tr>
<td>Maximum Output Voltage(High)</td>
<td>$V_{OH}$</td>
<td>25$^\circ$C</td>
<td>-</td>
<td>- VD-0.1</td>
<td>- V $R_L=10$kΩ</td>
</tr>
<tr>
<td>Maximum Output Voltage(Low)</td>
<td>$V_{OL}$</td>
<td>25$^\circ$C</td>
<td>-</td>
<td>VSS+0.1</td>
<td>V $R_L=10$kΩ</td>
</tr>
<tr>
<td>Large Signal Voltage Gain</td>
<td>$A_V$</td>
<td>25$^\circ$C</td>
<td>70</td>
<td>95</td>
<td>dB $R_L=10$kΩ</td>
</tr>
<tr>
<td>Input Common-mode Voltage Range</td>
<td>$V_{ICM}$</td>
<td>25$^\circ$C</td>
<td>0</td>
<td>3 V VSS to VDD</td>
<td></td>
</tr>
<tr>
<td>Common-mode Rejection Ratio</td>
<td>CMRR</td>
<td>25$^\circ$C</td>
<td>45</td>
<td>60</td>
<td>dB</td>
</tr>
<tr>
<td>Power Supply Rejection Ratio</td>
<td>PSRR</td>
<td>25$^\circ$C</td>
<td>60</td>
<td>80</td>
<td>dB</td>
</tr>
<tr>
<td>Output Source Current (Note 21)</td>
<td>$I_{SOURCE}$</td>
<td>25$^\circ$C</td>
<td>4</td>
<td>10</td>
<td>mA $OUT=VD-0.4$V</td>
</tr>
<tr>
<td>Output Sink Current (Note 21)</td>
<td>$I_{SINK}$</td>
<td>25$^\circ$C</td>
<td>5</td>
<td>12</td>
<td>mA $OUT=VSS+0.4$V</td>
</tr>
<tr>
<td>Slew Rate</td>
<td>SR</td>
<td>25$^\circ$C</td>
<td>-</td>
<td>0.4 - V/µs</td>
<td>$C_L=25$pF</td>
</tr>
<tr>
<td>Gain Bandwidth</td>
<td>GBW</td>
<td>25$^\circ$C</td>
<td>-</td>
<td>0.9 - MHz</td>
<td>$C_L=25$pF, $A_V=40$dB</td>
</tr>
<tr>
<td>Phase Margin</td>
<td>$\theta$</td>
<td>25$^\circ$C</td>
<td>-</td>
<td>50 - deg</td>
<td>$C_L=25$pF, $A_V=40$dB</td>
</tr>
<tr>
<td>Total Harmonic Distortion + Noise</td>
<td>THD+N</td>
<td>25$^\circ$C</td>
<td>-</td>
<td>0.05 - %</td>
<td>$OUT=0.8V_{PP}$, f=1kHz</td>
</tr>
</tbody>
</table>

(Note 19) Absolute value

(Notes 20) Full range: BU7241: $T_A=-40$ to +85$^\circ$C BU7241S: $T_A=-40$ to +105$^\circ$C

(Notes 21) Under the high temperature environment, consider the power dissipation of IC when selecting the output current.

When the terminal short circuits are continuously output, the output current is reduced to climb to the temperature inside IC.
## Electrical Characteristics - continued

OBU7242 (Unless otherwise specified \( VDD=+3V, VSS=0V, T_A=25\degree C \))

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Temperature Range</th>
<th>Limit</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>(Note 22, 23)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Offset Voltage</td>
<td>( V_{IO} ) 25\degree C</td>
<td>-</td>
<td>1</td>
<td>9</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Full range</td>
<td>-</td>
<td>10</td>
<td>-</td>
</tr>
<tr>
<td>Input Offset Current</td>
<td>( I_{IO} ) 25\degree C</td>
<td>-</td>
<td>1</td>
<td>-</td>
<td>pA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Full range</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Input Bias Current</td>
<td>( I_{IB} ) 25\degree C</td>
<td>-</td>
<td>1</td>
<td>-</td>
<td>pA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Full range</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Supply Current</td>
<td>( I_{DO} ) 25\degree C</td>
<td>-</td>
<td>180</td>
<td>360</td>
<td>( \mu A )</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Full range</td>
<td>-</td>
<td>-</td>
<td>600</td>
</tr>
<tr>
<td>Maximum Output Voltage (High)</td>
<td>( V_{OH} ) 25\degree C</td>
<td>VDD-0.1</td>
<td>-</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>Maximum Output Voltage (Low)</td>
<td>( V_{OL} ) 25\degree C</td>
<td>-</td>
<td>VSS+0.1</td>
<td>V</td>
<td>( R_L=10k\Omega )</td>
</tr>
<tr>
<td>Large Signal Voltage Gain</td>
<td>( A_v ) 25\degree C</td>
<td>70</td>
<td>95</td>
<td>-</td>
<td>dB</td>
</tr>
<tr>
<td>Input Common-mode Voltage Range</td>
<td>( V_{ICM} ) 25\degree C</td>
<td>0</td>
<td>-</td>
<td>3</td>
<td>V</td>
</tr>
<tr>
<td>Common-mode Rejection Ratio</td>
<td>CMRR 25\degree C</td>
<td>45</td>
<td>60</td>
<td>-</td>
<td>dB</td>
</tr>
<tr>
<td>Power Supply Rejection Ratio</td>
<td>PSRR 25\degree C</td>
<td>60</td>
<td>80</td>
<td>-</td>
<td>dB</td>
</tr>
<tr>
<td>Output Source Current (Note 24)</td>
<td>( I_{SOURCE} ) 25\degree C</td>
<td>4</td>
<td>10</td>
<td>-</td>
<td>mA</td>
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<tr>
<td>Output Sink Current (Note 24)</td>
<td>( I_{SINK} ) 25\degree C</td>
<td>5</td>
<td>12</td>
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<td>mA</td>
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<tr>
<td>Slew Rate</td>
<td>( S_R ) 25\degree C</td>
<td>-</td>
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<td>-</td>
<td>V/\mu s</td>
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<tr>
<td>Gain Bandwidth</td>
<td>GBW 25\degree C</td>
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<td>-</td>
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<tr>
<td>Phase Margin</td>
<td>( \theta ) 25\degree C</td>
<td>-</td>
<td>50</td>
<td>-</td>
<td>deg</td>
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<tr>
<td>Total Harmonic Distortion + Noise</td>
<td>THD+N 25\degree C</td>
<td>-</td>
<td>0.05</td>
<td>-</td>
<td>%</td>
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<tr>
<td>Channel Separation</td>
<td>CS 25\degree C</td>
<td>-</td>
<td>100</td>
<td>-</td>
<td>dB</td>
</tr>
</tbody>
</table>

(Note 22) Absolute value

(Note 23) Full range: BU7242: \( T_{A}=-40\degree C \) to +85\degree C \ BU7242S: \( T_{A}=-40\degree C \) to +105\degree C

(Note 24) Under the high temperature environment, consider the power dissipation of IC when selecting the output current.

When the terminal short circuits are continuously output, the output current is reduced to climb to the temperature inside IC.
### Electrical Characteristics - continued

OBU7244 (Unless otherwise specified VDD=+3V, VSS=0V, TA=25°C)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Temperature Range</th>
<th>Limit</th>
<th>Unit</th>
<th>Conditions</th>
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<td>Min</td>
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<td>Max</td>
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<td>Input Offset Voltage</td>
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<td></td>
<td></td>
<td>Full range</td>
<td>-</td>
<td>-</td>
<td>10</td>
</tr>
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<td>Input Offset Current</td>
<td>IO</td>
<td>25°C</td>
<td>-</td>
<td>1</td>
<td>-</td>
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<td>Input Bias Current</td>
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<td>1</td>
<td>-</td>
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<td>Supply Current</td>
<td>IS</td>
<td>25°C</td>
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<td>360</td>
<td>750</td>
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<td>-</td>
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<td>Maximum Output Voltage(High)</td>
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<td>-</td>
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<td>Maximum Output Voltage(Low)</td>
<td>VOL</td>
<td>25°C</td>
<td>VSS+0.1</td>
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<td>-</td>
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<tr>
<td>Large Signal Voltage Gain</td>
<td>AV</td>
<td>25°C</td>
<td>70</td>
<td>95</td>
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<td>Input Common-mode Voltage Range</td>
<td>VCM</td>
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<td>Common-mode Rejection Ratio</td>
<td>CMRR</td>
<td>25°C</td>
<td>45</td>
<td>60</td>
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<tr>
<td>Power Supply Rejection Ratio</td>
<td>PSRR</td>
<td>25°C</td>
<td>60</td>
<td>80</td>
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<td>10</td>
<td>-</td>
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<tr>
<td>Output Sink Current</td>
<td>IOS</td>
<td>25°C</td>
<td>5</td>
<td>12</td>
<td>-</td>
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<tr>
<td>Slew Rate</td>
<td>SR</td>
<td>25°C</td>
<td>-</td>
<td>0.4</td>
<td>-</td>
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<tr>
<td>Gain Bandwidth</td>
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<td>25°C</td>
<td>-</td>
<td>0.9</td>
<td>-</td>
</tr>
<tr>
<td>Phase Margin</td>
<td>θ</td>
<td>25°C</td>
<td>-</td>
<td>50</td>
<td>-</td>
</tr>
<tr>
<td>Total Harmonic Distortion + Noise</td>
<td>THD+N</td>
<td>25°C</td>
<td>-</td>
<td>0.05</td>
<td>-</td>
</tr>
<tr>
<td>Channel Separation</td>
<td>CS</td>
<td>25°C</td>
<td>-</td>
<td>100</td>
<td>-</td>
</tr>
</tbody>
</table>

(Note 25) Absolute value
(Note 26) Full range: BU7244: TA=-40°C to +85°C BU7244S: TA=-40°C to +105°C
(Note 27) Under the high temperature environment, consider the power dissipation of IC when selecting the output current.

When the terminal short circuits are continuously output, the output current is reduced to climb to the temperature inside IC.
Description of electrical characteristics
Described below are descriptions of the relevant electrical terms used in this datasheet. Items and symbols used are also shown. Note that item name and symbol and their meaning may differ from those on another manufacturer’s document or general document.

1. Absolute maximum ratings
Absolute maximum rating items indicate the condition which must not be exceeded. Application of voltage in excess of absolute maximum rating or use out of absolute maximum rated temperature environment may cause deterioration of characteristics.

(1) Supply Voltage (VDD/VSS)
Indicates the maximum voltage that can be applied between the VDD terminal and VSS terminal without deterioration or destruction of characteristics of internal circuit.

(2) Differential Input Voltage (Vin)
Indicates the maximum voltage that can be applied between non-inverting and inverting terminals without damaging the IC.

(3) Input Common-mode Voltage Range (Vicm)
Indicates the maximum voltage that can be applied to the non-inverting and inverting terminals without deterioration or destruction of electrical characteristics. Input common-mode voltage range of the maximum ratings does not assure normal operation of IC. For normal operation, use the IC within the input common-mode voltage range characteristics.

(4) Power Dissipation (Pd)
Indicates the power that can be consumed by the IC when mounted on a specific board at the ambient temperature 25°C (normal temperature). As for package product, Pd is determined by the temperature that can be permitted by the IC in the package (maximum junction temperature) and the thermal resistance of the package.

2. Electrical characteristics

(1) Input Offset Voltage (Vio)
Indicates the voltage difference between non-inverting terminal and inverting terminals. It can be translated into the input voltage difference required for setting the output voltage at 0 V.

(2) Input Offset Current (Iio)
Indicates the difference of input bias current between the non-inverting and inverting terminals.

(3) Input Bias Current (Ibi)
Indicates the current that flows into or out of the input terminal. It is defined by the average of input bias currents at the non-inverting and inverting terminals.

(4) Supply Current (Is)
Indicates the current that flows within the IC under specified no-load conditions.

(5) Maximum Output Voltage(High) / Maximum Output Voltage(Low) (Voh/Vol)
Indicates the voltage range of the output under specified load condition. It is typically divided into maximum output voltage high and low. Maximum output voltage high indicates the upper limit of output voltage. Maximum output voltage low indicates the lower limit.

(6) Large Signal Voltage Gain (AV)
Indicates the amplifying rate (gain) of output voltage against the voltage difference between non-inverting terminal and inverting terminal. It is normally the amplifying rate (gain) with reference to DC voltage.
\[ Av = \frac{Output\ voltage}{(Differential\ Input\ voltage)} \]

(7) Input Common-mode Voltage Range (Vicm)
Indicates the input voltage range where IC normally operates.

(8) Common-mode Rejection Ratio (CMRR)
Indicates the ratio of fluctuation of input offset voltage when the input common mode voltage is changed. It is normally the fluctuation of DC.
\[ CMRR = \frac{Change\ of\ Input\ common-mode\ voltage}{(Input\ offset\ fluctuation)} \]

(9) Power Supply Rejection Ratio (PSRR)
Indicates the ratio of fluctuation of input offset voltage when supply voltage is changed. It is normally the fluctuation of DC.
\[ PSRR = \frac{Change\ of\ power\ supply\ voltage}{(Input\ offset\ fluctuation)} \]

(10) Output Source Current / Output Sink Current (Isource/Isink)
The maximum current that can be output under specific output conditions, it is divided into output source current and output sink current. The output source current indicates the current flowing out of the IC, and the output sink current the current flowing into the IC.

(11) Slew Rate (SR)
Indicates the ratio of the change in output voltage with time when a step input signal is applied.

(12) Gain Bandwidth (GBW)
The product of the open-loop voltage gain and the frequency at which the voltage gain decreases 6dB/octave.
(13) Phase Margin (θ)
Indicates the margin of phase from 180 degree phase lag at unity gain frequency.

(14) Total Harmonic Distortion + Noise (THD+N)
Indicates the fluctuation of input offset voltage or that of output voltage with reference to the change of output voltage of driven channel.

(15) Channel Separation (CS)
Indicates the fluctuation in the output voltage of the driven channel with reference to the change of output voltage of the channel which is not driven.
Typical Performance Curves
OBU7241G, BU7241SG

Figure 2.
Power Dissipation vs Ambient Temperature
(Derating Curve)

Figure 3.
Power Dissipation vs Ambient Temperature
(Derating Curve)

Figure 4.
Supply Current vs Supply Voltage

Figure 5.
Supply Current vs Ambient Temperature

(*)The above characteristics are measurements of typical sample, they are not guaranteed.
BU7241G: -40°C to +85°C  BU7241SG: -40°C to +105°C
Typical Performance Curves - Continued
OBU7241G, BU7241SG

Figure 6. Maximum Output Voltage (High) vs Supply Voltage
(R_L=10kΩ)

Figure 7. Maximum Output Voltage (High) vs Ambient Temperature
(R_L=10kΩ)

Figure 8. Maximum Output Voltage (Low) vs Supply Voltage
(R_L=10kΩ)

Figure 9. Maximum Output Voltage (Low) vs Ambient Temperature
(R_L=10kΩ)

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BU7241G: -40°C to +85°C  BU7241SG: -40°C to +105°C
Typical Performance Curves - Continued

BU7241G, BU7241SG

Figure 10.
Output Source Current vs Output Voltage
(VDD=3V)

Figure 11.
Output Source Current vs Ambient Temperature
(OUT=VDD-0.4V)

Figure 12.
Output Sink Current vs Output Voltage
(VDD=3V)

Figure 13.
Output Sink Current vs Ambient Temperature
(OUT=VSS+0.4V)

(*)The above characteristics are measurements of typical sample, they are not guaranteed.
BU7241G: -40°C to +85°C  BU7241SG: -40°C to +105°C
Typical Performance Curves - Continued

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(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BU7241G: -40°C to +85°C  BU7241SG: -40°C to +105°C
Typical Performance Curves - Continued

**BU7241G, BU7241SG**

![Diagram](image1)

**Figure 18.**
Large Signal Voltage Gain vs Ambient Temperature

![Diagram](image2)

**Figure 19.**
Common Mode Rejection Ratio vs Supply Voltage

![Diagram](image3)

**Figure 20.**
Common Mode Rejection Ratio vs Ambient Temperature

![Diagram](image4)

**Figure 21.**
Power Supply Rejection Ratio vs Ambient Temperature

(*)The above characteristics are measurements of typical sample, they are not guaranteed.

BU7241G: -40°C to +85°C  BU7241SG: -40°C to +105°C
Typical Performance Curves - Continued

OBU7241G, BU7241SG

Figure 22.
Slew Rate L-H vs Ambient Temperature

Figure 23.
Slew Rate H-L vs Ambient Temperature

Figure 24.
Voltage Gain / Phase vs Frequency

(*) The above characteristics are measurements of typical sample, they are not guaranteed.
BU7241G: -40°C to +85°C  BU7241SG: -40°C to +105°C
Typical Performance Curves – Continued
OBU7242xxx, BU7242Sxxx

Figure 25.
Power Dissipation vs Ambient Temperature
(Derating Curve)

Figure 26.
Power Dissipation vs Ambient Temperature
(Derating Curve)

Figure 27.
Supply Current vs Supply Voltage

Figure 28.
Supply Current vs Ambient Temperature

(*)The above characteristics are measurements of typical sample, they are not guaranteed.
BU7242xxx: -40°C to +85°C  BU7242Sxxx: -40°C to +105°C
Typical Performance Curves – Continued

BU7242xxx, BU7242Sxxx

![Graph of Maximum Output Voltage (High) vs Supply Voltage](image1)

Figure 29. Maximum Output Voltage (High) vs Supply Voltage ($R_L=10k\Omega$)

![Graph of Maximum Output Voltage (High) vs Ambient Temperature](image2)

Figure 30. Maximum Output Voltage (High) vs Ambient Temperature ($R_L=10k\Omega$)

![Graph of Maximum Output Voltage (Low) vs Supply Voltage](image3)

Figure 31. Maximum Output Voltage (Low) vs Supply Voltage ($R_L=10k\Omega$)

![Graph of Maximum Output Voltage (Low) vs Ambient Temperature](image4)

Figure 32. Maximum Output Voltage (Low) vs Ambient Temperature ($R_L=10k\Omega$)

(*)The above characteristics are measurements of typical sample, they are not guaranteed.

BU7242xxx: -40°C to +85°C  BU7242Sxxx: -40°C to +105°C

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Typical Performance Curves – Continued

Figure 33. Output Source Current vs Output Voltage (VDD=3V)

Figure 34. Output Source Current vs Ambient Temperature (OUT=VDD-0.4V)

Figure 35. Output Sink Current vs Output Voltage (VDD=3V)

Figure 36. Output Sink Current vs Ambient Temperature (OUT=VSS+0.4V)

(*)The above characteristics are measurements of typical sample, they are not guaranteed.

BU7242xxx: -40°C to +85°C  BU7242Sxxx: -40°C to +105°C
Typical Performance Curves – Continued

BU7242xxx, BU7242Sxxx

(*)The above characteristics are measurements of typical sample, they are not guaranteed.

BU7242xxx: -40°C to +85°C  BU7242Sxxx: -40°C to +105°C
Typical Performance Curves – Continued

- Figure 41. Large Signal Voltage Gain vs Ambient Temperature
- Figure 42. Common Mode Rejection Ratio vs Supply Voltage (VDD=3V)
- Figure 43. Common Mode Rejection Ratio vs Ambient Temperature (VDD=3V)
- Figure 44. Power Supply Rejection Ratio vs Ambient Temperature

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BU7242xxx: -40°C to +85°C  BU7242Sxxx: -40°C to +105°C
Typical Performance Curves – Continued

OBU7242xxx, BU7242Sxxx

<table>
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<tr>
<th>Ambient Temperature [°C]</th>
<th>Slew Rate L-H [V/μs]</th>
<th>Slew Rate H-L [V/μs]</th>
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<th>Voltage Gain [dB]</th>
<th>Phase [deg]</th>
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(*) The above characteristics are measurements of typical sample, they are not guaranteed.
BU7242xxx: -40°C to +85°C
BU7242Sxxx: -40°C to +105°C

Figure 45. Slew Rate L-H vs Ambient Temperature
Figure 46. Slew Rate H-L vs Ambient Temperature
Figure 47. Voltage Gain / Phase vs Frequency
Typical Performance Curves – Continued

OBU7244xx, BU7244Sxx

Figure 48.
Power Dissipation vs Ambient Temperature
(Derating Curve)

Figure 49.
Power Dissipation vs Ambient Temperature
(Derating Curve)

Figure 50.
Supply Current vs Supply Voltage

Figure 51.
Supply Current vs Ambient Temperature

(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BU7244xx: -40°C to +85°C  BU7244Sxx: -40°C to +105°C
Typical Performance Curves – Continued

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(*) The above characteristics are measurements of typical sample, they are not guaranteed.

BU7244xx: -40°C to +85°C  BU7244Sxx: -40°C to +105°C
Typical Performance Curves – Continued

OBU7244xx, BU7244Sxx

Figure 56. Output Source Current vs Output Voltage (VDD=3V)

Figure 57. Output Source Current vs Ambient Temperature (OUT=VDD-0.4V)

Figure 58. Output Sink Current vs Output Voltage (VDD=3V)

Figure 59. Output Sink Current vs Ambient Temperature (OUT=VSS+0.4V)

(*)The above characteristics are measurements of typical sample, they are not guaranteed.
BU7244xx: -40°C to +85°C  BU7244Sxx: -40°C to +105°C
Typical Performance Curves – Continued
OBU7244xx, BU7244Sxx

Figure 60.
Input Offset Voltage vs Supply Voltage
\((V_{ICM}=VDD, E_K=-VDD/2)\)

Figure 61.
Input Offset Voltage vs Ambient Temperature
\((V_{ICM}=VDD, E_K=-VDD/2)\)

Figure 62.
Input Offset Voltage vs Input Voltage
\((VDD=3V)\)

Figure 63.
Large Signal Voltage Gain vs Supply Voltage

\(^{(*)}\)The above characteristics are measurements of typical sample, they are not guaranteed.

BU7244xx: -40°C to +85°C  BU7244Sxx: -40°C to +105°C
Typical Performance Curves – Continued

OBU7244xx, BU7244Sxx

(*) The above characteristics are measurements of typical sample, they are not guaranteed.
BU7244xx: -40°C to +85°C
BU7244Sxx: -40°C to +105°C
Typical Performance Curves – Continued

OBU7244xx, BU7244Sxx

Figure 68. Slew Rate L-H vs Ambient Temperature

Figure 69. Slew Rate H-L vs Ambient Temperature

Figure 70. Voltage Gain / Phase vs Frequency

(*)The above characteristics are measurements of typical sample, they are not guaranteed.
BU7244xx: -40°C to +85°C  BU7244Sxx: -40°C to +105°C
Application Information

NULL method condition for Test Circuit 1

<table>
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<tr>
<th>Parameter</th>
<th>( V_F )</th>
<th>S1</th>
<th>S2</th>
<th>S3</th>
<th>VDD</th>
<th>VSS</th>
<th>( E_K )</th>
<th>( V_{ICM} )</th>
<th>Calculation</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Offset Voltage</td>
<td>( V_{F1} )</td>
<td>ON</td>
<td>ON</td>
<td>OFF</td>
<td>3</td>
<td>0</td>
<td>-1.5</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>Large Signal Voltage Gain</td>
<td>( V_{F2} )</td>
<td>ON</td>
<td>ON</td>
<td>ON</td>
<td>3</td>
<td>0</td>
<td>-0.5</td>
<td>-2.5</td>
<td>2</td>
</tr>
<tr>
<td>Common-mode Rejection Ratio (Input Common-mode Voltage Range)</td>
<td>( V_{F5} )</td>
<td>ON</td>
<td>ON</td>
<td>OFF</td>
<td>3</td>
<td>0</td>
<td>-1.5</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>Power Supply Rejection Ratio</td>
<td>( V_{F6} )</td>
<td>ON</td>
<td>ON</td>
<td>OFF</td>
<td>1.8</td>
<td>5.5</td>
<td>0</td>
<td>-0.9</td>
<td>4</td>
</tr>
</tbody>
</table>

- Calculation -

1. Input Offset Voltage (\( V_{IO} \))

\[
V_{IO} = \frac{|V_{F1}|}{1+R_F/R_S} \quad [V]
\]

2. Large Signal Voltage Gain (\( A_V \))

\[
A_V = 20 \log \frac{\Delta E_K \times (1+R_F/R_S)}{|V_{F2}-V_{F3}|} \quad [\text{dB}]
\]

3. Common-mode Rejection Ratio (CMRR)

\[
CMRR = 20 \log \frac{\Delta V_{ICM} \times (1+R_F/R_S)}{|V_{F4}-V_{F5}|} \quad [\text{dB}]
\]

4. Power Supply Rejection Ratio (PSRR)

\[
PSRR = 20 \log \frac{\Delta V_{DD} \times (1+R_F/R_S)}{|V_{F6}-V_{F7}|} \quad [\text{dB}]
\]

Figure 71. Test Circuit 1 (One Channel Only)
## Application Information - continued

### Switch Condition for Test Circuit 2

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<tr>
<th>SW No.</th>
<th>SW1</th>
<th>SW2</th>
<th>SW3</th>
<th>SW4</th>
<th>SW5</th>
<th>SW6</th>
<th>SW7</th>
<th>SW8</th>
<th>SW9</th>
<th>SW10</th>
<th>SW11</th>
<th>SW12</th>
</tr>
</thead>
<tbody>
<tr>
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<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>Maximum Output Voltage $R_L=10k\Omega$</td>
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<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
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<td>Output Current</td>
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<td>ON</td>
<td>ON</td>
</tr>
<tr>
<td>Gain Bandwidth</td>
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<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
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<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
</tr>
</tbody>
</table>

![Test Circuit 2](image)

**Figure 72.** Test Circuit 2 (each channel)

### Input Voltage

<table>
<thead>
<tr>
<th>Voltage</th>
<th>0 V</th>
<th>3 V</th>
</tr>
</thead>
</table>

**Input Wave**

### Output Voltage

<table>
<thead>
<tr>
<th>Voltage</th>
<th>0 V</th>
<th>3 V</th>
</tr>
</thead>
</table>

**Output Wave**

**SR = $\Delta V / \Delta t$**

**Figure 73.** Slew Rate Input and Output Wave

![Test Circuit 3](image)

**Figure 74.** Test Circuit 3 (Channel Separation)
Examples of Circuit

○Voltage Follower

Voltage gain is 0dB.

Using this circuit, the output voltage (OUT) is configured to be equal to the input voltage (IN). This circuit also stabilizes the output voltage (OUT) due to high input impedance and low output impedance. Computation for output voltage (OUT) is shown below.

\[ \text{OUT} = \text{IN} \]

Figure 75. Voltage Follower Circuit

○Inverting Amplifier

For inverting amplifier, input voltage (IN) is amplified by a voltage gain and depends on the ratio of R1 and R2. The out-of-phase output voltage is shown in the next expression

\[ \text{OUT} = -(\frac{\text{R2}}{\text{R1}}) \cdot \text{IN} \]

This circuit has input impedance equal to R1.

Figure 76. Inverting Amplifier Circuit

○Non-inverting Amplifier

For non-inverting amplifier, input voltage (IN) is amplified by a voltage gain, which depends on the ratio of R1 and R2. The output voltage (OUT) is in-phase with the input voltage (IN) and is shown in the next expression.

\[ \text{OUT} = (1 + \frac{\text{R2}}{\text{R1}}) \cdot \text{IN} \]

Effectively, this circuit has high input impedance since its input side is the same as that of the operational amplifier.

Figure 77. Non-inverting Amplifier Circuit
Power Dissipation

Power dissipation (total loss) indicates the power that the IC can consume at $T_A=25^\circ\text{C}$ (normal temperature). As the IC consumes power, it heats up, causing its temperature to be higher than the ambient temperature. The allowable temperature that the IC can accept is limited. This depends on the circuit configuration, manufacturing process, and consumable power.

Power dissipation is determined by the allowable temperature within the IC (maximum junction temperature) and the thermal resistance of the package used (heat dissipation capability). Maximum junction temperature is typically equal to the maximum storage temperature. The heat generated through the consumption of power by the IC radiates from the mold resin or lead frame of the package. Thermal resistance, represented by the symbol $\theta_{JA}\text{°C/W}$, indicates this heat dissipation capability. Similarly, the temperature of an IC inside its package can be estimated by thermal resistance.

Figure 78(a) shows the model of the thermal resistance of a package. The equation below shows how to compute for the Thermal resistance ($\theta_{JA}$), given the ambient temperature ($T_A$), maximum junction temperature ($T_{J\text{max}}$), and power dissipation ($P_D$).

$$\theta_{JA} = \frac{(T_{J\text{max}} - T_A)}{P_D}\text{°C/W}$$

The Derating curve in Figure 78 (b) indicates the power that the IC can consume with reference to ambient temperature. Power consumption of the IC begins to attenuate at certain temperatures. This gradient is determined by Thermal resistance ($\theta_{JA}$), which depends on the chip size, power consumption, package, ambient temperature, package condition, wind velocity, etc. This may also vary even when the same of package is used. Thermal reduction curve indicates a reference value measured at a specified condition. Figure 78(c) to 78(h) shows an example of the derating curve for BU7241G, BU7241SG, BU7242xx, BU7242Sxx, BU7244xx, BU7244Sxx.
When using the unit above \( T_a = 25^\circ C \), subtract the value above per Celsius degree. Power dissipation is the value when FR4 glass epoxy board 70mm \( \times \) 70mm \( \times \) 1.6mm (copper foil area less than 3%) is mounted.

Figure 78. Thermal Resistance and Derating Curve
Operational Notes

1. **Reverse Connection of Power Supply**
   Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC’s power supply pins.

2. **Power Supply Lines**
   Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. **Ground Voltage**
   Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. **Ground Wiring Pattern**
   When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. **Thermal Consideration**
   Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. The absolute maximum rating of the $P_D$ stated in this specification is when the IC is mounted on a 70mm x 70mm x 1.6mm glass epoxy board. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the $P_D$ rating.

6. **Recommended Operating Conditions**
   These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

7. **Inrush Current**
   When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

8. **Operation Under Strong Electromagnetic Field**
   Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

9. **Testing on Application Boards**
   When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC’s power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

10. **Inter-pin Short and Mounting Errors**
    Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

11. **Unused Input Pins**
    Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.
Operational Notes – continued

12. Regarding the Input Pin of the IC
   In the construction of this IC, P-N junctions are inevitably formed creating parasitic diodes or transistors. The operation of these parasitic elements can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions which cause these parasitic elements to operate, such as applying a voltage to an input pin lower than the ground voltage should be avoided. Furthermore, do not apply a voltage to the input pins when no power supply voltage is applied to the IC. Even if the power supply voltage is applied, make sure that the input pins have voltages within the values specified in the electrical characteristics of this IC.

13. Unused Circuits
   When there are unused op-amps, it is recommended that they are connected as in Figure 79, setting the non-inverting input terminal to a potential within the Input Common-mode Voltage Range (V_{ICM}).

14. Input Voltage
   Applying VDD+0.3V to the input terminal is possible without causing deterioration of the electrical characteristics or destruction. However, this does not ensure normal circuit operation. Please note that the circuit operates normally only when the input voltage is within the common mode input voltage range of the electric characteristics.

15. Power Supply (single/dual)
   The operational amplifier operates when the voltage supplied is between VDD and VSS. Therefore, the single supply operational amplifiers can be used as dual supply operational amplifiers as well.

16. Output Capacitor
   If a large capacitor is connected between the output pin and VSS pin, current from the charged capacitor will flow into the output pin and may destroy the IC when the VDD pin is shorted to ground or pulled down to 0V. Use a capacitor smaller than 0.1uF between output pin and VSS pin.

17. Oscillation by Output Capacitor
   Please pay attention to the oscillation by output capacitor and in designing an application of negative feedback loop circuit with these ICs.

18. Latch up
   Be careful of input voltage that exceed the VDD and VSS. When CMOS device have sometimes occur latch up and protect the IC from abnormaly noise.

19. Decoupling Capacitor
   Insert the decoupling capacitance between VDD and VSS, for stable operation of operational amplifier.

20. Radiation Land
   The VSON008X2030 package has a radiation land in the center of the back. Please connect to VSS potential or don’t connect to other terminal.
### Marking Diagram

#### SSOP5 (TOP VIEW)
- **Part Number Marking**
- **LOT Number**

#### SOP8 (TOP VIEW)
- **Part Number Marking**
- **LOT Number**
- **1PIN MARK**

#### MSOP8 (TOP VIEW)
- **Part Number Marking**
- **LOT Number**
- **1PIN MARK**

#### VSON008X2030 (TOP VIEW)
- **Part Number Marking**
- **LOT Number**
- **1PIN MARK**

#### SOP14 (TOP VIEW)
- **Part Number Marking**
- **LOT Number**
- **1PIN MARK**

#### SSOP-B14 (TOP VIEW)
- **Part Number Marking**
- **LOT Number**
- **1PIN MARK**

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<thead>
<tr>
<th>Product Name</th>
<th>Package Type</th>
<th>Marking</th>
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<tbody>
<tr>
<td>BU7241</td>
<td>G</td>
<td>SSOP5 (AK)</td>
</tr>
<tr>
<td>BU7241S</td>
<td>G</td>
<td>SOP5 (AW)</td>
</tr>
<tr>
<td>BU7242</td>
<td>F</td>
<td>SOP8 (FVM 7242)</td>
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<tr>
<td></td>
<td>F</td>
<td>MSOP8 (NUX VSON008X2030)</td>
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<tr>
<td>BU7242S</td>
<td>F</td>
<td>SOP8 (FVM 7242S)</td>
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<td>MSOP8 (NUX VSON008X2030)</td>
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<tr>
<td>BU7244S</td>
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## Physical Dimension, Tape and Reel Information

<table>
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<tr>
<th>Package Name</th>
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![Physical Dimensions Diagram](image)

< UNIT: \( \text{mm} \)>

PKG: SSOP5

Drawing No. EX106-5001-2

---

### Tape and Reel Information

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<th>Description</th>
<th>Details</th>
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<td>Tape</td>
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<tr>
<td>Quantity</td>
<td>3000pcs</td>
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<tr>
<td>Direction of feed</td>
<td>TR</td>
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<td><a href="image">The direction is the 1pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand</a></td>
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</tbody>
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![Tape and Reel Diagram](image)
### Physical Dimension, Tape and Reel Information - continued

<table>
<thead>
<tr>
<th>Package Name</th>
<th>SOP8</th>
</tr>
</thead>
</table>

#### Tape and Reel Information

- **Tape**: Embossed carrier tape
- **Quantity**: 2500pcs

**Direction of feed**

- The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand.

**Order quantity needs to be multiple of the minimum quantity.**
Physical Dimension, Tape and Reel Information – continued

<table>
<thead>
<tr>
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![Diagram of SOP14 package](image)

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<tbody>
<tr>
<td>Quantity</td>
<td>2500 pcs</td>
</tr>
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</table>

**Direction of feed**

1. **E2**
   - The direction of the tape is the 1pin of the product at the upper left when you hold the reel on the left hand and pull out the tape on the right hand.

**Reel**

- 1pin
- Order quantity needs to be multiple of the minimum quantity.

(UNIT : mm)
PKG : SOP14
Drawing No. : EX113-5001

(Max 9.05 (Include BURR))

---

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TSZ22111-15-001

TSZ02201-ORAR0G200240-1-2
03.Dec.2014 Rev.003
Physical Dimension, Tape and Reel Information – continued

Package Name: SSOP-B14

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**Direction of feed**
- The direction is the 1pin of product is at the upper left when you hold the reel on the left hand and you pull out the tape on the right hand.

**Reel**
- Order quantity needs to be multiple of the minimum quantity.
Physical Dimension, Tape and Reel Information - continued

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![Diagrams](https://via.placeholder.com/150)

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<tr>
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<td>Direction of feed</td>
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<tr>
<td>The direction is the 1pin of product is at the upper right when you hold the reel on the left hand and you pull out the tape on the right hand</td>
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</tbody>
</table>

<Notes>
*Order quantity needs to be multiple of the minimum quantity.*

**BU7241G** **BU7241SG** **BU7242xxx** **BU7242Sxxx** **BU7244xx** **BU7244Sxx** Datasheet

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TSZ22111-15-001
TSZ02201-0RAR0G200240-1-2
03.Dec.2014 Rev.003

41/44
Physical Dimension, Tape and Reel Information – continued

<table>
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<tr>
<th>Package Name</th>
<th>VSON008X2030</th>
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![Diagram of package dimensions](image)

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<tbody>
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<td>Quantity</td>
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Direction of feed

TR

The direction is the 1pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand

![Diagram of reel and direction](image)

*Order quantity needs to be multiple of the minimum quantity,
Land Pattern Data

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SSOP5

VSON008X2030

SOP8, MSOP8, SOP14, SSOP-B14

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<td>18.Apr.2014</td>
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<td>Addition of BU7244FV,BU7244SFV in the Pin Configuration (Page 2.)</td>
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<tr>
<td>03.Dec.2014</td>
<td>003</td>
<td>Modify the Title of Figure. 7,9,30,34,54,55</td>
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(Note1) Medical Equipment Classification of the Specific Applications

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<th>EU</th>
<th>CHINA</th>
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<td>CLASS III</td>
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<tr>
<td>CLASS IV</td>
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<td>CLASS III</td>
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</tbody>
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[b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
[c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
[d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
[e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
[f] Sealing or coating our Products with resin or other coating materials
[g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
[h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification
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2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation
1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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