

Isolator Series

Isolation Voltage 2500 Vrms Bidirectional 3ch Digital Isolator

BM79413F-LBZ

General Description

This product is a rank product for the industrial equipment market. This is the best product for use in these applications.

BM79413F-LBZ is 3ch Digital Isolator with isolation element, Isolation Voltage 2500 Vrms, Propagation Delay 16 ns (Typ).

Features

- With Isolation Element
- With UVLO
- Max Data Rate 25 Mbps
- CMTI 75 kV/μs(Min)
- UL1577 (pending) Isolation Voltage 2500 Vrms

Applications

- Industrial Equipment
- Commercial Power Supply
- PLC
- Motor Control Systems
- PV Inverter
- Logic Signal Propagation in Industrial Equipment

Key Specifications

- Supply Voltage (VDD1): 2.7 V to 5.5 V
- Supply Voltage (VDD2): 2.7 V to 5.5 V
- Supply Current (VDD1): 5.0 mA (3.3 V, Typ)
- Supply Current (VDD2): 7.0 mA (3.3 V, Typ)
- Propagation Delay: 16 ns (3.3 V, Typ)
- CMTI: 75 kV/μs (Min)
- Operating Temperature Range: -40 °C to +125 °C

Package

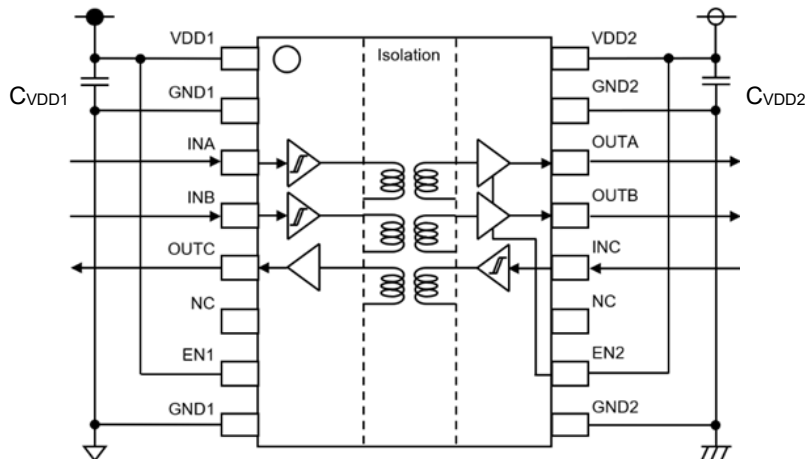
SOP16WA

W (Typ) x D (Typ) x H (Max)

10.3 mm x 10.3 mm x 2.65 mm



Typical Application Circuit



Recommended Range of External Constants

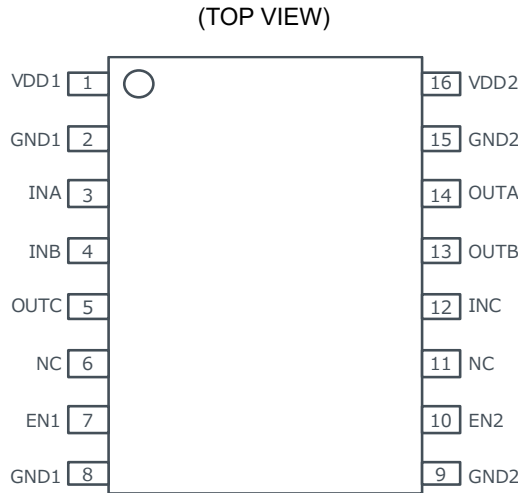
Pin Name	Symbol	value			Unit
		MIN	TYP	MAX	
VDD1	CVDD1	0.01	0.1	-	μF
VDD2	CVDD2	0.01	0.1	-	μF

○Product structure : Silicon integrated circuit ○This product has no designed protection against radioactive rays.

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Pin Configuration



Pin Descriptions

Pin No.	Pin Name	Function
1	VDD1	Power supply pin side 1 <i>(Note 1)</i>
2	GND1	Ground pin side 1
3	INA	Input channel A
4	INB	Input channel B
5	OUTC	Output channel C
6	NC	Not connected
7	EN1	Enable pin side 1
8	GND1	Ground pin side 1
9	GND2	Ground pin side 2
10	EN2	Enable pin side 2
11	NC	Not connected
12	INC	Input channel C
13	OUTB	Output channel B
14	OUTA	Output channel A
15	GND2	Ground pin side 2
16	VDD2	Power supply pin side 2 <i>(Note 2)</i>

(Note 1) Place a bypass capacitor near the VDD1-GND1 pins.

(Note 2) Place a bypass capacitor near the VDD2-GND2 pins.

Description of Operations

Truth Table

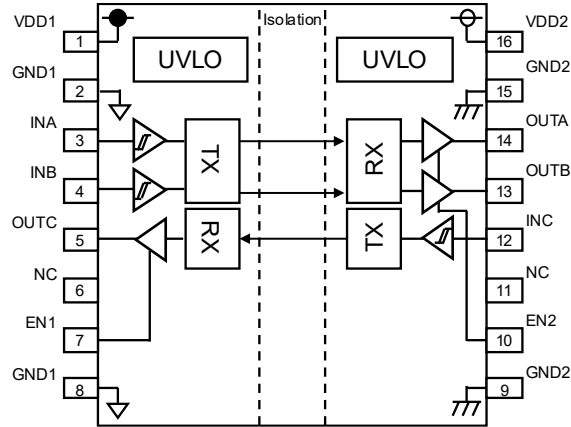
V_{DDI} <i>(Note 3)</i>	V_{DDO} <i>(Note 3)</i>	V_{INx} INPUT <i>(Note 3, 4)</i>	V_{ENx} INPUT <i>(Note 3, 4)</i>	V_{OUTx} OUTPUT <i>(Note 3, 4)</i>	Comments
Powered	Powered	L	H or NC	L	Normal
		H	H or NC	H	Normal
		X	L	Z	Output disabled
Unpowered	Powered	X	H or NC	L	Fail-safe output When V_{DDI} transitions from unpowered to powered, V_{OUTx} returns to the logic state of the input.
		X	L	Z	Output disabled
Powered	Unpowered	X	X	Undetermined	When V_{DDO} transitions from unpowered to powered, V_{OUTx} returns to the logic state of the input.

(Note 3) V_{DDI} and V_{DDO} are supply voltages in input and output side of each channel. V_{INx} and V_{OUTx} are input and output signal of each channel (A, B, C).

V_{ENx} is Enable Signal on the same side as V_{OUTx} .

(Note 4) L = "Low" level, H = "High" level, X = don't care, NC = Not connected, Z = High impedance

Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply Voltage Side 1	V _{DD1}	7.0	V
Supply Voltage Side 2	V _{DD2}	7.0	V
Input Pin Voltage Side 1	V _{IN1}	-0.3 to V _{DD1} +0.3 or 7.0	V
Input Pin Voltage Side 2	V _{IN2}	-0.3 to V _{DD2} +0.3 or 7.0	V
Maximum Junction Temperature	T _{jmax}	150	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Caution 1: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

Caution 2: Should by any chance the maximum junction temperature rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, design a PCB with thermal resistance taken into consideration by increasing board size and copper area so as not to exceed the maximum junction temperature rating.

Thermal Resistance (Note 5)

Parameter	Symbol	Thermal Resistance (Typ)		Unit
		1s <small>(Note 7)</small>	2s2p <small>(Note 8)</small>	
SOP16WA				
Junction to Ambient	θ _{JA}	158.3	89.9	°C/W
Junction to Top Characterization Parameter <small>(Note 6)</small>	Ψ _{JT}	50.0	44.0	°C/W

(Note 5) Based on JESD51-2A (Still-Air).

(Note 6) The thermal characterization parameter to report the difference between junction temperature and the temperature at the top center of the outside surface of the component package.

(Note 7) Using a PCB board based on JESD51-3.

(Note 8) Using a PCB board based on JESD51-7.

Layer Number of Measurement Board	Material	Board Size
Single	FR-4	114.3 mm x 76.2 mm x 1.57 mmt

Top	
Copper Pattern	Thickness
Footprints and Traces	70 μm

Layer Number of Measurement Board	Material	Board Size
4 Layers	FR-4	114.3 mm x 76.2 mm x 1.6 mmt

Top		2 Internal Layers		Bottom	
Copper Pattern	Thickness	Copper Pattern	Thickness	Copper Pattern	Thickness
Footprints and Traces	70 μm	74.2 mm x 74.2 mm	35 μm	74.2 mm x 74.2 mm	70 μm

Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
Supply Voltage Side 1	V _{DD1}	2.7	3.3	5.5	V
Supply Voltage Side 2	V _{DD2}	2.7	3.3	5.5	V
Operating Temperature	T _{opr}	-40	+25	+125	°C
UVLO Positive Threshold	V _{UVP}	-	2.3	2.6	V
UVLO Negative Threshold	V _{UVN}	1.6	2.0	-	V
UVLO Hysteresis	V _{UVHY}	-	0.3	-	V

Insulation Specifications UL1577 (pending)

Parameter	Symbol	Rating	Unit
Insulation Withstand Voltage / 1 min	V _{ISO1}	2500	Vrms
Insulation Test Voltage / 1 s	V _{ISO2}	3000	Vrms

Electrical Characteristics 5 V Supply

Unless otherwise specified, $T_a = -40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$, $V_{DD} = V_{DD1} = V_{DD2} = 4.5\text{ V}$ to 5.5 V

The typical value is defined at $T_a = 25\text{ }^{\circ}\text{C}$ and $V_{DD} = V_{DD1} = V_{DD2} = 5.0\text{ V}$

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Power Supply Specifications						
Supply Current 1 Side 1	I_{CC1Q}	-	2.6	4.0	mA	$V_{IN} = 0\text{ V}$ or V_{DD1} $C_L = 15\text{ pF}$
Supply Current 1 Side 2	I_{CC2Q}	-	4.2	6.5	mA	$V_{IN} = 0\text{ V}$ or V_{DD2} $C_L = 15\text{ pF}$
Supply Current 2 Side 1	I_{CC1A}	-	7.0	10.0	mA	$D_R = 25\text{ Mbps}$ $C_L = 15\text{ pF}$
Supply Current 2 Side 2	I_{CC2A}	-	9.0	13.0	mA	$D_R = 25\text{ Mbps}$ $C_L = 15\text{ pF}$
DC Specifications						
H Input Voltage	V_{IH}	$0.7V_{DD}$	-	-	V	
L Input Voltage	V_{IL}	-	-	$0.3V_{DD}$	V	
Low Input Current	I_{IL1}	-10	-	0	μA	$V_{DD1} = V_{DD2} = 5.0\text{ V}$ $V_{IN} = 0\text{ V}$ INA, INB, INC pin
	I_{IL2}	-20	-	0	μA	$V_{DD1} = V_{DD2} = 5.0\text{ V}$ $V_{IN} = 0\text{ V}$ EN1, EN2 pin
High Input Current	I_{IH1}	0	-	20	μA	$V_{DD1} = V_{DD2} = 5.0\text{ V}$ $V_{IN} = 5.0\text{ V}$ INA, INB, INC pin
	I_{IH2}	0	-	10	μA	$V_{DD1} = V_{DD2} = 5.0\text{ V}$ $V_{IN} = 5.0\text{ V}$ EN1, EN2 pin
High Output Voltage 1	V_{OH1}	$V_{DD} - 0.1$	V_{DD}	-	V	$I_{SOURCE} = -20\text{ }\mu\text{A}$
High Output Voltage 2	V_{OH2}	$V_{DD} - 0.4$	$V_{DD} - 0.2$	-	V	$I_{SOURCE} = -4\text{ mA}$
Low Output Voltage 1	V_{OL1}	-	0.0	0.1	V	$I_{SINK} = 20\text{ }\mu\text{A}$
Low Output Voltage 2	V_{OL2}	-	0.2	0.4	V	$I_{SINK} = 4\text{ mA}$
Switching Specifications						
Data Rate	D_R	0	-	25	Mbps	$C_L = 15\text{ pF}$
Pulse Width	t_{PW}	40	-	-	ns	$C_L = 15\text{ pF}$
Propagation Delay (Low to High)	t_{PLH}	-	15	25	ns	$C_L = 15\text{ pF}$
Propagation Delay (High to Low)	t_{PHL}	-	15	25	ns	$C_L = 15\text{ pF}$
Pulse Width Distortion	t_{PWD}	-	-	4	ns	$C_L = 15\text{ pF}$
Channel to Channel Output Skew	t_{SKO}	-	-	3	ns	$C_L = 15\text{ pF}$
AC Specifications						
Rise Time	t_R	-	2.5	-	ns	
Fall Time	t_F	-	2.5	-	ns	
Common-Mode Transient Immunity	C_{MTI}	75	-	-	kV/ μs	Guaranteed by design

Electrical Characteristics 3.3 V Supply

Unless otherwise specified, $T_a = -40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$, $V_{DD} = V_{DD1} = V_{DD2} = 3.0\text{ V}$ to 3.6 V

The typical value is defined at $T_a = 25\text{ }^{\circ}\text{C}$ and $V_{DD} = V_{DD1} = V_{DD2} = 3.3\text{ V}$

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Power Supply Specifications						
Supply Current 1 Side 1	I_{CC1Q}	-	2.2	3.6	mA	$V_{IN} = 0\text{ V}$ or V_{DD1} $C_L = 15\text{ pF}$
Supply Current 1 Side 2	I_{CC2Q}	-	3.8	6.0	mA	$V_{IN} = 0\text{ V}$ or V_{DD2} $C_L = 15\text{ pF}$
Supply Current 2 Side 1	I_{CC1A}	-	5.0	7.0	mA	$D_R = 25\text{ Mbps}$ $C_L = 15\text{ pF}$
Supply Current 2 Side 2	I_{CC2A}	-	7.0	10.5	mA	$D_R = 25\text{ Mbps}$ $C_L = 15\text{ pF}$
DC Specifications						
H Input Voltage	V_{IH}	$0.7V_{DD}$	-	-	V	
L Input Voltage	V_{IL}	-	-	$0.3V_{DD}$	V	
Low Input Current	I_{IL1}	-10	-	0	μA	$V_{DD1} = V_{DD2} = 3.3\text{ V}$ $V_{IN} = 0\text{ V}$ INA, INB, INC pin
	I_{IL2}	-20	-	0	μA	$V_{DD1} = V_{DD2} = 3.3\text{ V}$ $V_{IN} = 0\text{ V}$ EN1, EN2 pin
High Input Current	I_{IH1}	0	-	20	μA	$V_{DD1} = V_{DD2} = 3.3\text{ V}$ $V_{IN} = 3.3\text{ V}$ INA, INB, INC pin
	I_{IH2}	0	-	10	μA	$V_{DD1} = V_{DD2} = 3.3\text{ V}$ $V_{IN} = 3.3\text{ V}$ EN1, EN2 pin
High Output Voltage 1	V_{OH1}	$V_{DD} - 0.1$	V_{DD}	-	V	$I_{SOURCE} = -20\text{ }\mu\text{A}$
High Output Voltage 2	V_{OH2}	$V_{DD} - 0.4$	$V_{DD} - 0.2$	-	V	$I_{SOURCE} = -4\text{ mA}$
Low Output Voltage 1	V_{OL1}	-	0.0	0.1	V	$I_{SINK} = 20\text{ }\mu\text{A}$
Low Output Voltage 2	V_{OL2}	-	0.2	0.4	V	$I_{SINK} = 4\text{ mA}$
Switching Specifications						
Data Rate	D_R	0	-	25	Mbps	$C_L = 15\text{ pF}$
Pulse Width	t_{PW}	40	-	-	ns	$C_L = 15\text{ pF}$
Propagation Delay (Low to High)	t_{PLH}	-	16	28	ns	$C_L = 15\text{ pF}$
Propagation Delay (High to Low)	t_{PHL}	-	16	28	ns	$C_L = 15\text{ pF}$
Pulse Width Distortion	t_{PWD}	-	-	4	ns	$C_L = 15\text{ pF}$
Channel to Channel Output Skew	t_{SKO}	-	-	3	ns	$C_L = 15\text{ pF}$
AC Specifications						
Rise Time	t_R	-	2.5	-	ns	
Fall Time	t_F	-	2.5	-	ns	
Common-Mode Transient Immunity	C_{MTI}	75	-	-	kV/ μs	Guaranteed by design

Typical Performance Curves

(Reference Data)

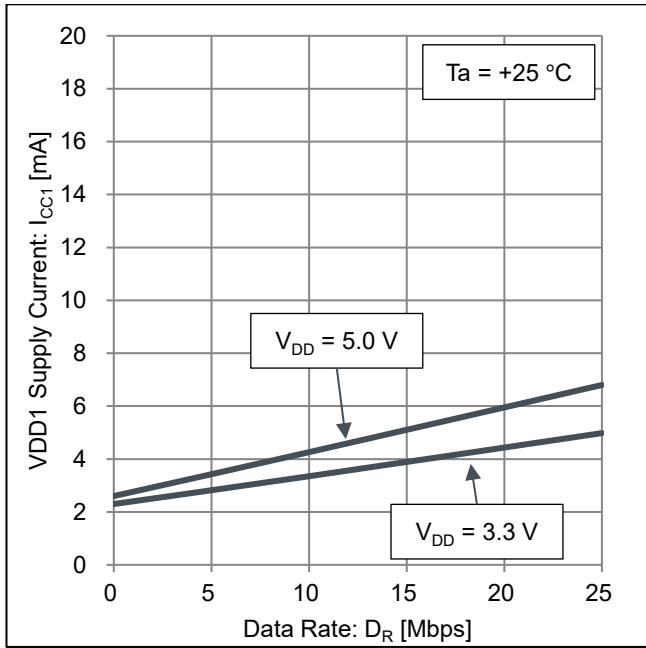


Figure 1. VDD1 Supply Current vs Data Rate (With 15 pF Load)

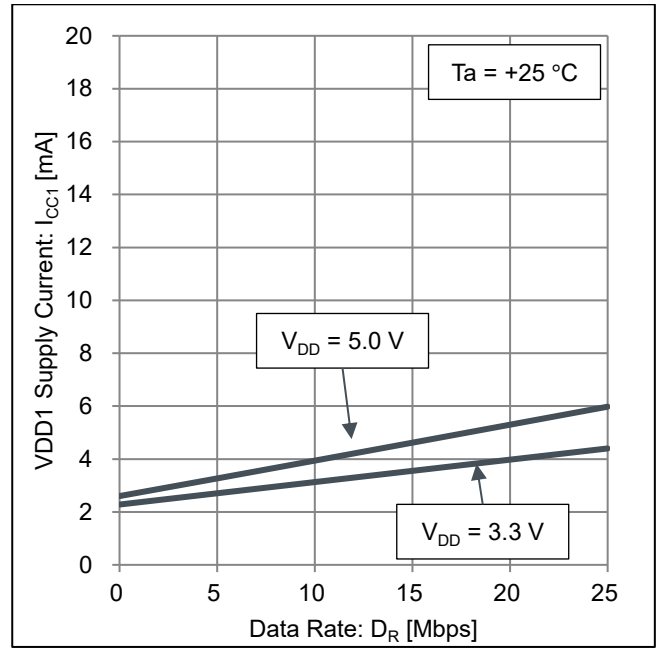


Figure 2. VDD1 Supply Current vs Data Rate (With No Load)

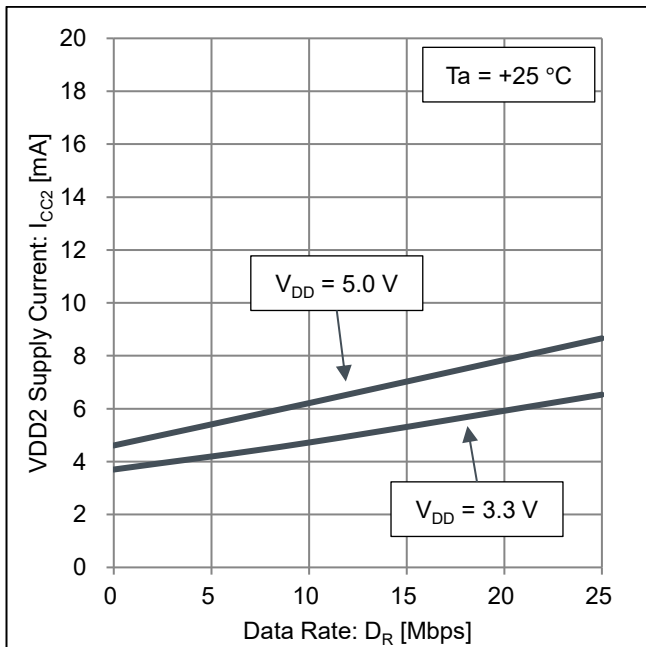


Figure 3. VDD2 Supply Current vs Data Rate (With 15 pF Load)

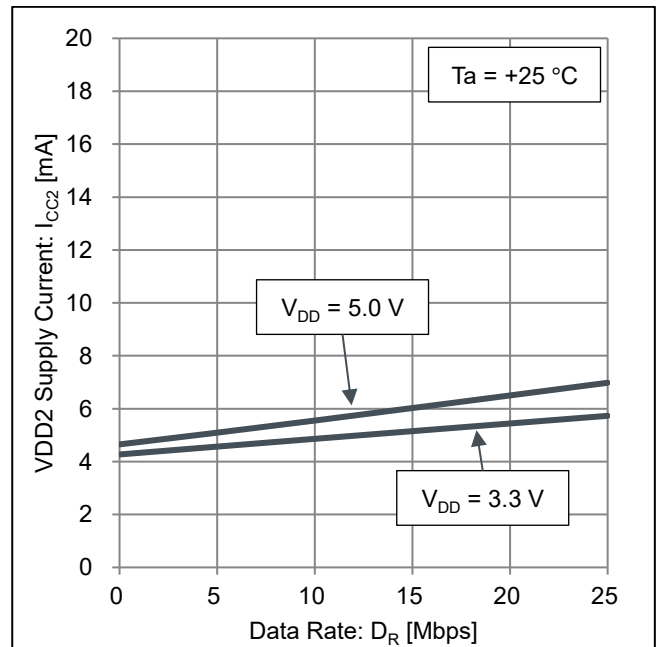


Figure 4. VDD2 Supply Current vs Data Rate (With No Load)

Typical Performance Curves – continued
(Reference Data)

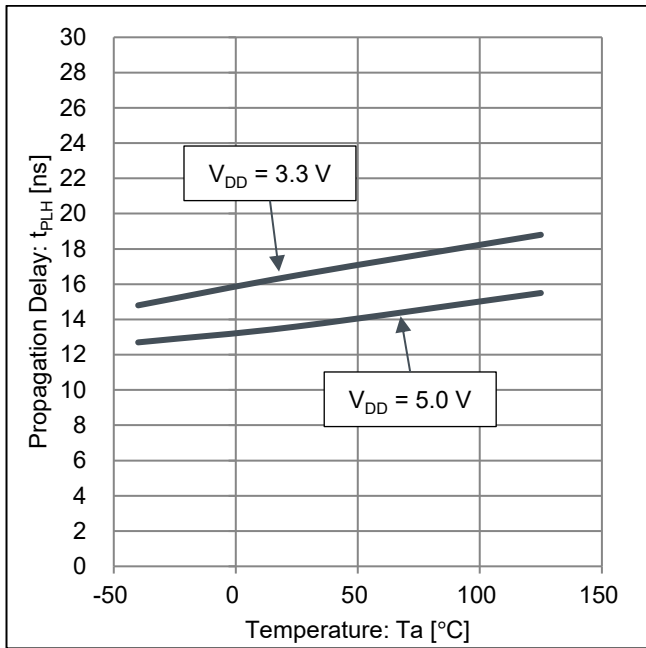


Figure 5. Propagation Delay (Low to High) vs Temperature

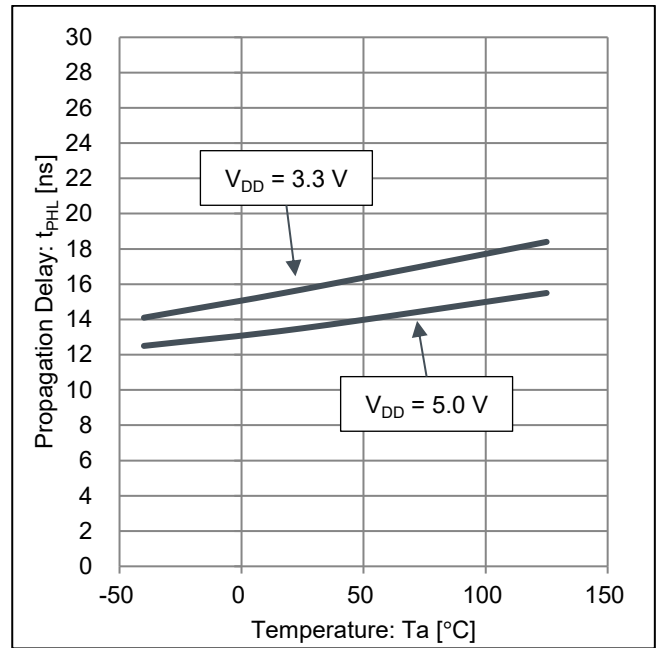
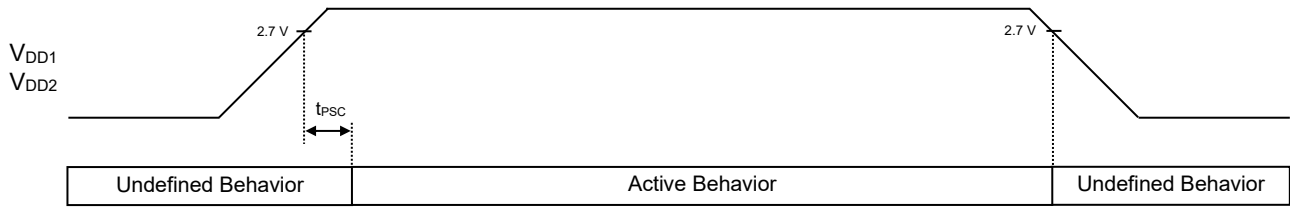


Figure 6. Propagation Delay (High to Low) vs Temperature

Power Supply Sequence

Unless otherwise specified Ta = -40 °C to +125 °C



Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Input Wait Time after Power-up	t _{PSC}	3	-	-	μs	EN1 = V _{DD1} , EN2 = V _{DD2}

Input is available after “t_{PSC}” from VDD is waked up.

If VDD voltage is below the recommended operating voltage range, internal state is “Undefined Behavior”. It then returns to the recommended operating voltage range, which returns to active behavior after the time in “t_{PSC}” has elapsed.

Parameters of Switching Specifications

Propagation Delay is a parameter that represents the time required for a signal to propagate from input to output.

Pulse Width Distortion is the maximum difference between t_{PLH} and t_{PHL} in Propagation Delay. This parameter represents the accuracy with which the timing of input signal is reproduced in output signal.

Channel to Channel Output Skew is the maximum value of the difference of Propagation Delay t_{PLH} or t_{PHL} between multiple channels in product. This parameter represents difference in signal propagation speed for each channel.

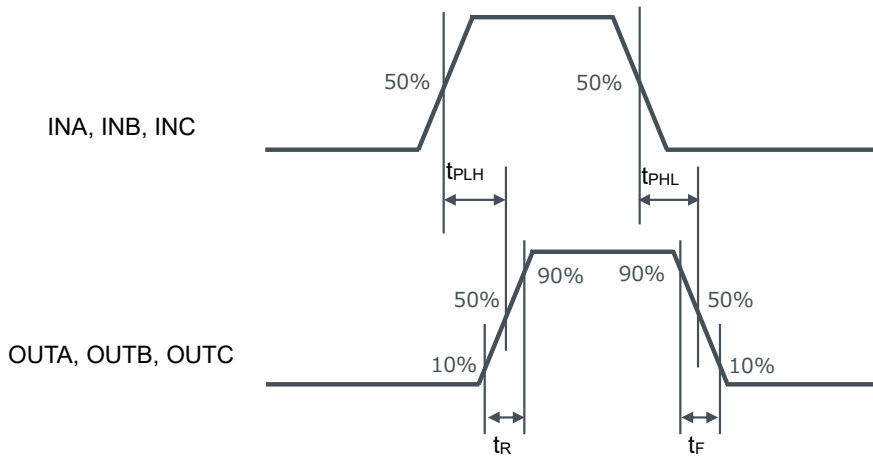


Figure 7. Input / Output Timing Chart

Refresh Operation and Fail-safe Function

If the input signal does not transition for approximately 0.3 μs (Typ), a refresh pulse is generated to maintain the output state.

If the receiving circuit does not receive a refresh pulse for approximately 2 μs (Typ), it determines that the input-side power supply is turned off or the input is in a non-operational state. In such cases, the Fail-safe function forces the output to transition to a Low level.

I/O Equivalence Circuits

Pin Name	Equivalent Circuit Diagram	Pin Name	Equivalent Circuit Diagram
INA INB		INC	
OUTC		OUTA OUTB	
EN1		EN2	

Operational Notes

1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. Recommended Operating Conditions

The function and operation of the IC are guaranteed within the range specified by the recommended operating conditions. The characteristic values are guaranteed only under the conditions of each item specified by the electrical characteristics.

6. Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

7. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

8. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

9. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

Operational Notes – continued

10. Regarding the Input Pin of the IC

This IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When $GND > Pin A$ and $GND > Pin B$, the P-N junction operates as a parasitic diode.
 When $GND > Pin B$, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

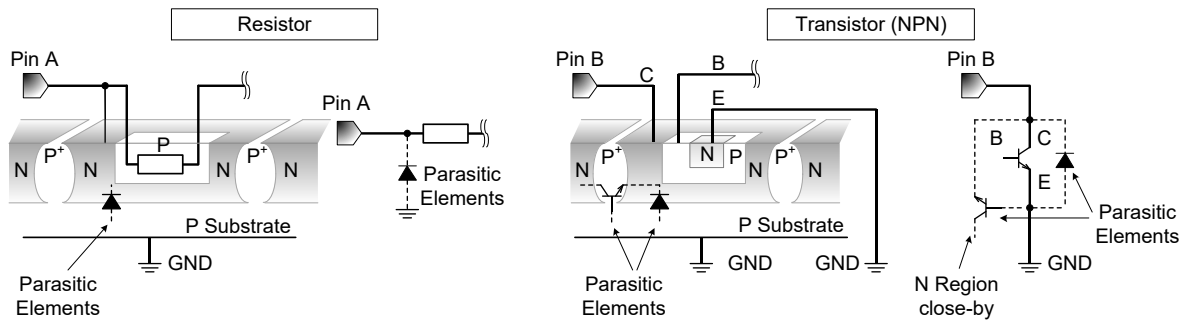
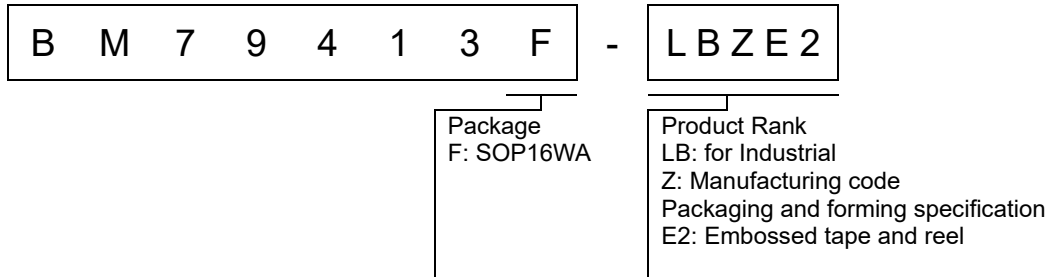


Figure 8. Example of IC Structure

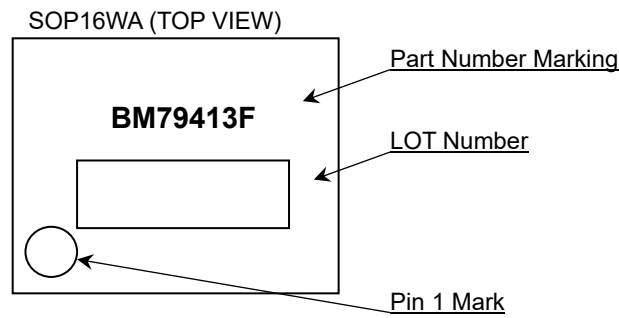
11. Ceramic Capacitor

When using a ceramic capacitor, determine a capacitance value considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

Ordering Information

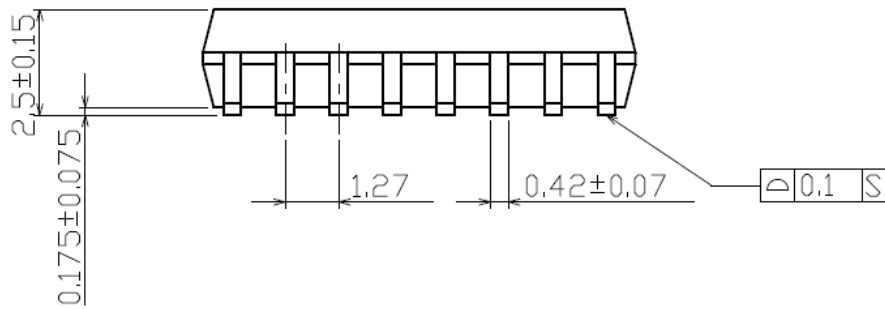
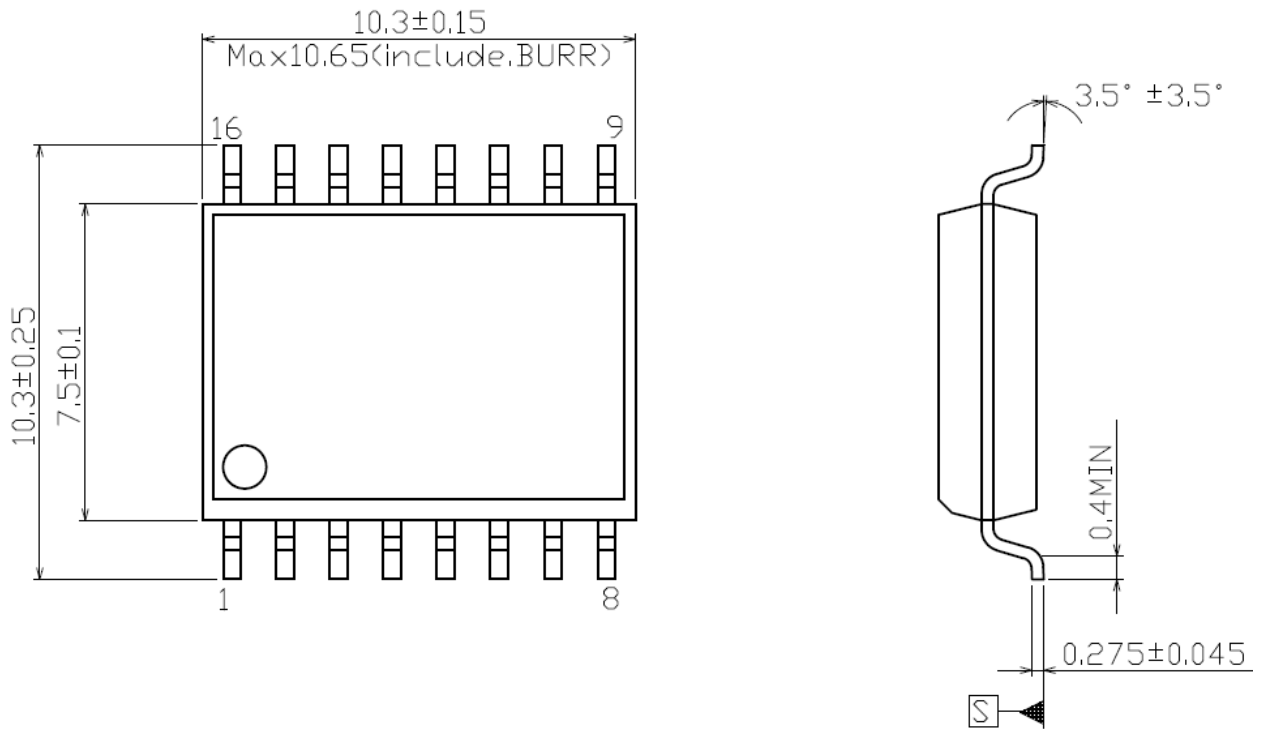


Marking Diagram



Physical Dimension and Packing Information

Package Name	SOP16WA
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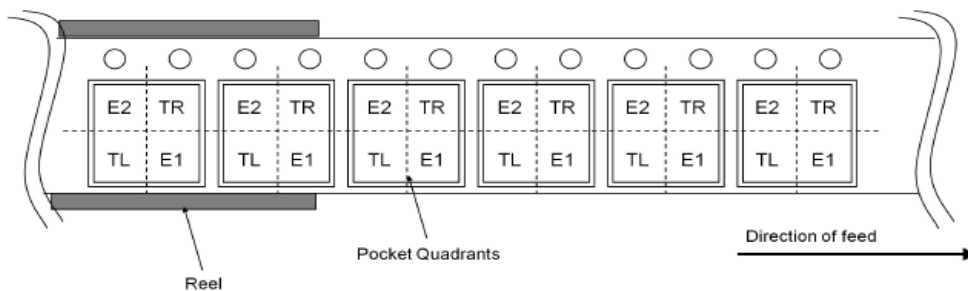


(UNIT:mm)

PKG:SOP16WA
Drawing No.EX001-0153

< Tape and Reel Information >

Tape	Embossed carrier tape
Quantity	1500pcs
Direction of feed	E2 The direction is the pin 1 of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand



Revision History

Date	Revision	Changes
01.Sep.2025	001	New Release

Notice

Precaution on using ROHM Products

1. If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment ^(Note 1), aircraft/spacecraft, nuclear power controllers, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - [a] Installation of protection circuits or other protective devices to improve system safety
 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
3. Our Products are not designed under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc. prior to use, must be necessary:
 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
4. The Products are not subject to radiation-proof design.
5. Please verify and confirm characteristics of the final or mounted products in using the Products.
6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
8. Confirm that operation temperature is within the specified range described in the product specification.
9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

Precaution Regarding Intellectual Property Rights

1. All information and data including but not limited to application example contained in this document is for reference only. ROHM does not warrant that foregoing information or data will not infringe any intellectual property rights or any other rights of any third party regarding such information or data.
2. ROHM shall not have any obligations where the claims, actions or demands arising from the combination of the Products with other articles such as components, circuits, systems or external equipment (including software).
3. No license, expressly or implied, is granted hereby under any intellectual property rights or other rights of ROHM or any third parties with respect to the Products or the information contained in this document. Provided, however, that ROHM will not assert its intellectual property rights or other rights against you or your customers to the extent necessary to manufacture or sell products containing the Products, subject to the terms and conditions herein.

Other Precaution

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2. The Products may not be disassembled, converted, modified, reproduced or otherwise changed without prior written consent of ROHM.
3. In no event shall you use in any way whatsoever the Products and the related technical information contained in the Products or this document for any military purposes, including but not limited to, the development of mass-destruction weapons.
4. The proper names of companies or products described in this document are trademarks or registered trademarks of ROHM, its affiliated companies or third parties.

General Precaution

1. Before you use our Products, you are requested to carefully read this document and fully understand its contents. ROHM shall not be in any way responsible or liable for failure, malfunction or accident arising from the use of any ROHM's Products against warning, caution or note contained in this document.
2. All information contained in this document is current as of the issuing date and subject to change without any prior notice. Before purchasing or using ROHM's Products, please confirm the latest information with a ROHM sales representative.
3. The information contained in this document is provided on an "as is" basis and ROHM does not warrant that all information contained in this document is accurate and/or error-free. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties resulting from inaccuracy or errors of or concerning such information.