

20 V Output Current 1 A (Continuous) 2 ch Brush Motor Driver

BD60210FV

General Description

BD60210FV is a built in 2 H bridge motor driver for 2 DC brush motors, 1 bipolar stepper motor or 4 solenoid loads. This driver can drive high-efficiency by direct PWM control. It is possible to output an abnormal detection signal for Wired-OR that notifies each protection circuit operation, which contributes to set high reliability.

Features

- Single Power Supply Input (Rated Voltage of 20 V)
- Rated Output Current (Continuous) 1.0 A/Phase
- Rated Output Current (Peak) 4.0 A/Phase
- Low ON Resistance DMOS Output
- Forward, Reverse, Brake and Stop Functions
- Power Save Function
- Direct PWM Control
- Driver for 2 DC Brush Motors
- Driver for 1 Bipolar Stepping Motor
- Driver for Solenoid
- FULL STEP and HALF STEP Functionality (Driving Stepper Motor)
- Built-in Logic Input Pulldown Resistor
- Cross Conduction Prevention Circuit
- Thermal Shutdown Circuit (TSD)
- Over Current Protection Circuit (OCP)
- Under Voltage Lock Out Circuit (UVLO)
- Protects Against Malfunction when Power Supply is Disconnected (Ghost Supply Prevention Function)
- Adjacent Pins Short Protection

Applications

- Refrigerator, Air Conditioner, Printers, and other common office appliances

Key Specification

- Input Voltage Range: 8 V to 18 V
- Rated Output Current (Continuous): 1.0 A/Phase
- Rated Output Current (Peak): 4.0 A/Phase
- Operating Temperature Range: -25 °C to +85 °C
- Output ON Resistance: 0.95 Ω (Typ)
(total of upper and lower resistors)

Package

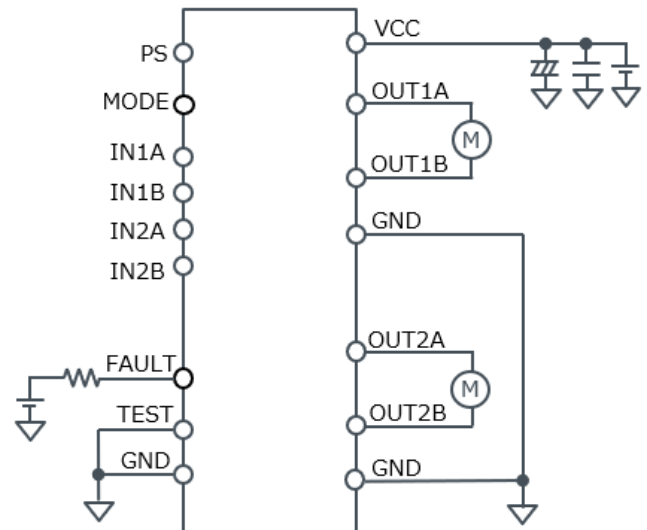
SSOP-B16

W (Typ) x D (Typ) x H (Max)

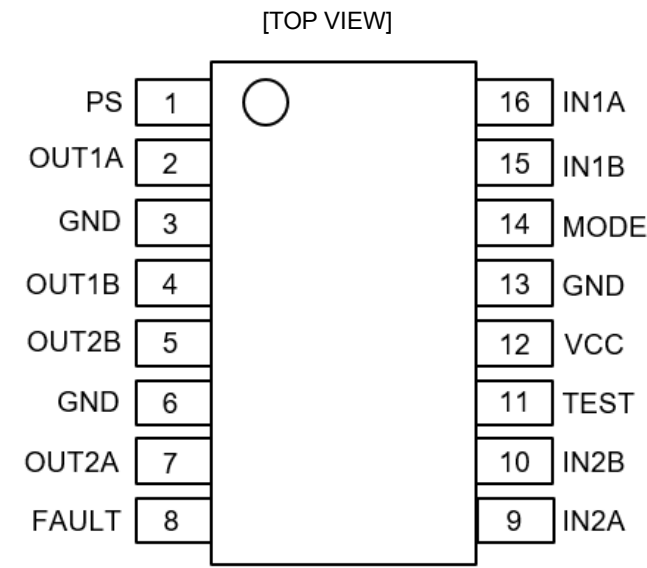
5.0 mm x 6.4 mm x 1.35 mm



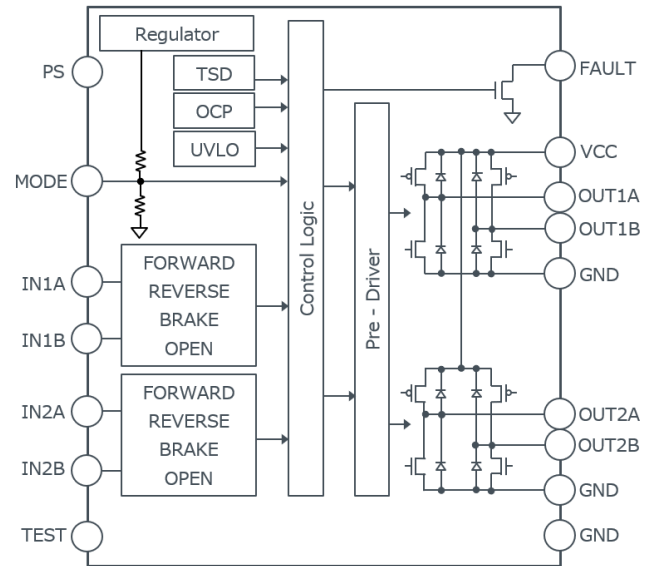
Typical Application Circuit



Pin Configuration



Block Diagram



Pin Description

Pin No.	Pin Name	Function	Pin No.	Pin Name	Function
1	PS	Power save pin	9	IN2A	H bridge control pin
2	OUT1A	H bridge output pin	10	IN2B	H bridge control pin
3	GND	Ground pin	11	TEST	Test pin (used connected to GND)
4	OUT1B	H bridge output pin	12	VCC	Power supply pin
5	OUT2B	H bridge output pin	13	GND	Ground pin
6	GND	Ground pin	14	MODE	Drive mode setting pin
7	OUT2A	H bridge output pin	15	IN1B	H bridge control pin
8	FAULT	Abnormal state detection output pin	16	IN1A	H bridge control pin

Function Description

1 VCC/Power supply pin

The motor's drive current is flowing through this pin, so please connect it in such a way that the wire is thick & short and has low impedance. VCC voltage may have great fluctuation by the back EMF of the motor or PWM switching noise, so please connect the bypass capacitor (10 μ F to 470 μ F) as close as possible to the pin. Adjust in such a way that the VCC voltage is stable. Please increase the capacitance if needed, especially when large current or motors that have great back electromotive force are used.

In addition, to reduce the power supply's impedance in wide frequency bandwidth, parallel connection of multi-layered ceramic capacitor (0.01 μ F to 0.1 μ F) is recommended. Extreme care must be observed to make sure that the VCC voltage does not exceed the rating even for a moment.

Moreover, there is a built-in clamp component in the output pin to prevent electrostatic destruction. If sudden pulse or surge voltage of more than the maximum absolute rating is applied, the clamp component operates which can result to destruction. Please be sure to not exceed the maximum absolute rating. It is effective to mount a Zener diode with maximum absolute rating. Also, diode is inserted between VCC pin and GND pin to prevent electrostatic destruction. If reverse voltage is applied between VCC pin and GND pin, there is a danger of IC destruction so please be careful.

2 GND/Ground pin

To reduce the noise caused by switching current and to stabilize the internal reference voltage of IC, please connect it in such a way that the wiring impedance from this pin is made as low as possible to achieve the lowest electrical potential no matter what operating state it may be. Design the pattern so that it does not have a common impedance with other GND patterns.

3 PS/Power save pin

The PS pin can make circuits in standby state and make motor output OPEN.

When PS = L to H, be careful because there is a delay of 40 μ s (Max) before it is returned from standby state to normal state and the motor output become ACTIVE.

PS	State
L	STANDBY
H	ACTIVE

4 MODE/Drive mode setting pin

This is a 3-value input pin that sets the output drive mode. Four drive modes can be set by combining the MODE and IN2A pins.

MODE	IN2A	Drive Mode
L	X	4 Pins Interface Mode
H	L	2 Pins Interface Mode
H	H	Parallel Bridge Interface Mode
M (OPEN)	X	Half Bridge Interface Mode

5 IN1A, IN1B, IN2A, IN2B/H bridge control pin

It decides output logic for H bridge. Please refer to [page 10](#) for details.

6 OUT1A, OUT1B, OUT2A, OUT2B/H bridge output pin

The motor's drive current is flowing in it, so please connect it in such a way that the wire is thick & short and has low impedance. It is also effective to add a Schottky diode if output has great positive or negative fluctuation when large current is applied. For example, counter electromotive voltage etc. is great.

Moreover, there is a built-in clamp component in the output pin to prevent electrostatic destruction. If sudden pulse or surge voltage of more than the maximum absolute rating is applied, the clamp component operates which can result in destruction. Please be sure to not exceed the maximum absolute rating.

Function Description - continued

7 FAULT/Abnormal state detection output pin

If overcurrent protection (OCP) or thermal shutdown circuit (TSD) is activated, an abnormality detection signal will be output. This signal can be connected to a microcontroller to shutdown the system. The output format of this pin is open drain, so please use it by pulling up a 5 kΩ to 100 kΩ resistor to a 7 V or lower power source, such as a 5 V power supply or 3.3 V power supply. When this pin is not used, please connect it to GND.

Protect Function (OCP or TSD)	FAULT
INACTIVE	H
ACTIVE	L

8 TEST/Pin for testing

This is the pin to use when testing the IC for shipment. This is used while connected to GND. Please take note that if used without connecting to GND, there is a possibility of malfunction.

Protection Circuits

1 Thermal Shutdown (TSD)

This IC has a built-in thermal shutdown circuit as an overheat protection measure. When the IC chip temperature exceeds 175 °C (Typ), the motor output is set to OPEN. Also, when the temperature drops below 150 °C (Typ), it will automatically return to normal operation. However, even when the TSD is operating, if heat is continued to be applied from the outside, thermal runaway will occur and lead to destruction.

2 Over Current Protection (OCP)

This IC has a built-in over current protection circuit as a provision against destruction when the motor outputs are shorted each other or VCC-motor output or motor output-GND is shorted. This circuit latches the motor output to OPEN condition when the regulated current flows for 2 μ s (Typ). It returns with power reactivation or a reset by the PS pin. The over current protection circuit's only aim is to prevent the destruction of the IC from irregular situations such as motor output shorts and is not meant to be used as protection or security for the set. Therefore, sets should not be designed to take into account this circuit's functions. After OCP operating, if irregular situations continue and the return by power reactivation or a reset by the PS pin, then OCP operates repeatedly, and the IC may generate heat or otherwise deteriorate. When the L value of the wiring is great due to the wiring being long, the motor outputs have shorted each other or VCC-motor output or motor output-GND is shorted, if the output pin voltage jumps up and the absolute maximum values can be exceeded after the over current has flowed, there is a possibility of destruction. Also, when current which is the output current rating or more and the OCP detection current or less flows, the IC can heat up to $T_{jmax} = 150$ °C exceeds and can deteriorate, so current which or more the output rating should not be applied.

3 Under Voltage Lock Out (UVLO)

This IC has a built-in under voltage lock out function to prevent false operation such as IC output during power supply under voltage is low. When the applied voltage to the VCC pin goes 5 V (Typ) or less, the motor output is set to OPEN. This switching voltage has a 1 V (Typ) hysteresis to prevent false operation by noise etc. Be aware that this circuit does not operate during power save mode.

4 Protects against malfunction when power supply is disconnected (Ghost Supply Prevention Function)

If a control signal^(Note 1) is input when there is no power supplied to this IC, there is a function which prevents a malfunction where voltage is supplied to power supply of this IC or other IC in the set via the electrostatic destruction prevention diode from these input pins to the VCC. Therefore, there is no malfunction of the circuit even when voltage is supplied to these input pins while there is no power supply.

(Note 1) Control Signal = PS, IN1A, IN1B, IN2A, IN2B, MODE

5 Operation Under Strong Electric Field

The IC is not designed for using in the presence of strong electromagnetic field. Be sure to confirm that no malfunction is found when using the IC in a strong electromagnetic field.

Absolute Maximum Rating

Parameter	Symbol	Rating	Unit
Supply Voltage	V _{CC}	-0.3 to +20.0	V
Input Voltage for Control Pin <i>(Note 1)</i>	V _{IN}	-0.3 to +6.5	V
Output Current (Continuous)	I _{OUT}	1.0 <i>(Note 2)</i>	A/Phase
Output Current (Peak)	I _{OUTPEAK}	4.0 <i>(Note 2)(Note 3)</i>	A/Phase
Storage Temperature Range	T _{stg}	-55 to +150	°C
Maximum Junction Temperature	T _{jmax}	+150	°C

(Note 1) Control Input = PS, IN1A, IN1B, IN2A, IN2B, MODE

(Note 2) Do not exceed T_{jmax} = 150 °C.

(Note 3) Pulse width t_w ≤ 500 ns, Duty 20 %

Caution 1: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

Caution 2: Should by any chance the maximum junction temperature rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, design a PCB with thermal resistance taken into consideration by increasing board size and copper area so as not to exceed the maximum junction temperature rating.

Recommended Operating Condition

Parameter	Symbol	Min	Typ	Max	Unit
Power Supply Voltage	V _{CC}	8	12	18	V
PWM Frequency (IN1A, IN1B, IN2A, IN2B)	f _{PWM}	0	-	250	kHz
Operating Temperature	T _{opr}	-25	+25	+85	°C
Maximum Output Current (Continuous)	I _{OUT}	-	-	0.7 <i>(Note 4)</i>	A/Phase

(Note 4) Do not exceed T_{jmax} = 150 °C.

Thermal Resistance *(Note 5)*

Parameter	Symbol	Thermal Resistance (Typ)		Unit
		1s ^(Note 7)	2s2p ^(Note 8)	
SSOP-B16				
Junction to Ambient	θ _{JA}	138.0	77.7	°C/W
Junction to Top Characterization Parameter ^(Note 6)	Ψ _{JT}	12	9	°C/W

(Note 5) Based on JESD51-2A (Still-Air), using a BD60210FV Chip.

(Note 6) The thermal characterization parameter to report the difference between junction temperature and the temperature at the top center of the outside surface of the component package.

(Note 7) Using a PCB board based on JESD51-3.

(Note 8) Using a PCB board based on JESD51-7.

Layer Number of Measurement Board	Material	Board Size
Single	FR-4	114.3 mm x 76.2 mm x 1.57 mmt

Top	
Copper Pattern	Thickness
Footprints and Traces	70 μm

Layer Number of Measurement Board	Material	Board Size
4 Layers	FR-4	114.3 mm x 76.2 mm x 1.6 mmt

Top		2 Internal Layers		Bottom	
Copper Pattern	Thickness	Copper Pattern	Thickness	Copper Pattern	Thickness
Footprints and Traces	70 μm	74.2 mm x 74.2 mm	35 μm	74.2 mm x 74.2 mm	70 μm

Electrical Characteristics (Unless otherwise specified, $V_{CC} = 12\text{ V}$, $T_a = 25\text{ }^{\circ}\text{C}$)

Parameter	Symbol	Specification			Unit	Condition
		Min	Typ	Max		
[Overall]						
Circuit Current at Standby	I _{CCST}	-	0	1	μA	PS = L
Circuit Current	I _{CC}	-	2.5	5.0	mA	PS = H
[Control Input (Note 1)]						
H-level Input Voltage	V _{INH}	2.0	-	-	V	
L-level Input Voltage	V _{INL}	0	-	0.8	V	
H-level Input Current	I _{INH}	13	25	38	μA	V _{IN} = 5 V
L-level Input Current	I _{INL}	-1.0	-	+1.0	μA	V _{IN} = 0 V
[MODE Input]						
H-level Input Voltage	V _{MINH}	2.8	-	-	V	
Open Voltage	V _{MOPEN}	1.30	1.65	2.00	V	
L-level Input Voltage	V _{MINL}	0	-	0.8	V	
H-level Input Current	I _{MINH}	8	17	25	μA	V _{MIN} = 5 V
L-level Input Current	I _{MINL}	-12	-8	-4	μA	V _{MIN} = 0 V
[Abnormality Detection Signal Output]						
Output L Voltage	V _{FL}	-	0.3	0.5	V	FAULT = 5 mA
Output Leak	I _{FLEAK}	-	-	1	μA	FAULT = 5 V
[Output (Note 2)]						
Output ON Resistance	R _{ON}	-	0.95	1.23	Ω	I _{OUT} = ±0.5 A (Sum of upper and lower)
Output Leak	I _{LEAK}	-	-	1	μA	

(Note 1) Control Input = PS, IN1A, IN1B, IN2A, IN2B

(Note 2) Output = OUT1A, OUT1B, OUT2A, OUT2B

Direct PWM Control (4 pin-Interface)

This IC can perform PWM control directly with a microcontroller using IN1A, IN1B, IN2A, and IN2B. Two current regeneration methods can be selected: SLOW DECAY and FAST DECAY. An example control sequence and current regeneration path are shown below.

1 SLOW DECAY (FORWARD Rotation)

Input			Output		Status
PS	INxA (Note 1)	INxB (Note 1)	OUTxA (Note 1)	OUTxB (Note 1)	
H	H	L	H	L	ON
H	H	H	L	L	SLOW DECAY
H	H	L	H	L	ON
H	H	H	L	L	SLOW DECAY
H	H	L	H	L	ON

(Note 1) x = 1, 2

2 FAST DECAY (FORWARD Rotation)

Input			Output		Status
PS	INxA (Note 2)	INxB (Note 2)	OUTxA (Note 2)	OUTxB (Note 2)	
H	H	L	H	L	ON
H	L	H	L	H	FAST DECAY
H	H	L	H	L	ON
H	L	H	L	H	FAST DECAY
H	H	L	H	L	ON

(Note 2) x = 1, 2

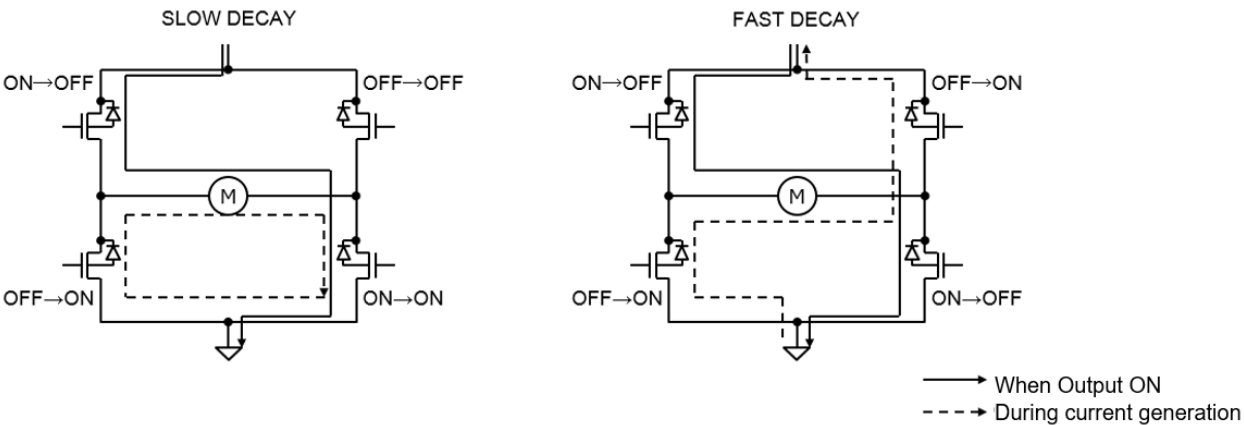
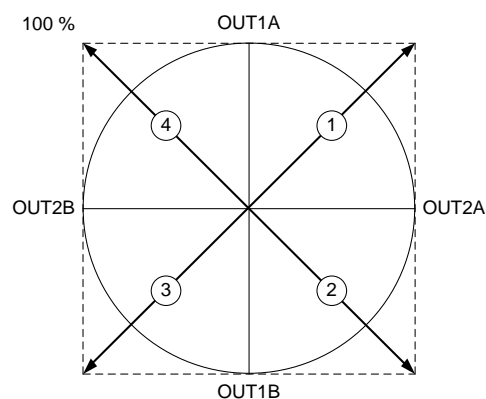
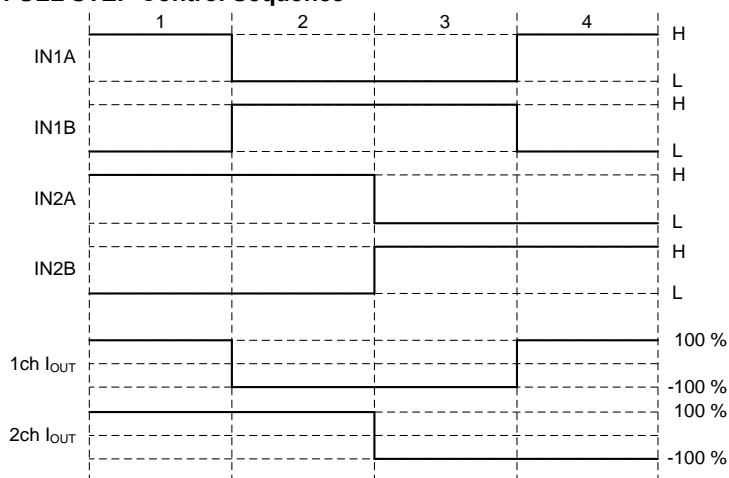


Figure 1. Current Generation Path

Stepping Motor Drive Sequence (4 pin-interface)

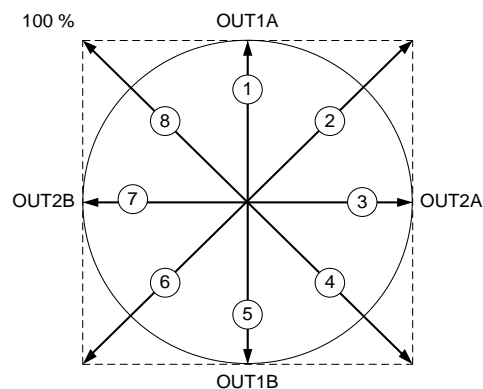
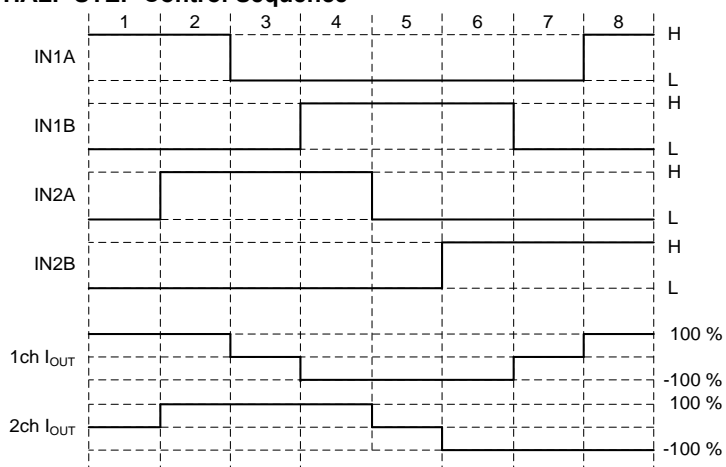
By inputting a phase switching logic signal, the stepping motor can be driven in FULL STEP drive or HALF STEP drive. An example control sequence and torque vector are shown below.

1 FULL STEP Control Sequence



	Input				Output			
	IN1A	IN1B	IN2A	IN2B	OUT1A	OUT1B	OUT2A	OUT2B
1	H	L	H	L	H	L	H	L
2	L	H	H	L	L	H	H	L
3	L	H	L	H	L	H	L	H
4	H	L	L	H	H	L	L	H

2 HALF STEP Control Sequence



	Input				Output			
	IN1A	IN1B	IN2A	IN2B	OUT1A	OUT1B	OUT2A	OUT2B
1	H	L	L	L	H	L	OPEN	OPEN
2	H	L	H	L	H	L	H	L
3	L	L	H	L	OPEN	OPEN	H	L
4	L	H	H	L	L	H	H	L
5	L	H	L	L	L	H	OPEN	OPEN
6	L	H	L	H	L	H	L	H
7	L	L	L	H	OPEN	OPEN	L	H
8	H	L	L	H	H	L	L	H

H Bridge Control

The IC includes two identical H bridge motor drivers with current-controlled PWM circuits, and four different operating modes can be selected according to demand.

The control sequences for the four operating modes are shown below.

4pin - Interface Mode (MODE = L)

Input					Output				Status (DC MOTOR)
PS	IN1A	IN1B	IN2A	IN2B	OUT1A	OUT1B	OUT2A	OUT2B	
0	X	X	X	X	Z	Z	Z	Z	STANDBY
1	0	0	-	-	Z	Z	-	-	STANDBY
1	0	1	-	-	L	H	-	-	REVERSE
1	1	0	-	-	H	L	-	-	FORWARD
1	1	1	-	-	L	L	-	-	BRAKE
1	-	-	0	0	-	-	Z	Z	STANDBY
1	-	-	0	1	-	-	L	H	REVERSE
1	-	-	1	0	-	-	H	L	FORWARD
1	-	-	1	1	-	-	L	L	BRAKE

2pin - Interface Mode (MODE = H, IN2A = L)

Input					Output				Status (DC MOTOR)
PS	IN1A	IN1B	IN2A	IN2B	OUT1A	OUT1B	OUT2A	OUT2B	
0	X	X	X	X	Z	Z	Z	Z	STANDBY
1	0	-	0	X	L	H	-	-	REVERSE
1	1	-	0	X	H	L	-	-	FORWARD
1	-	0	0	X	-	-	L	H	REVERSE
1	-	1	0	X	-	-	H	L	FORWARD

Parallel Bridge - Interface Mode (MODE = H, IN2A = H)

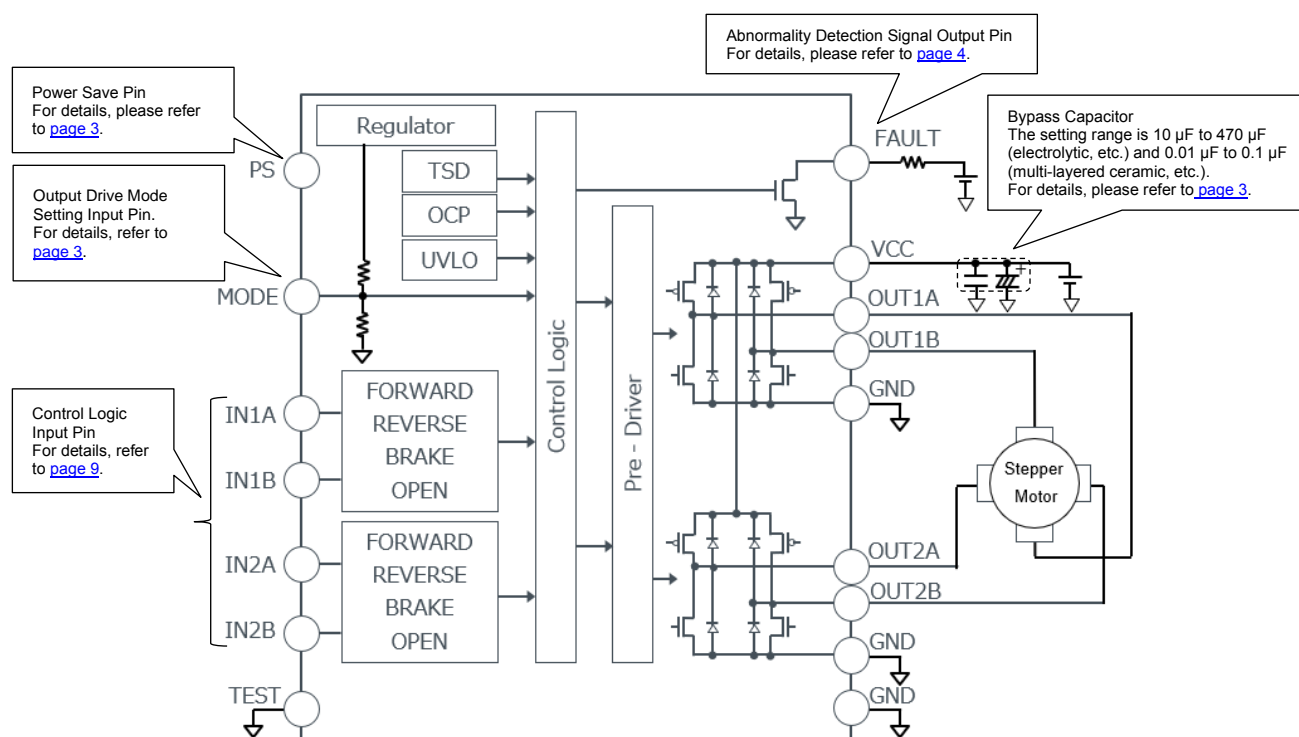
Input					Output				Status (DC MOTOR)
PS	IN1A	IN1B	IN2A	IN2B	OUT1A	OUT1B	OUT2A	OUT2B	
0	X	X	X	X	Z	Z	Z	Z	STANDBY
1	0	0	1	X	Z	Z	Z	Z	STANDBY
1	0	1	1	X	L	H	L	H	REVERSE
1	1	0	1	X	H	L	H	L	FORWARD
1	1	1	1	X	L	L	L	L	BRAKE

Half Bridge - Interface Mode (MODE = OPEN)

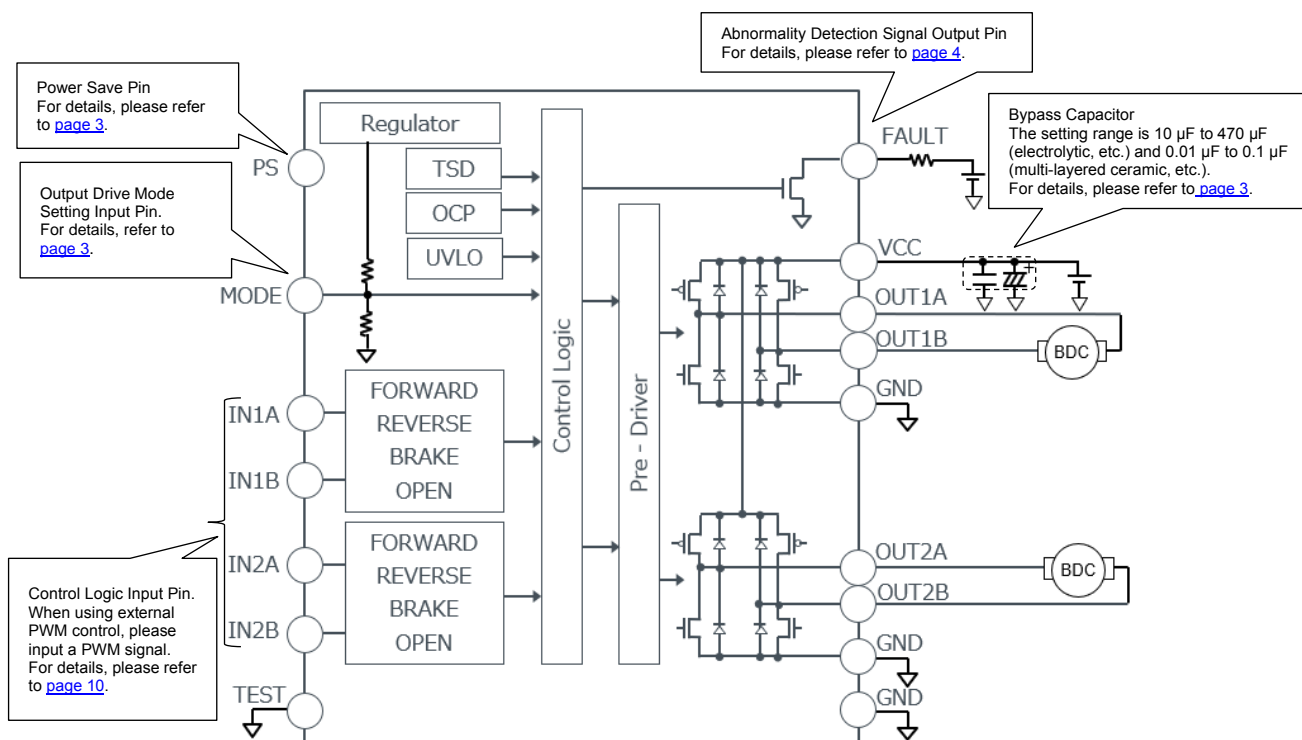
Input					Output				Status
PS	IN1A	IN1B	IN2A	IN2B	OUT1A	OUT1B	OUT2A	OUT2B	
0	X	X	X	X	Z	Z	Z	Z	STANDBY
1	0	-	-	-	L	-	-	-	OUT1A connected to GND
1	1	-	-	-	H	-	-	-	OUT1A connected to V _{CC}
1	-	0	-	-	-	L	-	-	OUT1B connected to GND
1	-	1	-	-	-	H	-	-	OUT1B connected to V _{CC}
1	-	-	0	-	-	-	L	-	OUT2A connected to GND
1	-	-	1	-	-	-	H	-	OUT2A connected to V _{CC}
1	-	-	-	0	-	-	-	L	OUT2B connected to GND
1	-	-	-	1	-	-	-	H	OUT2B connected to V _{CC}

Application Example

1 Stepping Motor Drive

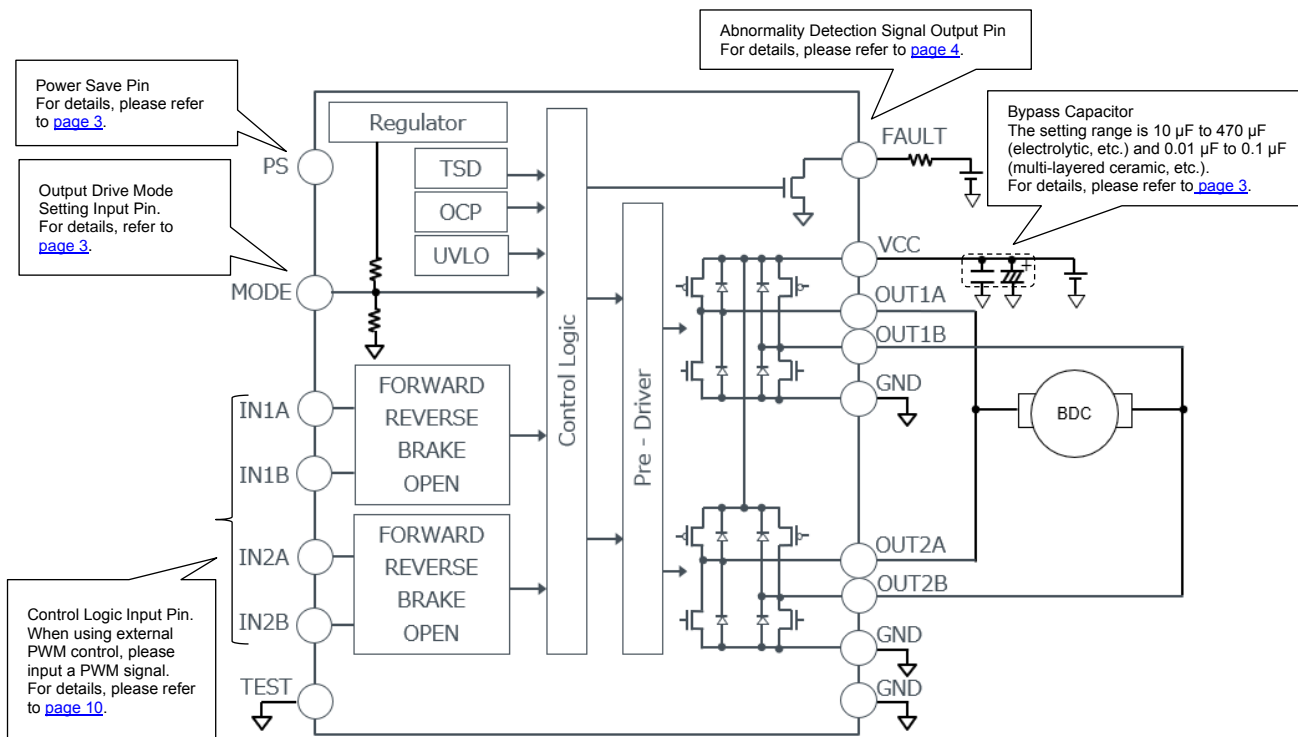


2 Brush Motor 2ch Drive

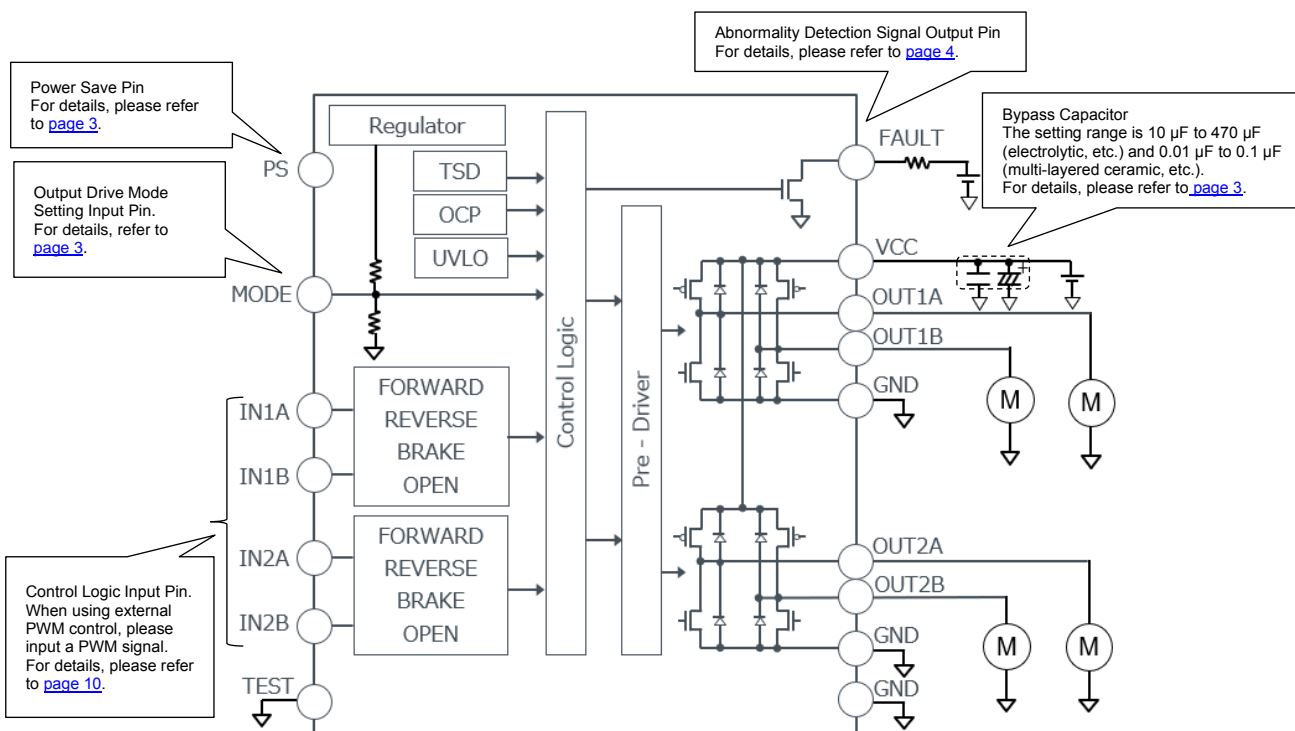


Application Example – continued

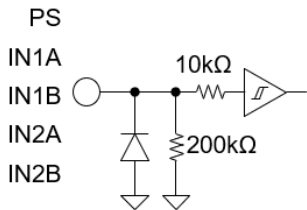
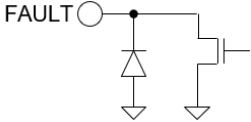
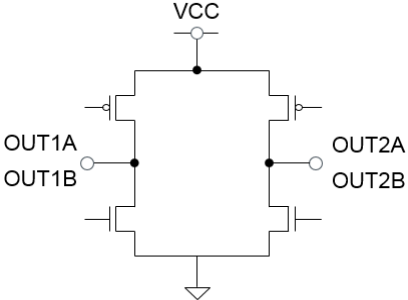
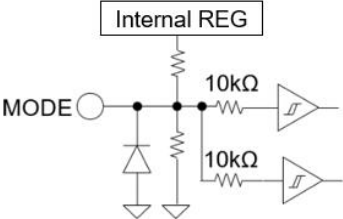
3 Parallel Bridge Drive



4 Half Bridge Drive



I/O Equivalence Circuit

Pin No.	Pin Name	Equivalence Circuit Diagram	Pin No.	Pin Name	Equivalence Circuit Diagram
1	PS		8	FAULT	
16	IN2A				
15	IN1B				
9	IN2A				
10	IN2B				
2	OUT1A		14	MODE	
4	OUT1B				
7	OUT2A				
5	OUT2B				

Operational Notes

1 Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2 Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3 Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4 Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5 Recommended Operating Conditions

The function and operation of the IC are guaranteed within the range specified by the recommended operating conditions. The characteristic values are guaranteed only under the conditions of each item specified by the electrical characteristics.

6 Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

7 Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

8 Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

9 Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So, unless otherwise specified, unused input pins should be connected to the power supply or ground line.

Operational Notes - continued

10 Regarding the Input Pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When $GND > Pin\ A$ and $GND > Pin\ B$, the P-N junction operates as a parasitic diode.

When $GND > Pin\ B$, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

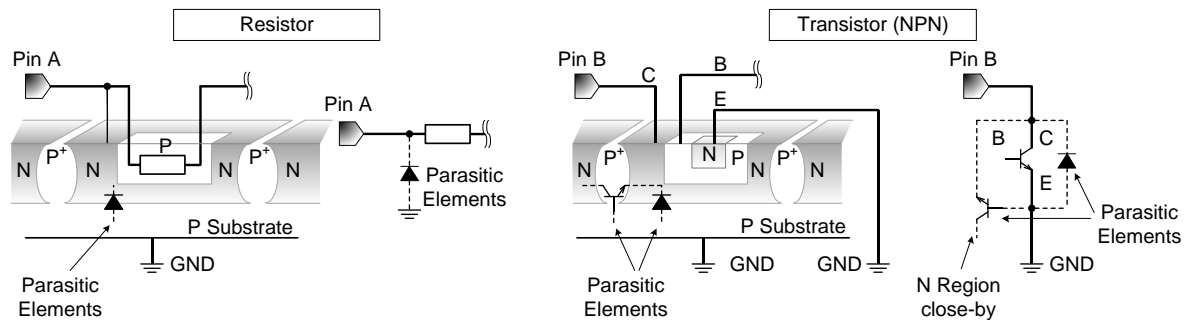


Figure 2. Example of Monolithic IC Structure

11 Ceramic Capacitor

When using a ceramic capacitor, determine a capacitance value considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

12 Thermal Shutdown Circuit (TSD)

This IC has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC's maximum junction temperature rating. If however the rating is exceeded for a continued period, the junction temperature (T_j) will rise which will activate the TSD circuit that will turn OFF power output pins. When the T_j falls below the TSD threshold, the circuits are automatically restored to normal operation.

Note that the TSD circuit operates in a situation that exceeds the absolute maximum ratings and therefore, under no circumstances, should the TSD circuit be used in a set design or for any purpose other than protecting the IC from heat damage.

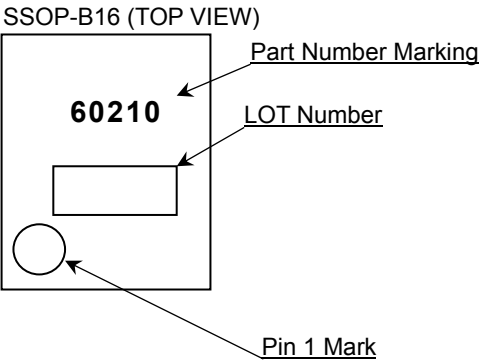
13 Over Current Protection Circuit (OCP)

This IC incorporates an integrated overcurrent protection circuit that is activated when the load is shorted. This protection circuit is effective in preventing damage due to sudden and unexpected incidents. However, the IC should not be used in applications characterized by continuous operation or transitioning of the protection circuit.

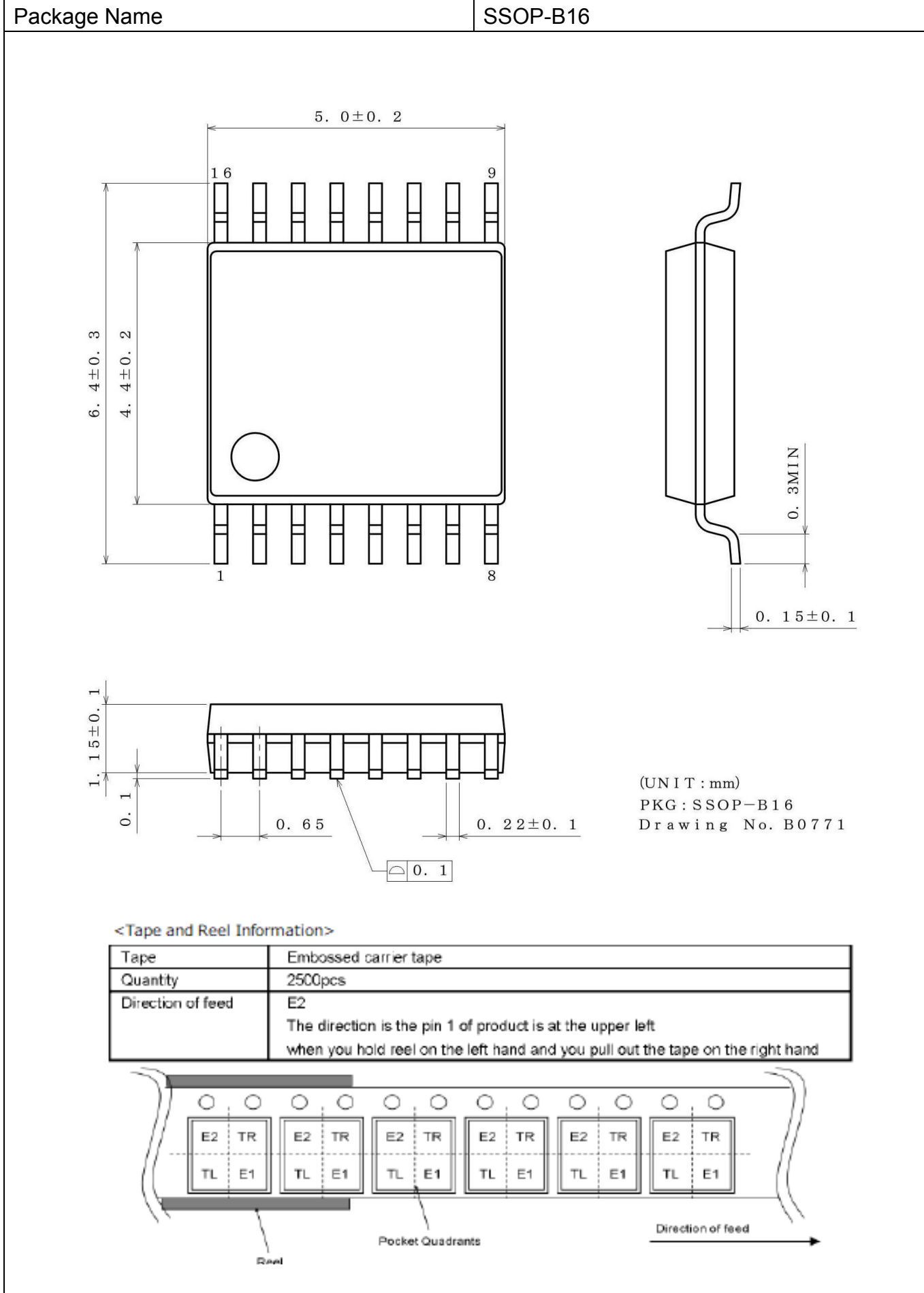
Ordering Information

B D 6 0 2 1 0 F V										-	E 2	
Part Number										Package FV: SSOP-B16	Packing and Forming Specification E2: Embossed tape and reel	

Marking Diagram



Physical Dimension and Packing Information



Revision History

Date	Revision	Changes
22.Nov. 2024	001	New Release

Notice

Precaution on using ROHM Products

- Our Products are designed and manufactured for application in ordinary electronic equipment (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment ^(Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

- ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - Installation of protection circuits or other protective devices to improve system safety
 - Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc. prior to use, must be necessary:
 - Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - Sealing or coating our Products with resin or other coating materials
 - Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.) ; or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

Precaution Regarding Intellectual Property Rights

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General Precaution

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