

Product	Schottky barrier diode	Grade	Normal
Package	PMDEM	JEDEC Code	-
Type	RB168VWM-60TR		

1. CHARGED DEVICE MODEL (CDM)

Table1 CHARGED DEVICE MODEL ESD IMMUNITY FOR EACH TESTING STANDARDS

STANDARD	TEST TYPE	TEST LEVEL	RESULT	CLASS
JEDEC JESD22-C101	FI-CDM	1000V	PASS	C3
JEITA ED-4701/302	FI-CDM (Condition:A)	1000V	PASS	IV

2. HUMAN BODY MODEL (HBM)

Table2 HUMAN BODY MODEL ESD IMMUNITY FOR EACH TESTING STANDARDS

STANDARD	TEST TYPE	TEST CONDITION	TEST LEVEL	RESULT
JEDEC JESD22-A114	Human body model (Contact mode)	C=100pF, R=1.5kΩ	2kV	PASS
JEITA ED-4701/302	Human body model (Contact mode)	C=100pF, R=1.5kΩ	2kV	PASS

3. NOTICE

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Proper handling in the manufacturing process and storage conditions are required to prevent voltage exceeding the Product maximum rating to be applied to the Products. Caution especially required in dry environment (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of ionizer, friction prevention and temperature / humidity control).

Notes

- 1) The information contained herein is subject to change without notice.
- 2) Before you use our Products, please contact our sales representative and verify the latest specifications.
- 3) Although ROHM is continuously working to improve product reliability and quality, semiconductors can break down and malfunction due to various factors. Therefore, in order to prevent personal injury or fire arising from failure, please take safety measures such as complying with the derating characteristics, implementing redundant and fire prevention designs, and utilizing backups and fail-safe procedures. ROHM shall have no responsibility for any damages arising out of the use of our Products beyond the rating specified by ROHM.
- 4) Examples of application circuits, circuit constants and any other information contained herein are provided only to illustrate the standard usage and operations of the Products. The peripheral conditions must be taken into account when designing circuits for mass production.
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