

Product TVS(Transient Voltage Suppressor) Package Surface mount molded package

Life Test				
TEST ITEM	TEST CONDITION STANDAR		n[pcs] (Sample QTY.)	Pn[pcs] (NG QTY.)
High temperature reverce bias	Tjmax、VR=VR Max、 1000h	JESD22-A108	77	0
High humidity,High temp. reverse bias	85±2°C、Rh=85±5%、 VR = less than VR MAX※, 1000h	JESD22-A101	77	0
Temperature cycle	-55±5℃(30min.)←→150±5℃(30min.)、 200cyc	JESD22-A104	77	0
Pressure cooker test (Autoclave)	Ta=121±2℃、Rh=100%、203kPa、 100h	JESD22-A102	77	0

Stress Test				
TEST ITEM	TEST CONDITION	STANDARD	n[pcs] (Sample QTY.)	Pn[pcs] (NG QTY.)
Solder heat resistance 1	260±5°C、10sec.、Solder - bath	JESD22-B106D	30	0
Solder heat resistance 2	$350\pm10^{\circ}$ C、3s.、Dipping leads into solder bath at $350\pm10^{\circ}$ C.	JESD22-B106D	30	0
Solderability	245±5°C、3s.、Solder-Bath	JESD22-B102	10	0
Terminal strength (Push)	Push force on mounted PCB ; 5N、 10s	JEITA ED-4702B	10	0
Terminal strength (PCB Bending)	Mounted PCB bending depth ; 1.0mm/s、3mm、5 s	JEITA ED-4702B	10	0

Measurement Item & Criteria					
Item	Condition	Criteria			
Forward Voltage(V F)	Par specification	Less than x1.1 of Initial value			
Reverse Current (IR)	Par specification	Less than x2 of Initial value			
Appearance	Visual check	No outstanding change in physical			
Solderability	Visual check	Reflow			
		Solder Bath	non-wering miseen		

JUDGEMENT

No failure is observed from each test item.

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