

Product	Zener Diode	Package	Wafer level chip size package
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Life Test				
TEST ITEM	TEST CONDITION	STANDARD	n[pcs] (Sample QTY.)	Pn[pcs] (NG QTY.)
High temperature reverse bias	Tjmax, VR=VR Max, 1000h	JESD22-A108	77	0
High humidity, High temp. reverse bias	85±2°C, Rh=85±5%, VR = VR MAX, 1000h	JESD22-A101	77	0
Temperature cycle	-55±5°C(30min.)↔150±5°C(30min.), 200cyc	JESD22-A104	77	0
Pressure cooker test (Autoclave)	Ta=121±2°C, Rh=100%, 203kPa, 100h	JESD22-A102	77	0
Intermittent Operation Life or Power and Temperature Cycle	Ta=25±5°C, ΔTj≥100°C, Duration: 15000cycles Ta = -40(+0/-10)°C~+85(+10/-0)°C, ON 300s/OFF 300s per cycle Duration: 6000cycles	MIL-STD-750 Method 1037 JESD22 A-105	77	0

Stress Test				
TEST ITEM	TEST CONDITION	STANDARD	n[pcs] (Sample QTY.)	Pn[pcs] (NG QTY.)
Solder heat resistance 1	260±5°C, 10sec., Solder - bath	JESD22-B106D	30	0
Solder heat resistance 2	350±10°C, 3s., Dipping leads into solder bath at 350±10°C.	JESD22-B106D	30	0
Solderability	245±5°C, 3s., Solder-Bath	JESD22-B102	10	0

Measurement Item & Criteria			
Item	Condition	Criteria	
Forward Voltage (V F)	Par specification	Less than x1.1 of Initial value	
Reverse Current (I R)	Par specification	Less than x2 of Initial value	
Appearance	Visual check	No outstanding change in physical	
Solderability	Visual check	Reflow	Non-wetting unseen
		Solder Bath	

JUDGEMENT
No failure is observed from each test item.

Notes

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