

Product	Transistor	Grade	Standard
Package	SOP8	JEDEC Code	-
Type	SH8KC7	Sample Size	30pcs

### 1. CHARGED DEVICE MODEL (CDM) / デバイス帯電モデル静電破壊試験

Table1 Charged device model ESD test results for each testing standards

各規格におけるデバイス帯電モデル静電破壊試験結果

TR1 and TR2

STANDARD	TEST TYPE	CLASS
JEDEC JS-002-2018	FI-CDM	C3
JEITA ED-4701/302A	FI-CDM (Condition:A)	C3

### 2. HUMAN BODY MODEL (HBM) / 人体モデル静電破壊試験

Table2 Human body model ESD test results for each testing standards

各規格における人体モデル静電破壊試験結果

TR1 and TR2

STANDARD	TEST TYPE	CLASS
JEDEC JS-001-2017	Human body model (Contact mode)	1B
JEITA ED-4701/302A	Human body model (Contact mode)	1B

### 3. NOTICE / 注意事項

This product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Proper handling in the manufacturing process and storage conditions are required to prevent voltage exceeding the product maximum rating to be applied to the products. Electro-static protection are preferred; grounding human body and equipment, isolation from charged objects, ionizer, friction prevention, temperature and humidity control, etc..

本製品は静電気に対して敏感な製品であり、静電気放電等により破壊することがあります。取り扱い時や工程での実装時、保管時において静電気対策を実施のうえ、絶対最大定格以上の過電圧等が印加されないようにご使用ください。特に乾燥した環境では静電気が発生しやすくなるため、十分な静電気対策を実施ください。（人体及び設備のアース、帯電物からの隔離、イオナイザの設置、摩擦防止、温湿度管理、はんだごてのこて先のアース等）

## Notes

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