

Reliability Test Result

1. TEST RESULT	T		I		- 5 3
TEST ITEM	TEST CONDITION		STANDARD	n[pcs] (Sample QTY.)	Pn[pcs] (NG QTY.)
Soldering heat resistance	260±5°C , 30sec. , Reflow method 3times		JESD22-A111	22	0
	260±5°C , 10sec. , Solder∸bath			22	0
	350±10°C , 3sec. , Hand soldering			22	0
Solderability	245±5°C ,3sec. , Reflow method		J-STD-002	22	0
	245±5°C , 3sec. , Solder-bath		JESD22-B102	22	0
Temperature cycle	-55±5°C←→150±5°C 200cycles		JESD22-A104	22	0
High temp. high humidity reverse bia	85±2°C, 85±5%RH, specified bias ,1000hours		JESD22-A101	22	0
Pressure cooker test	121±2°C , 10096RH , 203kPa , 100hours		JESD22-A102	22	0
High temperature reverse bias	Tj max., specified bias , 1000hours		JESD22-A108	22	0
High temperature gate bias	Tj max, VGSS(max), , 1000hours		JESD22-A108	22	0
Ta=25±5°C. \triangle Tj \ge 100°C. ON 120s/OFF 120s per cycle Duration: 15000cycles Wer and Temperature Cycle Ta=-40(+0/-10)°C ~+85(+10/-0)°C. ON 300s/OFF 300s per cycle		·0)°C、	MIL-STD-750 Method 1037	22	0
	Duration: 6000cycles		JESD22 A-105		
Lead strength (lead pull)	Sample body fixed, pulling lead axis direction , $0.5N$, 10 ± 1 sec.		JEITA ED-4701/400 Test Method 401	22	0
2. MEASUREMENT ITEM & CRIT ITEM	ERIA CONDITION		CRITERIA		
Cutoff current : ICBO	Per specification		SIGILITY		
Cutoff current : IEBO	Per specification	According to the electrical characteristics specified by the specification			
DC current gain : hFE	Per specification				
Physical	Visual check	No outstanding change in physical.			
Solderability	Visual check	Reflow Soldering	Immersed surfase, other than the end of pin as cut-surface, must be covered by solder.		
		Solder-bath	More than 95% of the electrode must be covered with solder.		

3. JUDGEMENT

No failure is observed from each test item.

Notes

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